



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp[®]2006 = 60.8

PowerEdge R420 (Intel Xeon E5-2450L v2, 1.70 GHz)

SPECfp_base2006 = 57.7

CPU2006 license: 55

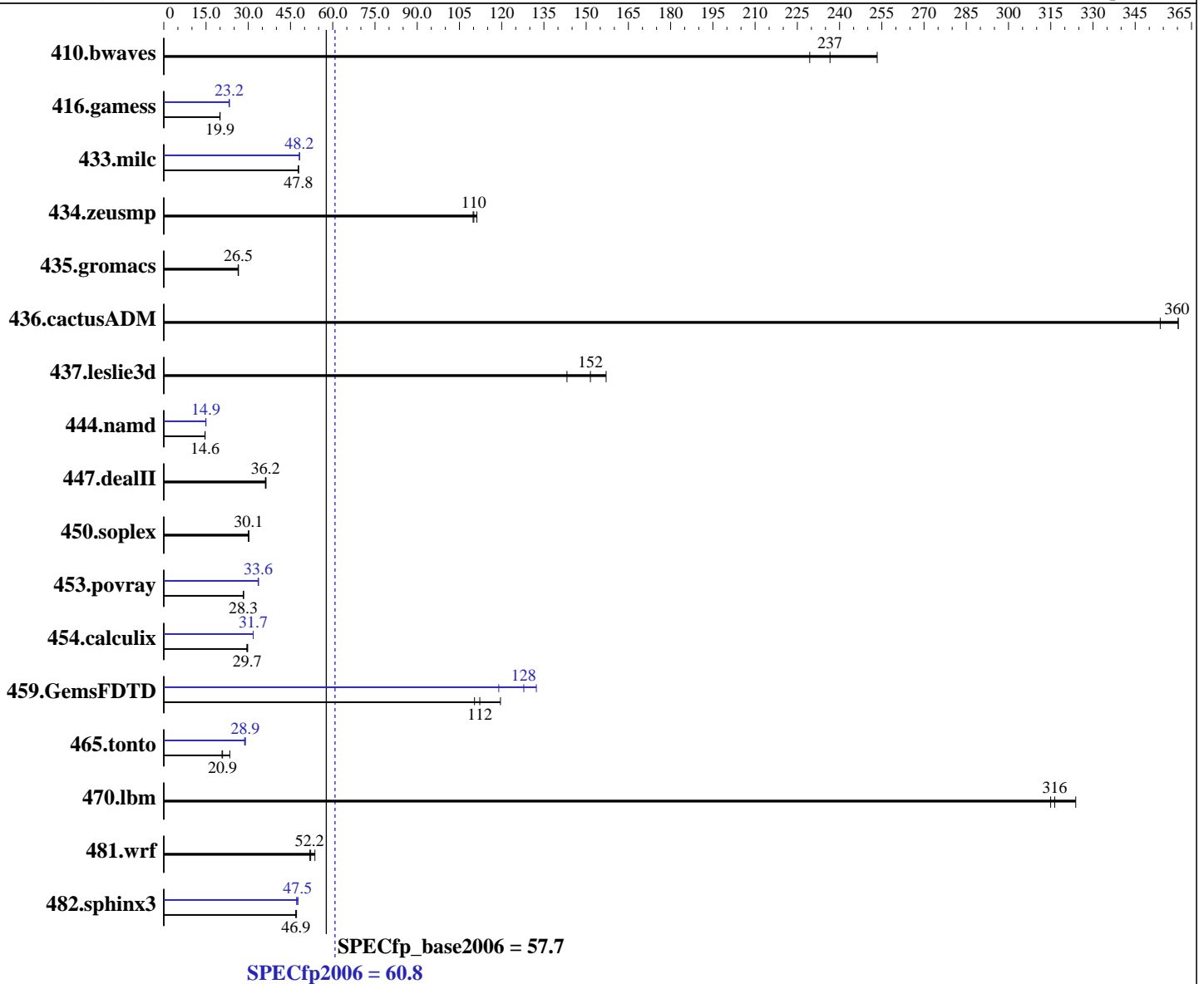
Test date: Oct-2013

Test sponsor: Dell Inc.

Hardware Availability: Jan-2014

Tested by: Dell Inc.

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2450L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz
 CPU MHz: 1700
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 3.0.76-0.11-default
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 60.8

PowerEdge R420 (Intel Xeon E5-2450L v2, 1.70 GHz)

SPECfp_base2006 = 57.7

CPU2006 license: 55

Test date: Oct-2013

Test sponsor: Dell Inc.

Hardware Availability: Jan-2014

Tested by: Dell Inc.

Software Availability: Sep-2013

L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 192 GB (12 x 16 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | <u>57.4</u> | <u>237</u> | 53.6 | 253 | 59.2 | 229 | <u>57.4</u> | <u>237</u> | 53.6 | 253 | 59.2 | 229 |
| 416.gamess | <u>982</u> | <u>19.9</u> | 979 | 20.0 | 984 | 19.9 | 842 | 23.2 | <u>842</u> | <u>23.2</u> | 843 | 23.2 |
| 433.milc | 192 | 47.8 | 192 | 47.8 | <u>192</u> | <u>47.8</u> | 191 | 48.2 | <u>191</u> | <u>48.2</u> | 190 | 48.2 |
| 434.zeusmp | <u>82.6</u> | <u>110</u> | 81.8 | 111 | 82.8 | 110 | <u>82.6</u> | <u>110</u> | 81.8 | 111 | 82.8 | 110 |
| 435.gromacs | 271 | 26.4 | 270 | 26.5 | <u>270</u> | <u>26.5</u> | 271 | 26.4 | 270 | 26.5 | <u>270</u> | <u>26.5</u> |
| 436.cactusADM | <u>33.2</u> | <u>360</u> | 33.8 | 354 | 33.2 | 360 | <u>33.2</u> | <u>360</u> | 33.8 | 354 | 33.2 | 360 |
| 437.leslie3d | <u>62.0</u> | <u>152</u> | 59.8 | 157 | 65.6 | 143 | <u>62.0</u> | <u>152</u> | 59.8 | 157 | 65.6 | 143 |
| 444.namd | 548 | 14.6 | 547 | 14.6 | <u>548</u> | <u>14.6</u> | 537 | 14.9 | 537 | 14.9 | <u>537</u> | <u>14.9</u> |
| 447.dealII | 317 | 36.1 | <u>316</u> | <u>36.2</u> | 315 | 36.3 | 317 | 36.1 | <u>316</u> | <u>36.2</u> | 315 | 36.3 |
| 450.soplex | <u>277</u> | <u>30.1</u> | 277 | 30.1 | 276 | 30.2 | <u>277</u> | <u>30.1</u> | 277 | 30.1 | 276 | 30.2 |
| 453.povray | <u>188</u> | <u>28.3</u> | 188 | 28.3 | 188 | 28.3 | <u>159</u> | <u>33.6</u> | 159 | 33.5 | 158 | 33.6 |
| 454.calculix | 277 | 29.8 | <u>278</u> | <u>29.7</u> | 280 | 29.5 | 260 | 31.7 | <u>260</u> | <u>31.7</u> | 260 | 31.7 |
| 459.GemsFDTD | <u>94.5</u> | <u>112</u> | 96.1 | 110 | 88.7 | 120 | <u>83.0</u> | <u>128</u> | 89.2 | 119 | 80.2 | 132 |
| 465.tonto | 420 | 23.4 | 476 | 20.7 | <u>471</u> | <u>20.9</u> | 340 | 28.9 | <u>340</u> | <u>28.9</u> | 343 | 28.7 |
| 470.lbm | 43.6 | 315 | 42.4 | 324 | <u>43.4</u> | <u>316</u> | 43.6 | 315 | 42.4 | 324 | <u>43.4</u> | <u>316</u> |
| 481.wrf | 208 | 53.7 | <u>214</u> | <u>52.2</u> | 215 | 51.9 | 208 | 53.7 | <u>214</u> | <u>52.2</u> | 215 | 51.9 |
| 482.sphinx3 | 416 | 46.9 | <u>415</u> | <u>46.9</u> | 413 | 47.1 | 413 | 47.1 | 409 | 47.6 | <u>410</u> | <u>47.5</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
 Virtualization Technology disabled
 Execute Disable disabled
 Logical Processor disabled
 System Profile set to Performance
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
 running on linux Fri Oct 18 01:29:10 2013

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 60.8

PowerEdge R420 (Intel Xeon E5-2450L v2, 1.70 GHz)

SPECfp_base2006 = 57.7

CPU2006 license: 55

Test date: Oct-2013

Test sponsor: Dell Inc.

Hardware Availability: Jan-2014

Tested by: Dell Inc.

Software Availability: Sep-2013

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2450L v2 @ 1.70GHz
 2 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 10
  siblings     : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size     : 25600 KB

```

```

From /proc/meminfo
MemTotal:      198410440 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

```

```

uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 17 16:42 last=S

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext2  267G  9.4G  257G   4% /

```

```

Additional information from dmidecode:
BIOS Dell Inc. 2.0.21 09/23/2013
Memory:
12x 00CE00B300CE M393B2G70BH0-YK0 16 GB 1600 MHz

```

(End of data from sysinfo program)



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 60.8

PowerEdge R420 (Intel Xeon E5-2450L v2, 1.70 GHz)

SPECfp_base2006 = 57.7

CPU2006 license: 55

Test date: Oct-2013

Test sponsor: Dell Inc.

Hardware Availability: Jan-2014

Tested by: Dell Inc.

Software Availability: Sep-2013

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "20"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 60.8

PowerEdge R420 (Intel Xeon E5-2450L v2, 1.70 GHz)

SPECfp_base2006 = 57.7

CPU2006 license: 55

Test date: Oct-2013

Test sponsor: Dell Inc.

Hardware Availability: Jan-2014

Tested by: Dell Inc.

Software Availability: Sep-2013

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 60.8

PowerEdge R420 (Intel Xeon E5-2450L v2, 1.70 GHz)

SPECfp_base2006 = 57.7

CPU2006 license: 55

Test date: Oct-2013

Test sponsor: Dell Inc.

Hardware Availability: Jan-2014

Tested by: Dell Inc.

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp2006 = 60.8

PowerEdge R420 (Intel Xeon E5-2450L v2, 1.70 GHz)

SPECfp_base2006 = 57.7

CPU2006 license: 55

Test date: Oct-2013

Test sponsor: Dell Inc.

Hardware Availability: Jan-2014

Tested by: Dell Inc.

Software Availability: Sep-2013

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 21:18:53 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 January 2014.