



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant DL380e Gen8
(2.40 GHz, Intel Xeon E5-2470 v2)

SPECint®2006 = 55.4

SPECint_base2006 = 51.1

CPU2006 license: 3

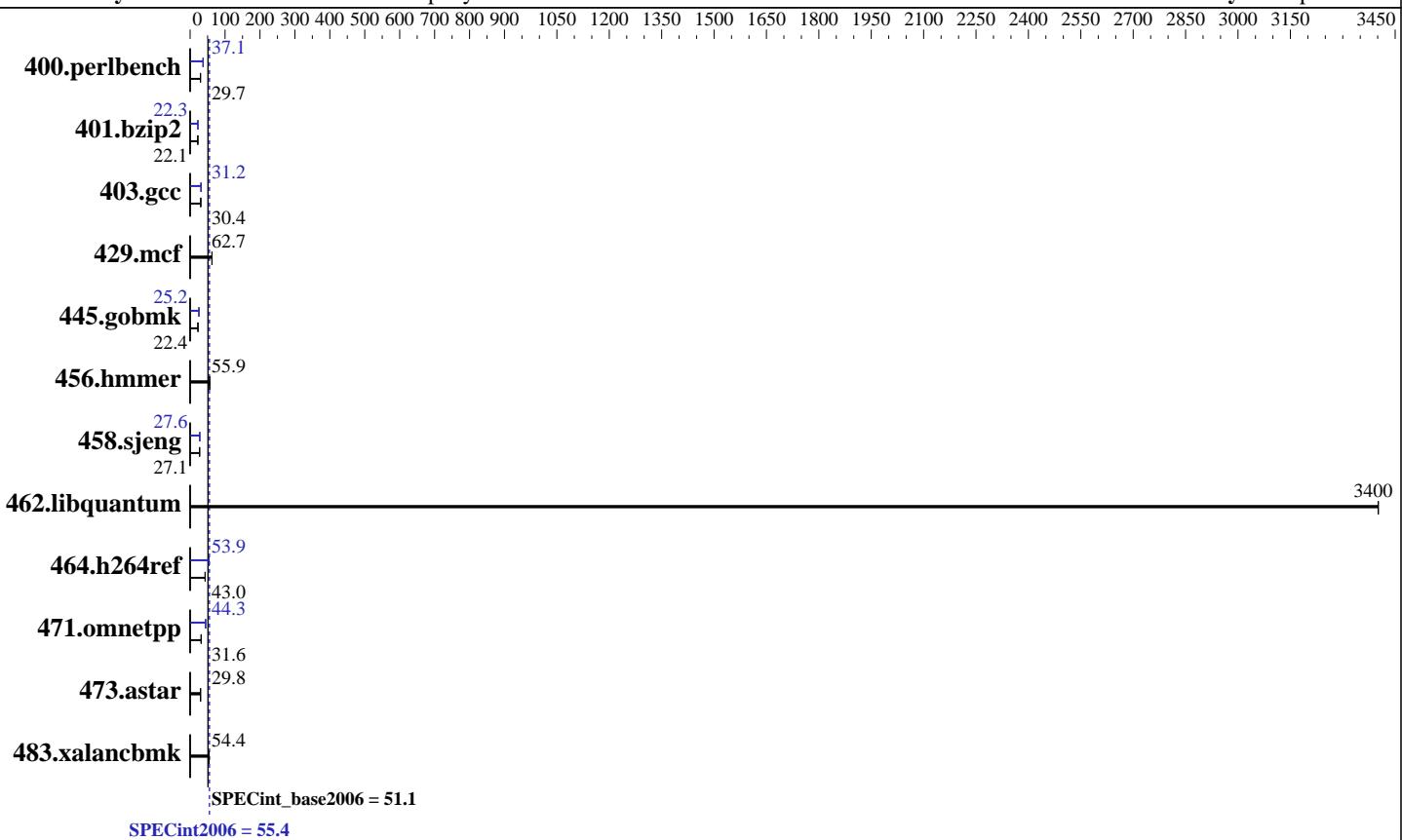
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Dec-2013

Hardware Availability: Jan-2014

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2470 v2
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 300 GB 15 K SAS, RAID 0
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP3
Kernel 3.0.76-0.11-default
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	329	29.7	329	29.7	329	29.7	263	37.1	263	37.1	263	37.1
401.bzip2	437	22.1	437	22.1	437	22.1	434	22.3	433	22.3	433	22.3
403.gcc	265	30.4	263	30.6	264	30.4	258	31.2	258	31.2	258	31.2
429.mcf	146	62.7	145	62.7	146	62.5	146	62.7	145	62.7	146	62.5
445.gobmk	468	22.4	468	22.4	467	22.5	416	25.2	415	25.3	416	25.2
456.hmmer	167	56.0	167	55.8	167	55.9	167	56.0	167	55.8	167	55.9
458.sjeng	446	27.1	446	27.1	446	27.1	438	27.6	438	27.7	438	27.6
462.libquantum	6.09	3400	6.09	3400	6.09	3400	6.09	3400	6.09	3400	6.09	3400
464.h264ref	515	43.0	514	43.1	516	42.9	411	53.9	411	53.8	410	53.9
471.omnetpp	196	32.0	198	31.6	198	31.5	141	44.3	141	44.4	141	44.3
473.astar	235	29.8	235	29.8	235	29.9	235	29.8	235	29.8	235	29.9
483.xalancbmk	127	54.3	127	54.4	126	54.7	127	54.3	127	54.4	126	54.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

Reclaim mode enabled with:

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Disabled unused Linux services through "stop_services.sh" before running.

Platform Notes

BIOS Configuration:

Intel Hyperthreading Options set to Disabled

HP Power Profile set to Maximum Performance

Minimum Processor Idle Power Core State set to C1E state

Minimum Processor Idle Power Package State set to Package C6 (retention) State

Memory Power Savings Mode set to Maximum Performance

Thermal Configuration set to Maximum Cooling

Collaborative Power Control set to Disabled

Dynamic Power Capping Functionality set to Disabled

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x

Sysinfo program /cpu2006/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191

running on DL380e-Gen8-RF0 Fri Dec 13 18:30:02 2013

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2470 v2 @ 2.40GHz
        2 "physical id"s (chips)
        20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 10
    siblings   : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
    cache size : 25600 KB
```

```
From /proc/meminfo
    MemTotal:      98903452 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 11 (x86_64)
    VERSION = 11
    PATCHLEVEL = 3
```

```
uname -a:
Linux DL380e-Gen8-RF0 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
(ccab990) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 13 18:24 last=S
```

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        ext3  273G  28G  232G  11%  /
```

Additional information from dmidecode:

```
BIOS HP P73 11/12/2013
Memory:
12x HP 689911-071 8 GB 1600 MHz
```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

-xsse4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xsse4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

Continued on next page



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Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmr: basepeak = yes

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml>



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