



SPEC[®] CFP2006 Result

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NEC Corporation

SPECfp[®]2006 = **97.0**

Express5800/R120e-1M (Intel Xeon E5-2695 v2)

SPECfp_base2006 = **92.6**

CPU2006 license: 9006

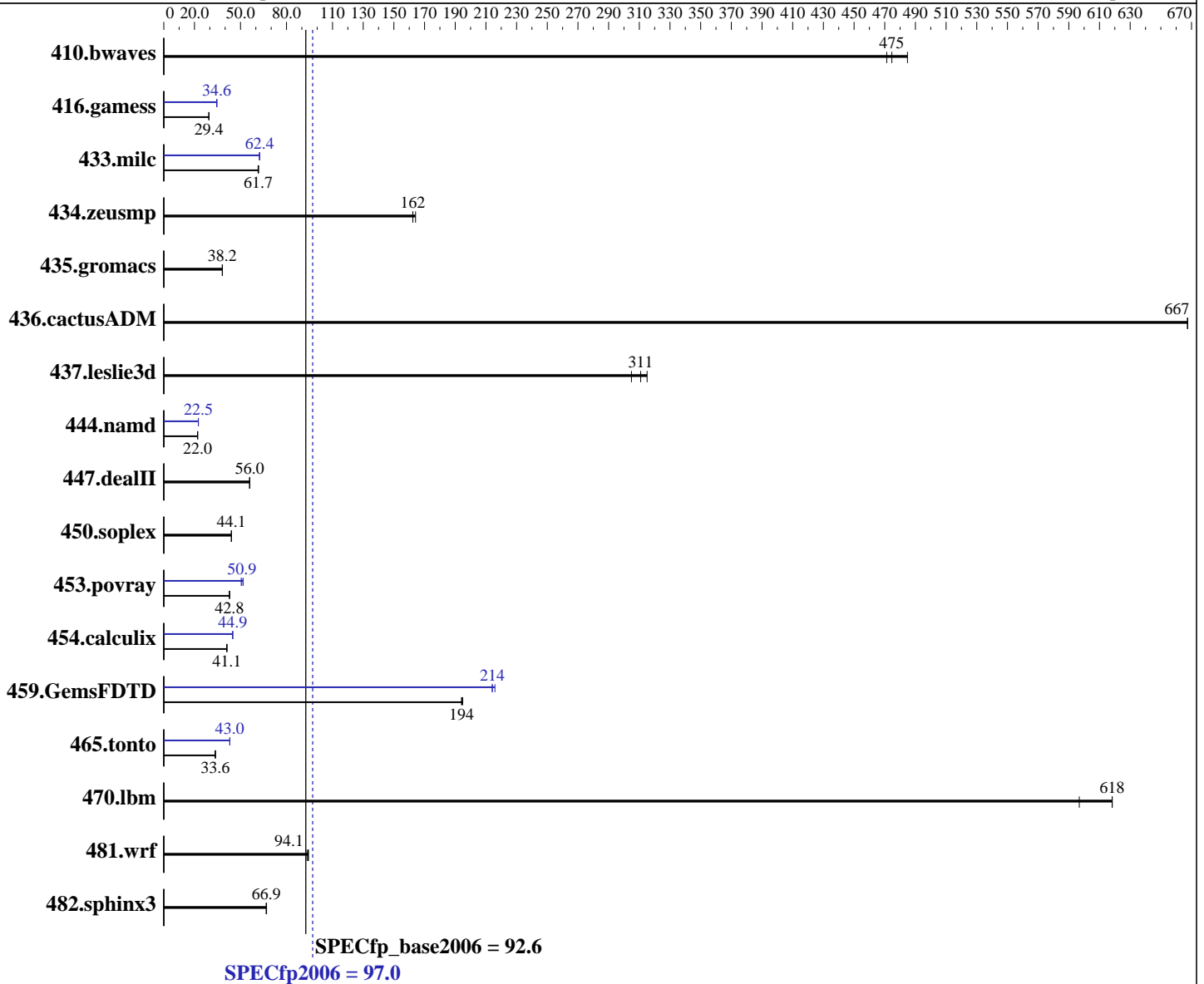
Test date: Oct-2013

Test sponsor: NEC Corporation

Hardware Availability: Sep-2013

Tested by: NEC Corporation

Software Availability: Sep-2013



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Hardware

CPU Name: Intel Xeon E5-2695 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Kernel 2.6.32-358.18.1.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	28.8	471	28.6	475	28.0	485	28.8	471	28.6	475	28.0	485
416.gamess	664	29.5	666	29.4	667	29.4	566	34.6	567	34.5	567	34.6
433.milc	149	61.8	149	61.5	149	61.7	147	62.4	147	62.5	147	62.4
434.zeusmp	55.4	164	56.0	162	56.0	162	55.4	164	56.0	162	56.0	162
435.gromacs	187	38.2	187	38.1	187	38.2	187	38.2	187	38.1	187	38.2
436.cactusADM	17.9	667	17.9	667	17.9	667	17.9	667	17.9	667	17.9	667
437.leslie3d	30.2	311	30.8	305	29.8	315	30.2	311	30.8	305	29.8	315
444.namd	364	22.0	364	22.0	364	22.0	356	22.5	356	22.5	356	22.5
447.dealII	204	56.0	204	56.0	204	55.9	204	56.0	204	56.0	204	55.9
450.soplex	189	44.1	189	44.0	189	44.1	189	44.1	189	44.0	189	44.1
453.povray	124	42.8	124	42.8	124	42.9	105	50.9	105	50.4	103	51.8
454.calculix	200	41.2	201	41.0	200	41.1	184	44.9	184	44.9	184	44.9
459.GemsFDTD	54.6	194	54.6	194	54.4	195	49.5	214	49.1	216	49.5	214
465.tonto	294	33.5	292	33.7	293	33.6	229	43.1	229	43.0	229	43.0
470.lbm	22.2	618	22.2	618	23.0	597	22.2	618	22.2	618	23.0	597
481.wrf	120	93.4	119	94.1	118	94.3	120	93.4	119	94.1	118	94.3
482.sphinx3	292	66.8	291	67.0	292	66.9	292	66.8	291	67.0	292	66.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Energy Performance: Performance

General Notes

Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,compact,1,0"
 LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
 OMP_NUM_THREADS = "24"

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General Notes (Continued)

Added glibc-static-2.12-1.107.el6.x86_64.rpm to enable static linking

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
The Express5800/R120e-1M and the Express5800/R120e-2M models are electronically equivalent. The results have been measured on the Express5800/R120e-1M model.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>



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For other inquiries, please contact webmaster@spec.org.

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