



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 100

PowerEdge R720 (Intel Xeon E5-2680 v2, 2.80 GHz)

SPECfp_base2006 = 95.2

CPU2006 license: 55

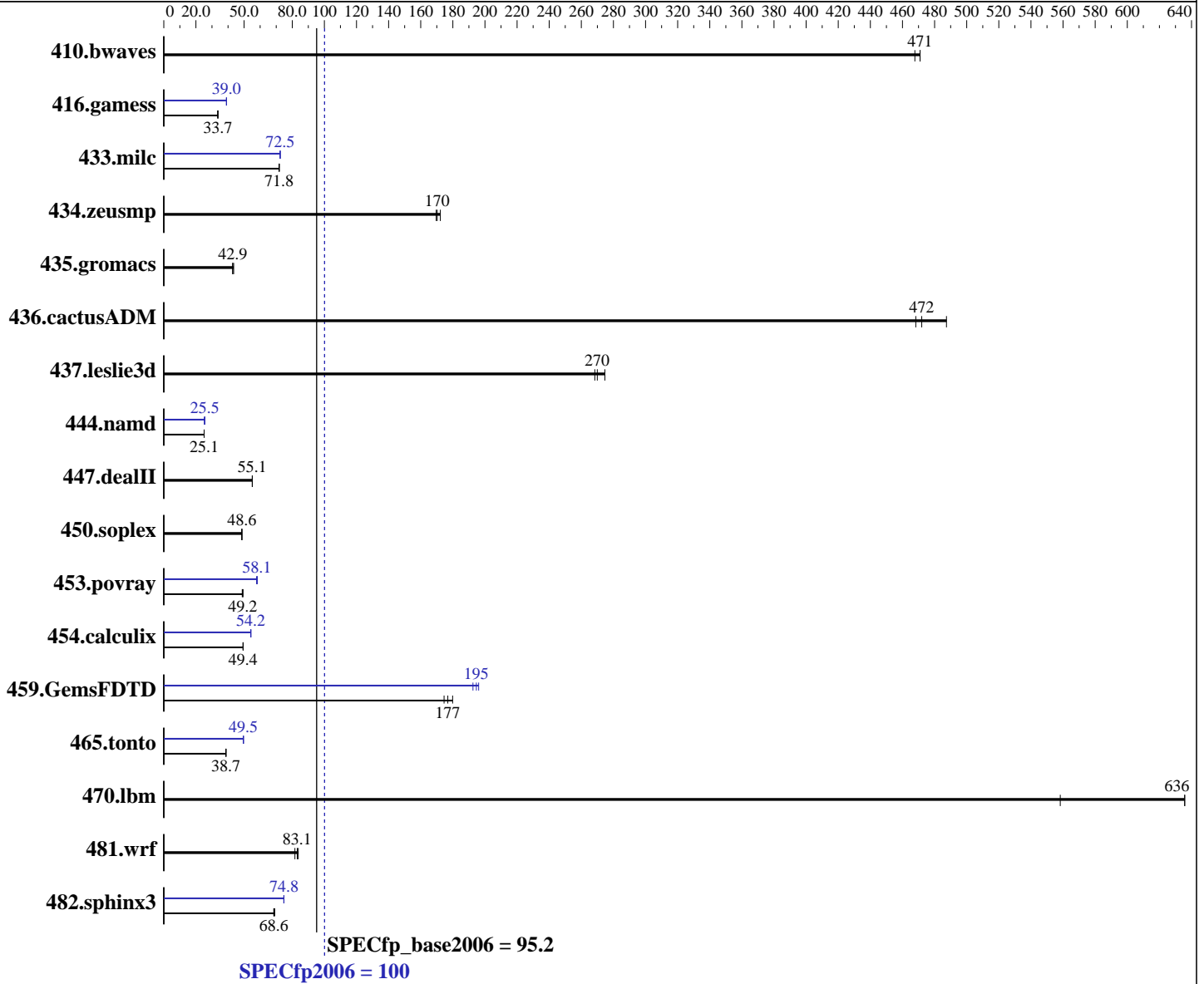
Test date: Aug-2013

Test sponsor: Dell Inc.

Hardware Availability: Sep-2013

Tested by: Dell Inc.

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2680 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP3 (x86_64) 3.0.76-0.11-default
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)

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L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
 Disk Subsystem: 1 x 1 TB 7200 RPM SATA
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	28.9	471	29.1	468	<u>28.9</u>	<u>471</u>	28.9	471	29.1	468	<u>28.9</u>	<u>471</u>
416.gamess	<u>581</u>	<u>33.7</u>	580	33.7	584	33.5	501	39.1	502	39.0	<u>502</u>	<u>39.0</u>
433.milc	128	71.9	128	71.8	<u>128</u>	<u>71.8</u>	127	72.4	<u>127</u>	<u>72.5</u>	127	72.5
434.zeusmp	53.6	170	52.8	172	<u>53.4</u>	<u>170</u>	53.6	170	52.8	172	<u>53.4</u>	<u>170</u>
435.gromacs	164	43.5	167	42.9	<u>166</u>	<u>42.9</u>	164	43.5	167	42.9	<u>166</u>	<u>42.9</u>
436.cactusADM	24.5	487	<u>25.3</u>	<u>472</u>	25.5	468	24.5	487	<u>25.3</u>	<u>472</u>	25.5	468
437.leslie3d	<u>34.8</u>	<u>270</u>	35.0	268	34.2	275	<u>34.8</u>	<u>270</u>	35.0	268	34.2	275
444.namd	319	25.1	<u>319</u>	<u>25.1</u>	319	25.1	<u>314</u>	<u>25.5</u>	318	25.2	314	25.5
447.dealII	208	55.1	208	55.1	<u>208</u>	<u>55.1</u>	208	55.1	208	55.1	<u>208</u>	<u>55.1</u>
450.soplex	171	48.7	<u>172</u>	<u>48.6</u>	172	48.6	171	48.7	<u>172</u>	<u>48.6</u>	172	48.6
453.povray	<u>108</u>	<u>49.2</u>	109	48.8	108	49.3	92.1	57.8	91.4	58.2	<u>91.6</u>	<u>58.1</u>
454.calculix	167	49.3	<u>167</u>	<u>49.4</u>	167	49.4	152	54.2	152	54.2	<u>152</u>	<u>54.2</u>
459.GemsFDTD	59.0	180	<u>60.0</u>	<u>177</u>	60.8	175	<u>54.5</u>	<u>195</u>	54.1	196	55.1	193
465.tonto	255	38.6	<u>254</u>	<u>38.7</u>	254	38.8	198	49.6	199	49.5	<u>199</u>	<u>49.5</u>
470.lbm	21.6	636	24.6	558	<u>21.6</u>	<u>636</u>	21.6	636	24.6	558	<u>21.6</u>	<u>636</u>
481.wrf	137	81.6	133	83.7	<u>134</u>	<u>83.1</u>	137	81.6	133	83.7	<u>134</u>	<u>83.1</u>
482.sphinx3	282	69.1	<u>284</u>	<u>68.6</u>	284	68.5	261	74.7	260	74.8	<u>260</u>	<u>74.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
 Virtualization Technology disabled
 Execute Disable disabled
 Logical Processor disabled
 System Profile set to Performance
 Sysinfo program /root/cpu2006.1.2.ic13/config/sysinfo.rev6818
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
 running on linux Wed Aug 21 00:19:38 2013

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2680 v2 @ 2.80GHz
    2 "physical id"s (chips)
    20 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 10
    siblings  : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 25600 KB

```

```

From /proc/meminfo
MemTotal:      264601764 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

```

```

uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 20 18:58 last=S

```

SPEC is set to: /root/cpu2006.1.2.ic13
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext2  817G  101G  716G  13% /

```

```

Additional information from dmidecode:
BIOS Dell Inc. 2.0.18 08/10/2013
Memory:
8x 00AD00B300AD HMT42GR7MFR4C-RD 16 GB 1866 MHz
8x 00AD04B300AD HMT42GR7AFR4C-RD 16 GB 1866 MHz

```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006.1.2.ic13/libs/32:/root/cpu2006.1.2.ic13/libs/64:/root/cpu2006.1.2.ic13/sh"
OMP_NUM_THREADS = "20"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>



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