



# SPEC® CFP2006 Result

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## IBM Corporation

IBM System x3250 M4  
(Intel Celeron G550, 2.60 GHz)

SPECfp®2006 = **37.5**

SPECfp\_base2006 = **36.8**

CPU2006 license: 11

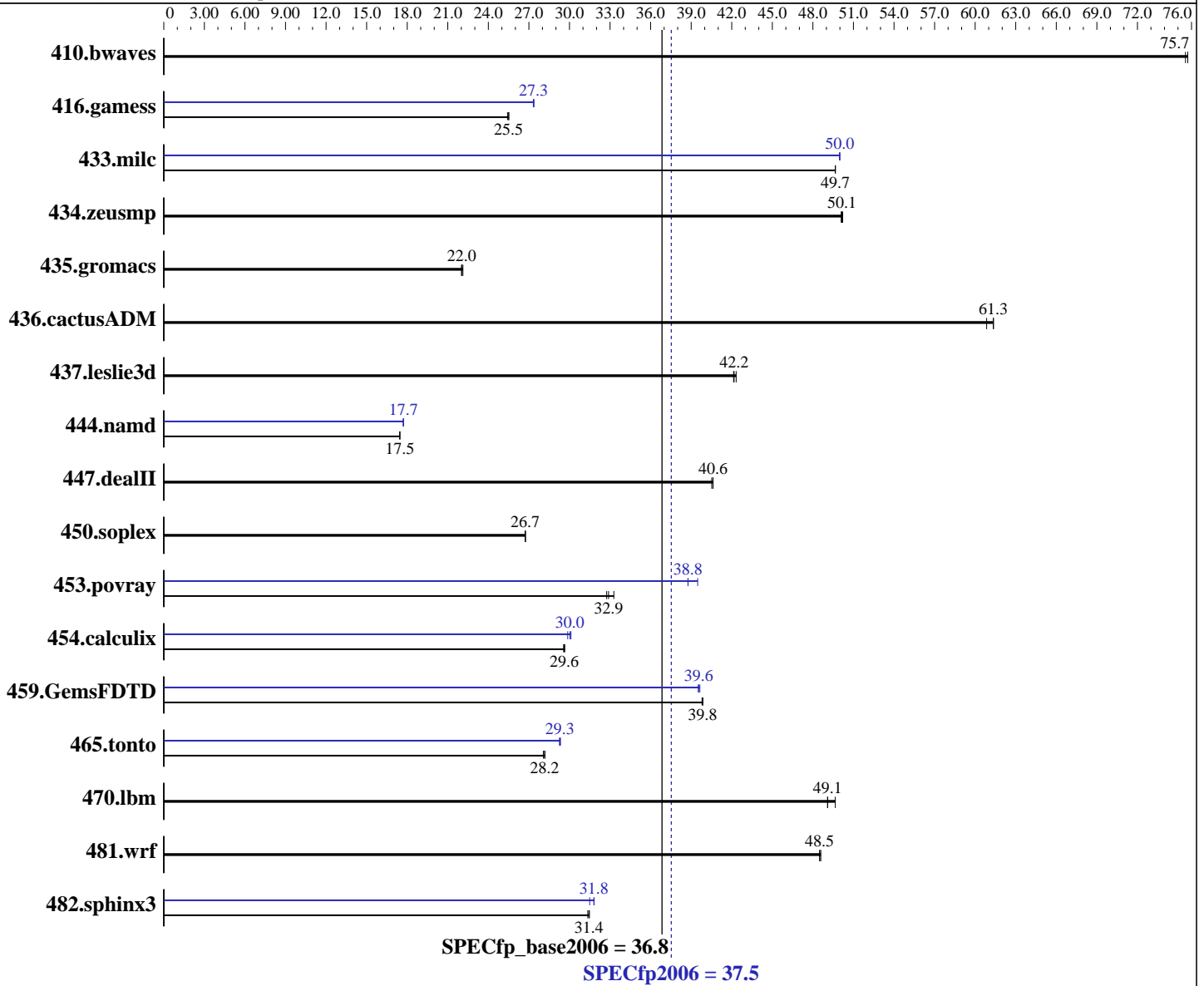
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Sep-2012

Hardware Availability: May-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Celeron G550  
 CPU Characteristics:  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 2 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC, running at 1066 MHz)  
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	179	75.7	<b>179</b>	<b>75.7</b>	180	75.6	179	75.7	<b>179</b>	<b>75.7</b>	180	75.6
416.gamess	767	25.5	770	25.4	<b>768</b>	<b>25.5</b>	<b>716</b>	<b>27.3</b>	717	27.3	715	27.4
433.milc	<b>185</b>	<b>49.7</b>	185	49.7	185	49.7	<b>184</b>	<b>50.0</b>	184	50.0	184	50.0
434.zeusmp	<b>181</b>	<b>50.1</b>	182	50.1	181	50.2	<b>181</b>	<b>50.1</b>	182	50.1	181	50.2
435.gromacs	323	22.1	324	22.0	<b>324</b>	<b>22.0</b>	323	22.1	324	22.0	<b>324</b>	<b>22.0</b>
436.cactusADM	<b>195</b>	<b>61.3</b>	195	61.3	196	60.8	<b>195</b>	<b>61.3</b>	195	61.3	196	60.8
437.leslie3d	222	42.3	223	42.1	<b>223</b>	<b>42.2</b>	222	42.3	223	42.1	<b>223</b>	<b>42.2</b>
444.namd	<b>460</b>	<b>17.5</b>	460	17.5	460	17.5	<b>453</b>	<b>17.7</b>	453	17.7	453	17.7
447.dealII	<b>282</b>	<b>40.6</b>	282	40.6	282	40.5	<b>282</b>	<b>40.6</b>	282	40.6	282	40.5
450.soplex	312	26.7	<b>312</b>	<b>26.7</b>	312	26.7	312	26.7	<b>312</b>	<b>26.7</b>	312	26.7
453.povray	<b>162</b>	<b>32.9</b>	162	32.7	160	33.3	137	38.8	<b>137</b>	<b>38.8</b>	135	39.5
454.calculix	278	29.7	<b>279</b>	<b>29.6</b>	279	29.6	274	30.1	276	29.9	<b>275</b>	<b>30.0</b>
459.GemsFDTD	266	39.9	<b>266</b>	<b>39.8</b>	267	39.8	<b>268</b>	<b>39.6</b>	268	39.5	268	39.6
465.tonto	349	28.2	<b>349</b>	<b>28.2</b>	351	28.1	336	29.3	<b>336</b>	<b>29.3</b>	335	29.3
470.lbm	277	49.7	280	49.1	<b>280</b>	<b>49.1</b>	277	49.7	280	49.1	<b>280</b>	<b>49.1</b>
481.wrf	230	48.5	<b>230</b>	<b>48.5</b>	230	48.6	230	48.5	<b>230</b>	<b>48.5</b>	230	48.6
482.sphinx3	619	31.5	622	31.4	<b>620</b>	<b>31.4</b>	612	31.8	619	31.5	<b>613</b>	<b>31.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Turbo Mode enabled in BIOS

C-State enabled in BIOS

Sysinfo program /root/SPECcpul.2/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3

running on localhost.localdomain Wed Sep 19 04:24:40 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Celeron(R) CPU G550 @ 2.60GHz
 1 "physical id"s (chips)
 2 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 2
  siblings  : 2
  physical 0: cores 0 1
  cache size : 2048 KB
```

```
From /proc/meminfo
MemTotal:      16322724 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13
EST 2011 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Sep 17 11:28
```

```
SPEC is set to: /root/SPECcpul.2
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
                ext4      50G  20G  27G  43% /
```

```
Additional information from dmidecode:
Memory:
 2x Micron 18JSF1G72AZ-1G6D1 8 GB 1067 MHz 2 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/root/SPECcpul.2/libs/32:/root/SPECcpul.2/libs/64"  
OMP\_NUM\_THREADS = "2"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

`433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

`470.lbm: basepeak = yes`

`482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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