



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

IBM System x iDataPlex dx360 M4 (Intel Xeon E5-2648L)

SPECfp<sup>®</sup>2006 = **60.3**

SPECfp\_base2006 = **56.9**

CPU2006 license: 11

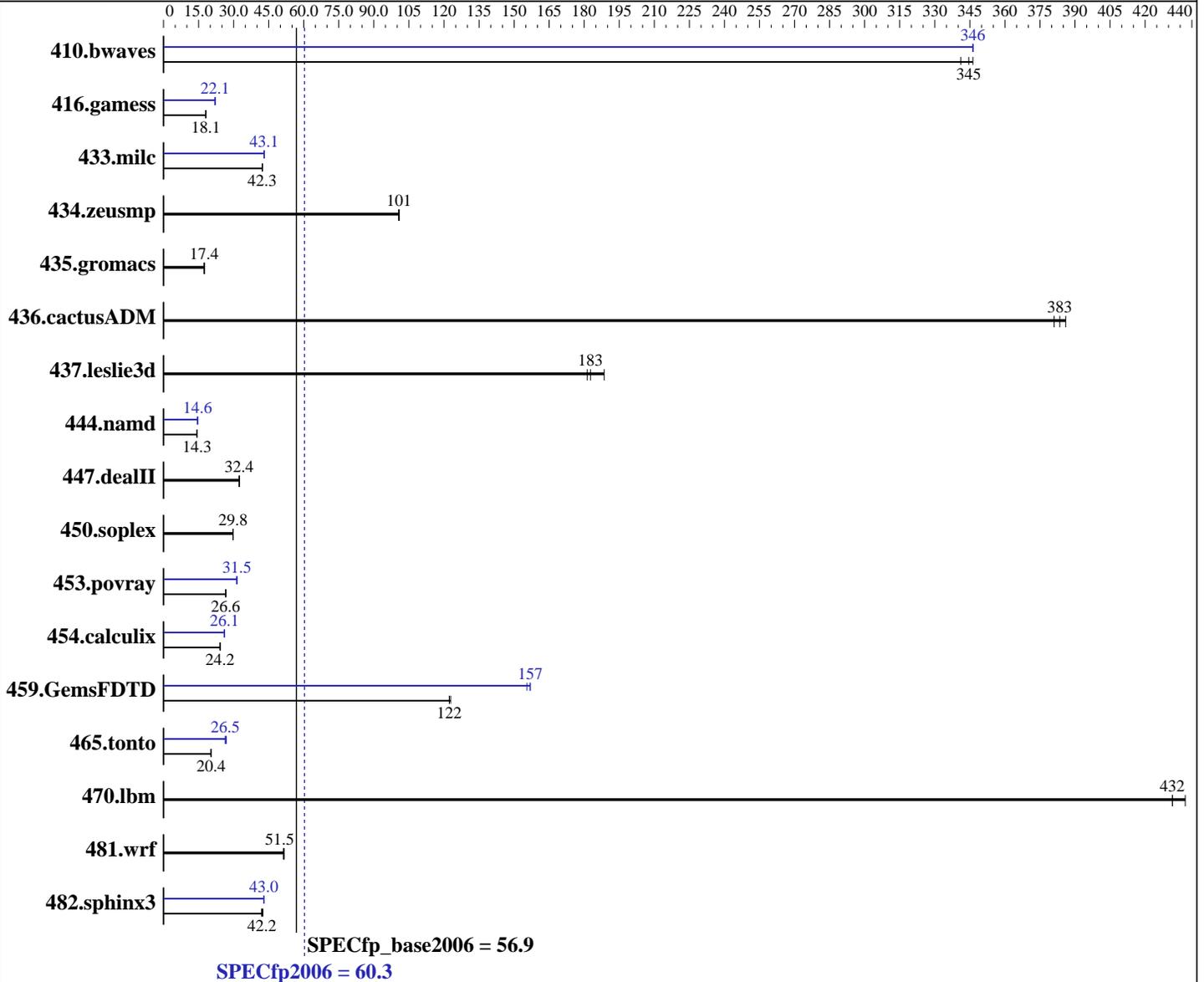
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jul-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



SPECfp\_base2006 = 56.9  
SPECfp2006 = 60.3

### Hardware

CPU Name: Intel Xeon E5-2648L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
Disk Subsystem: 1 x 250 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio								
410.bwaves	<b>39.4</b>	<b>345</b>	39.8	341	39.2	346	39.2	346	<b>39.2</b>	<b>346</b>	39.2	346
416.gamess	<b>1081</b>	<b>18.1</b>	1081	18.1	1086	18.0	<b>887</b>	<b>22.1</b>	886	22.1	888	22.0
433.milc	<b>217</b>	<b>42.3</b>	217	42.3	217	42.3	213	43.1	<b>213</b>	<b>43.1</b>	213	43.1
434.zeusmp	<b>90.3</b>	<b>101</b>	90.3	101	90.5	101	<b>90.3</b>	<b>101</b>	90.3	101	90.5	101
435.gromacs	<b>410</b>	<b>17.4</b>	409	17.4	410	17.4	<b>410</b>	<b>17.4</b>	409	17.4	410	17.4
436.cactusADM	<b>31.2</b>	<b>383</b>	31.0	386	31.4	381	<b>31.2</b>	<b>383</b>	31.0	386	31.4	381
437.leslie3d	<b>51.5</b>	<b>183</b>	49.8	189	51.9	181	<b>51.5</b>	<b>183</b>	49.8	189	51.9	181
444.namd	<b>559</b>	<b>14.3</b>	559	14.3	559	14.3	550	14.6	<b>550</b>	<b>14.6</b>	550	14.6
447.dealII	<b>353</b>	<b>32.4</b>	352	32.5	353	32.4	<b>353</b>	<b>32.4</b>	352	32.5	353	32.4
450.soplex	280	29.8	<b>280</b>	<b>29.8</b>	280	29.7	280	29.8	<b>280</b>	<b>29.8</b>	280	29.7
453.povray	<b>200</b>	<b>26.6</b>	201	26.5	199	26.7	170	31.3	<b>169</b>	<b>31.5</b>	169	31.5
454.calculix	<b>340</b>	<b>24.2</b>	340	24.2	341	24.2	316	26.1	<b>316</b>	<b>26.1</b>	317	26.0
459.GemsFDTD	86.8	122	<b>86.8</b>	<b>122</b>	86.4	123	68.2	156	67.6	157	<b>67.6</b>	<b>157</b>
465.tonto	<b>483</b>	<b>20.4</b>	483	20.4	484	20.3	366	26.9	<b>372</b>	<b>26.5</b>	373	26.4
470.lbm	31.4	437	31.8	432	<b>31.8</b>	<b>432</b>	31.4	437	31.8	432	<b>31.8</b>	<b>432</b>
481.wrf	216	51.6	<b>217</b>	<b>51.5</b>	218	51.3	216	51.6	<b>217</b>	<b>51.5</b>	218	51.3
482.sphinx3	465	42.0	458	42.5	<b>462</b>	<b>42.2</b>	<b>453</b>	<b>43.0</b>	453	43.0	455	42.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS Settings:  
Operating Mode set to Maximum Performance  
Sysinfo program /root/SPECcpu-v1.2/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on tianden Sun Jul 15 09:14:49 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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### Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2648L 0 @ 1.80GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      132093704 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux tianden 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
x86_64 x86_64 GNU/Linux

```

run-level 3 Jul 13 11:26

```

SPEC is set to: /root/SPECcpu-v1.2
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_tianden-lv_root
                ext4      145G  71G   68G  52% /

```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/root/SPECcpu-v1.2/libs/32:/root/SPECcpu-v1.2/libs/64"
OMP_NUM_THREADS = "16"

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:

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## General Notes (Continued)

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.lelie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

C++ benchmarks:

444.namd: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32`

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## Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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