



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®2006 = 77.1

PowerEdge M820 (Intel Xeon E5-4617, 2.90 GHz)

SPECfp_base2006 = 72.4

CPU2006 license: 55

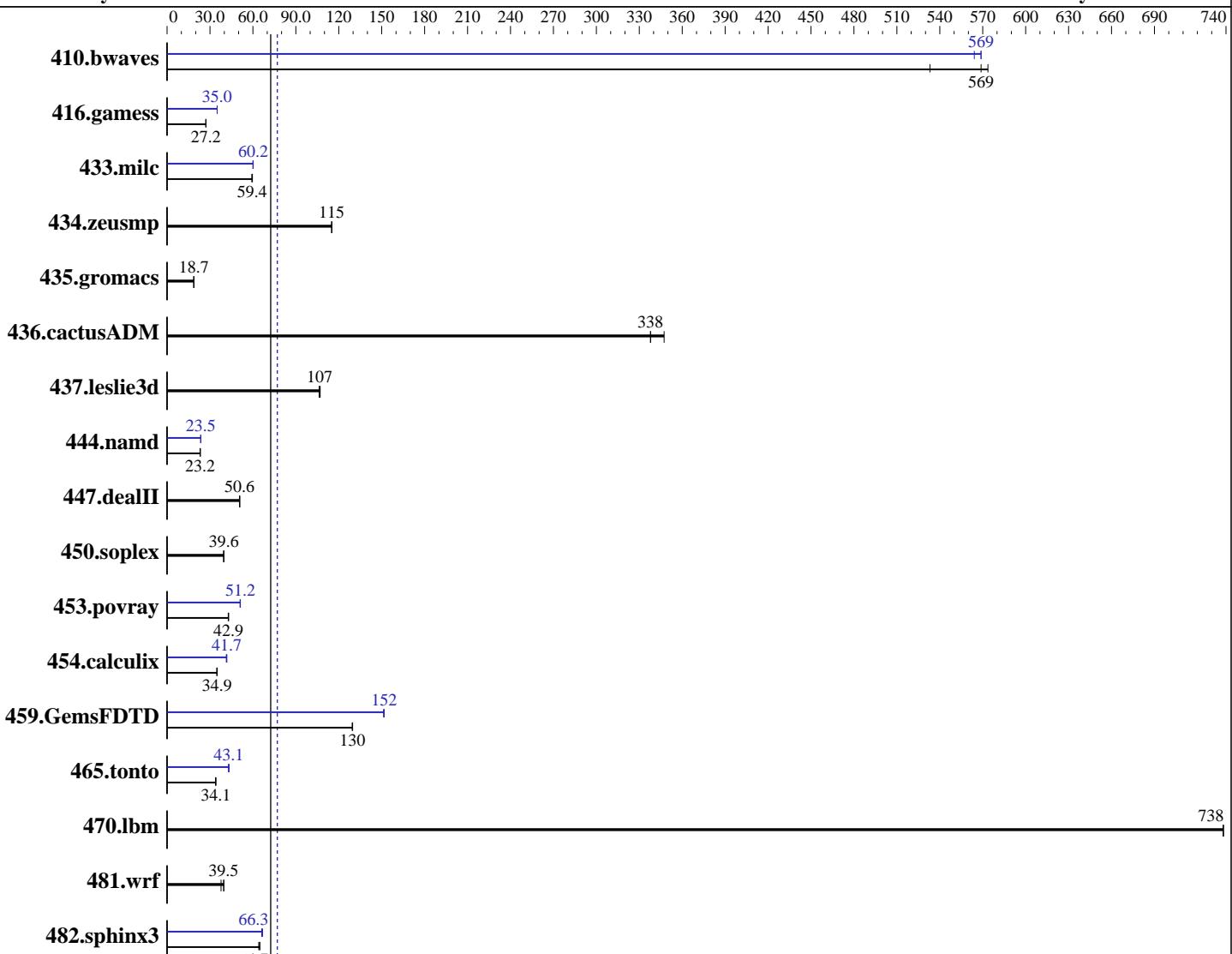
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Jun-2012

Hardware Availability: Aug-2012

Software Availability: Feb-2012



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Hardware

CPU Name: Intel Xeon E5-4617
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 2900
 FPU: Integrated
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64)
 3.0.13-0.27-default
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE
 for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran
 Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

Continued on next page

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L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)
 Disk Subsystem: 1 x 900 GB 10000 RPM SAS
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 25.5 | 533 | <u>23.9</u> | <u>569</u> | 23.7 | 574 | <u>23.9</u> | <u>569</u> | 24.1 | 564 | <u>23.9</u> | <u>569</u> |
| 416.gamess | 718 | 27.3 | <u>720</u> | <u>27.2</u> | 724 | 27.0 | <u>559</u> | <u>35.0</u> | <u>559</u> | <u>35.0</u> | 560 | 35.0 |
| 433.milc | 154 | 59.6 | 155 | 59.3 | <u>155</u> | <u>59.4</u> | 152 | 60.2 | <u>153</u> | <u>60.2</u> | 153 | 60.0 |
| 434.zeusmp | 79.1 | 115 | <u>79.1</u> | <u>115</u> | 79.1 | 115 | 79.1 | 115 | <u>79.1</u> | <u>115</u> | 79.1 | 115 |
| 435.gromacs | <u>381</u> | <u>18.7</u> | 383 | 18.6 | 380 | 18.8 | <u>381</u> | <u>18.7</u> | 383 | 18.6 | 380 | 18.8 |
| 436.cactusADM | 34.4 | 347 | 35.4 | 338 | <u>35.4</u> | <u>338</u> | 34.4 | 347 | 35.4 | 338 | <u>35.4</u> | <u>338</u> |
| 437.leslie3d | 88.5 | 106 | 87.9 | 107 | <u>88.1</u> | <u>107</u> | 88.5 | 106 | 87.9 | 107 | <u>88.1</u> | <u>107</u> |
| 444.namd | <u>346</u> | <u>23.2</u> | 346 | 23.2 | 346 | 23.2 | 340 | 23.6 | 341 | 23.5 | <u>341</u> | <u>23.5</u> |
| 447.dealII | 225 | 50.9 | 226 | 50.6 | <u>226</u> | <u>50.6</u> | 225 | 50.9 | 226 | 50.6 | <u>226</u> | <u>50.6</u> |
| 450.soplex | 211 | 39.6 | <u>210</u> | <u>39.6</u> | 210 | 39.7 | 211 | 39.6 | <u>210</u> | <u>39.6</u> | 210 | 39.7 |
| 453.povray | 124 | 42.7 | <u>124</u> | <u>42.9</u> | 124 | 43.0 | 104 | 51.0 | 104 | 51.2 | <u>104</u> | <u>51.2</u> |
| 454.calculix | 237 | 34.7 | <u>236</u> | <u>34.9</u> | 235 | 35.1 | 198 | 41.7 | <u>198</u> | <u>41.7</u> | 199 | 41.4 |
| 459.GemsFDTD | 81.8 | 130 | 82.0 | 129 | <u>81.8</u> | <u>130</u> | <u>69.9</u> | <u>152</u> | 69.9 | 152 | 70.1 | 151 |
| 465.tonto | 289 | 34.1 | 291 | 33.8 | <u>289</u> | <u>34.1</u> | 229 | 42.9 | 227 | 43.3 | <u>228</u> | <u>43.1</u> |
| 470.lbm | 18.6 | 738 | 18.6 | 738 | <u>18.6</u> | <u>738</u> | 18.6 | 738 | 18.6 | 738 | <u>18.6</u> | <u>738</u> |
| 481.wrf | 296 | 37.7 | <u>283</u> | <u>39.5</u> | 281 | 39.7 | 296 | 37.7 | <u>283</u> | <u>39.5</u> | 281 | 39.7 |
| 482.sphinx3 | <u>301</u> | <u>64.7</u> | 304 | 64.1 | 300 | 64.9 | <u>294</u> | <u>66.3</u> | 292 | 66.8 | 295 | 66.1 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

CPU Power Management set to Maximum Performance

Memory Frequency set to Maximum Performance

Turbo Boost set to Enabled

C States/C1E set to Enabled

Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 #\\$ 6f2ebdff5032aaa42e583f96b07f99d3

running on linux-b611 Fri Jun 1 18:46:04 2012

This section contains SUT (System Under Test) info as seen by

Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-4617 0 @ 2.90GHz
        4 "physical id"s (chips)
        24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings   : 6
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
    physical 2: cores 0 1 2 3 4 5
    physical 3: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      264468688 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 11 (x86_64)
    VERSION = 11
    PATCHLEVEL = 2
```

```
uname -a:
Linux linux-b611 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
(d73692b) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 1 12:40 last=S
```

```
SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sdal      ext3  911G  97G  769G  12%  /
```

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/root/CPU2006-1.2/lib32:/root/CPU2006-1.2/lib64"

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General Notes (Continued)

OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop_caches

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias  
-parallel
```

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Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
             -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
             -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

```
435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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