



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = 77.9

Huawei BH622 V2 (Intel Xeon E5-2650)

SPECfp_base2006 = 73.9

CPU2006 license: 3175

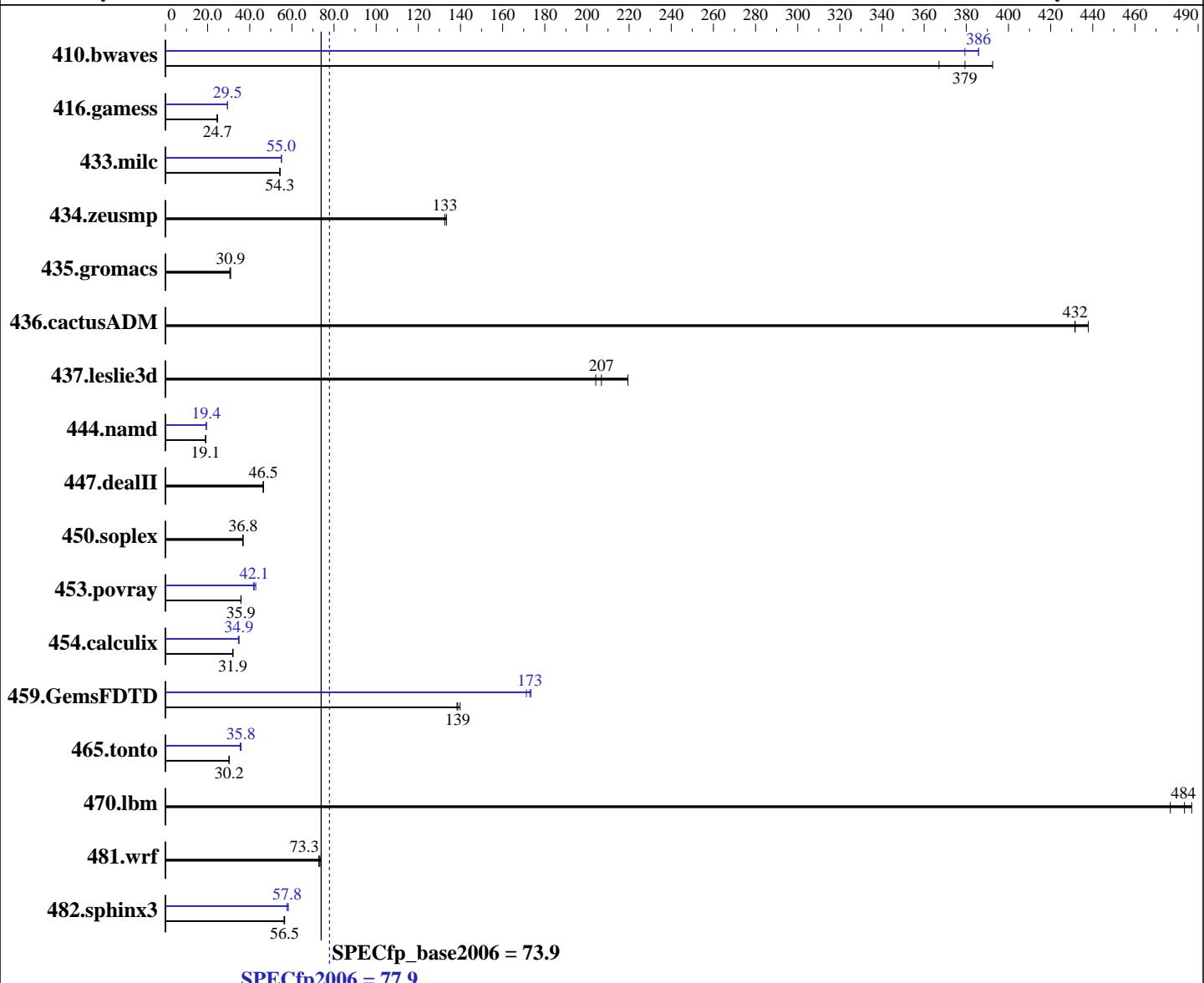
Test date: Jun-2012

Test sponsor: Huawei

Hardware Availability: Mar-2012

Tested by: Huawei

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2650
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
Compiler: 2.6.32-220.el6.x86_64
C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	37.0	367	34.6	392	<u>35.8</u>	<u>379</u>	35.8	379	35.2	386	<u>35.2</u>	<u>386</u>
416.gamess	793	24.7	800	24.5	<u>793</u>	<u>24.7</u>	<u>664</u>	<u>29.5</u>	666	29.4	664	29.5
433.milc	169	54.4	169	54.3	<u>169</u>	<u>54.3</u>	<u>167</u>	<u>55.0</u>	167	55.0	167	55.0
434.zeusmp	<u>68.6</u>	<u>133</u>	68.2	133	68.6	133	68.2	133	68.6	133		
435.gromacs	231	30.9	<u>231</u>	<u>30.9</u>	233	30.6	231	30.9	<u>231</u>	<u>30.9</u>	233	30.6
436.cactusADM	<u>27.7</u>	<u>432</u>	27.3	438	27.7	432	<u>27.7</u>	<u>432</u>	27.3	438	27.7	432
437.leslie3d	42.8	219	46.0	204	<u>45.4</u>	<u>207</u>	42.8	219	46.0	204	<u>45.4</u>	<u>207</u>
444.namd	<u>421</u>	<u>19.1</u>	420	19.1	421	19.1	413	19.4	<u>413</u>	<u>19.4</u>	413	19.4
447.dealII	247	46.3	246	46.6	<u>246</u>	<u>46.5</u>	247	46.3	246	46.6	<u>246</u>	<u>46.5</u>
450.soplex	<u>226</u>	<u>36.8</u>	226	36.9	227	36.7	<u>226</u>	<u>36.8</u>	226	36.9	227	36.7
453.povray	149	35.8	148	35.9	<u>148</u>	<u>35.9</u>	<u>126</u>	<u>42.1</u>	127	41.9	124	42.9
454.calculix	259	31.9	257	32.1	<u>258</u>	<u>31.9</u>	236	34.9	<u>236</u>	<u>34.9</u>	238	34.6
459.GemsFDTD	<u>76.5</u>	<u>139</u>	75.9	140	76.7	138	<u>61.2</u>	<u>173</u>	62.0	171	<u>61.4</u>	<u>173</u>
465.tonto	<u>325</u>	<u>30.2</u>	326	30.2	325	30.3	<u>275</u>	<u>35.8</u>	274	35.9	277	35.5
470.lbm	<u>28.4</u>	<u>484</u>	28.8	477	28.2	487	<u>28.4</u>	<u>484</u>	28.8	477	28.2	487
481.wrf	151	73.8	<u>152</u>	<u>73.3</u>	153	72.8	<u>151</u>	<u>73.8</u>	<u>152</u>	<u>73.3</u>	153	72.8
482.sphinx3	347	56.2	<u>345</u>	<u>56.5</u>	345	56.6	<u>338</u>	<u>57.7</u>	334	58.3	<u>337</u>	<u>57.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Select only test related files when installing the operating system

Platform Notes

BIOS configuration:

Intel Hyper-Threading set to Disabled

Set Power Efficiency Mode to Performance

Sysinfo program /spec/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date::: 2011-10-11 ## 6f2ebdff5032aaa42e583f96b07f99d3

running on RH62-rebuild Thu Jun 21 16:19:11 2012

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2650 0 @ 2.00GHz
        2 "physical id"s (chips)
        16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 8
    siblings   : 8
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
    cache size : 20480 kB
```

```
From /proc/meminfo
MemTotal:      132124032 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux RH62-rebuild 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 21 09:16

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda1       ext4  289G  100G  175G  37%  /
```

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,0,1"

LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20120703.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20120703.xml>

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For other inquiries, please contact webmaster@spec.org.

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