SPEC® CINT2006 Result
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Fujitsu

PRIMERGY BX920 S3, Intel Xeon E5-2403, 1.80 GHz

$SPECint®2006 =$ 28.0

$SPECint\_base2006 =$ 26.6

CPU2006 license: 19	**Test date:** May-2012
Test sponsor: Fujitsu	**Hardware Availability:** May-2012
Tested by: Fujitsu	**Software Availability:** Feb-2012

Benchmark results bar chart (scale 0 to 1100):

- **400.perlbench** — 19.1 / 16.1
- **401.bzip2** — 12.3 / 12.0
- **403.gcc** — 17.8 / 17.6
- **429.mcf** — 37.5
- **445.gobmk** — 13.7 / 13.4
- **456.hmmer** — 28.3 / 27.6
- **458.sjeng** — 15.1 / 15.1
- **462.libquantum** — 1100 / 1070
- **464.h264ref** — 28.6 / 26.0
- **471.omnetpp** — 17.8 / 15.0
- **473.astar** — 15.8
- **483.xalancbmk** — 28.9 / 26.9

SPECint_base2006 = 26.6

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Hardware

CPU Name:	Intel Xeon E5-2403
CPU Characteristics:	
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	10 MB I+D on chip per chip
Other Cache:	None
Memory:	96 GB (12 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1067 MHz and CL7)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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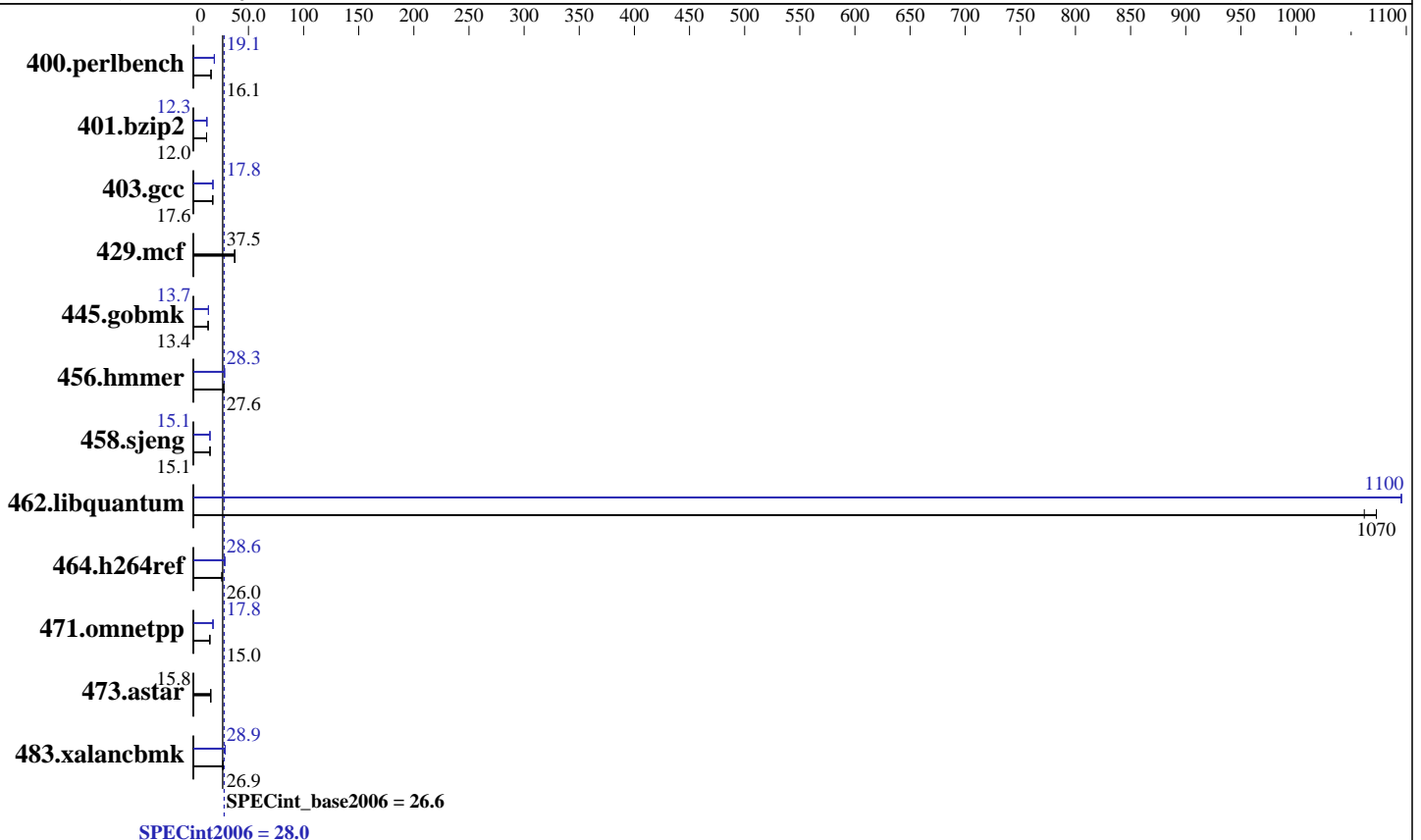
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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	607	16.1	608	16.1	**608**	**16.1**	513	19.0	**513**	**19.1**	512	19.1
401.bzip2	803	12.0	**802**	**12.0**	802	12.0	787	12.3	**786**	**12.3**	785	12.3
403.gcc	**458**	**17.6**	458	17.6	457	17.6	452	17.8	453	17.8	**452**	**17.8**
429.mcf	243	37.5	242	37.7	**243**	**37.5**	243	37.5	242	37.7	**243**	**37.5**
445.gobmk	**782**	**13.4**	782	13.4	781	13.4	**765**	**13.7**	765	13.7	765	13.7
456.hmmer	338	27.6	**338**	**27.6**	339	27.6	**330**	**28.3**	330	28.3	330	28.3
458.sjeng	**799**	**15.1**	799	15.1	799	15.1	**802**	**15.1**	802	15.1	803	15.1
462.libquantum	**19.3**	**1070**	19.5	1060	19.3	1070	**18.9**	**1100**	18.9	1100	18.9	1100
464.h264ref	**850**	**26.0**	857	25.8	848	26.1	**774**	**28.6**	782	28.3	773	28.6
471.omnetpp	**418**	**15.0**	419	14.9	416	15.0	348	18.0	352	17.8	**351**	**17.8**
473.astar	**444**	**15.8**	447	15.7	443	15.8	**444**	**15.8**	447	15.7	443	15.8
483.xalancbmk	255	27.0	**256**	**26.9**	258	26.7	239	28.9	**239**	**28.9**	239	28.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:

Frequency Floor Override = Enable
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
OMP_NUM_THREADS = "8"

 Binaries compiled on a system with 1x E3-1270v2 CPU + 32 GB
 memory using RHEL6.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m64
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m64

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
           401.bzip2: -DSPEC_CPU_LP64
             403.gcc: -DSPEC_CPU_LP64
             429.mcf: -DSPEC_CPU_LP64
          445.gobmk: -DSPEC_CPU_LP64
          456.hmmer: -DSPEC_CPU_LP64
            458.sjeng: -DSPEC_CPU_LP64
      462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
         464.h264ref: -DSPEC_CPU_LP64
         471.omnetpp: -DSPEC_CPU_LP64
            473.astar: -DSPEC_CPU_LP64
      483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -Wl,-z,muldefs -L/opt/SmartHeap/lib64 -lsmartheap64

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m64

    400.perlbench: icc -m32

    445.gobmk: icc -m32

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Peak Compiler Invocation (Continued)

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: basepeak = yes

445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2)
`-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
`-ansi-alias`

458.sjeng: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-unroll4`

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Peak Optimization Flags (Continued)

462.libquantum: `-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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