



SPEC[®] CFP2006 Result

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Fujitsu

PRIMERGY TX120 S3p, Intel Xeon E3-1265L v2, 2.50 GHz

SPECfp[®]2006 = 65.8

SPECfp_base2006 = 63.6

CPU2006 license: 19

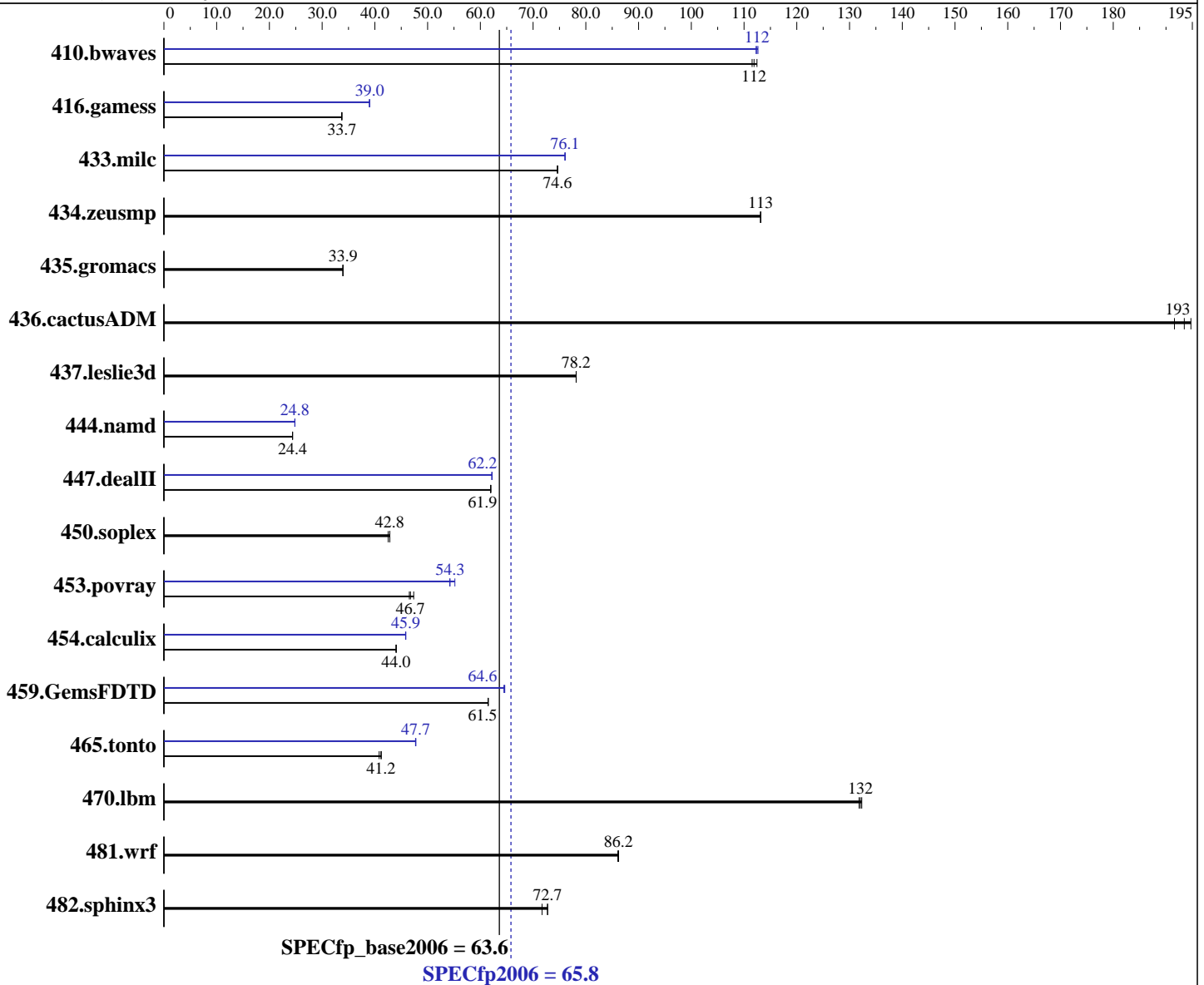
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2012

Hardware Availability: May-2012

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E3-1265L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.5 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.293 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	122	112	121	112	<u>121</u>	<u>112</u>	121	112	<u>121</u>	<u>112</u>	121	113
416.gamess	<u>580</u>	<u>33.7</u>	581	33.7	580	33.8	502	39.0	503	39.0	<u>502</u>	<u>39.0</u>
433.milc	123	74.6	<u>123</u>	<u>74.6</u>	123	74.7	121	76.1	<u>121</u>	<u>76.1</u>	121	76.0
434.zeusmp	80.4	113	<u>80.4</u>	<u>113</u>	80.4	113	80.4	113	<u>80.4</u>	<u>113</u>	80.4	113
435.gromacs	<u>210</u>	<u>33.9</u>	210	33.9	210	33.9	<u>210</u>	<u>33.9</u>	210	33.9	210	33.9
436.cactusADM	61.4	195	<u>61.8</u>	<u>193</u>	62.4	192	61.4	195	<u>61.8</u>	<u>193</u>	62.4	192
437.leslie3d	120	78.2	120	78.2	<u>120</u>	<u>78.2</u>	120	78.2	120	78.2	<u>120</u>	<u>78.2</u>
444.namd	<u>329</u>	<u>24.4</u>	329	24.4	329	24.4	323	24.8	<u>323</u>	<u>24.8</u>	323	24.8
447.dealII	185	61.9	184	62.0	<u>185</u>	<u>61.9</u>	184	62.2	<u>184</u>	<u>62.2</u>	184	62.2
450.soplex	196	42.5	195	42.8	<u>195</u>	<u>42.8</u>	196	42.5	195	42.8	<u>195</u>	<u>42.8</u>
453.povray	114	46.5	112	47.4	<u>114</u>	<u>46.7</u>	96.4	55.2	<u>98.0</u>	<u>54.3</u>	98.2	54.2
454.calculix	187	44.1	187	44.0	<u>187</u>	<u>44.0</u>	<u>180</u>	<u>45.9</u>	180	45.9	180	45.8
459.GemsFDTD	<u>173</u>	<u>61.5</u>	172	61.5	173	61.5	<u>164</u>	<u>64.6</u>	164	64.6	165	64.5
465.tonto	239	41.2	<u>239</u>	<u>41.2</u>	241	40.8	206	47.7	206	47.8	<u>206</u>	<u>47.7</u>
470.lbm	104	132	104	132	<u>104</u>	<u>132</u>	104	132	104	132	<u>104</u>	<u>132</u>
481.wrf	130	86.1	130	86.2	<u>130</u>	<u>86.2</u>	130	86.1	130	86.2	<u>130</u>	<u>86.2</u>
482.sphinx3	<u>268</u>	<u>72.7</u>	272	71.7	268	72.8	<u>268</u>	<u>72.7</u>	272	71.7	268	72.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Intel HT Technology = Disable

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
OMP_NUM_THREADS = "4"

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General Notes (Continued)

Binaries compiled on a system with 1x E3-1270V2 CPU + 32 GB memory using RHEL6.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
This result was measured on the PRIMERGY TX140 S1p. The PRIMERGY TX140 S1p and the PRIMERGY TX120 S3p are electronically equivalent.
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-ansi-alias

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 06:24:53 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 June 2012.