



# SPEC® CFP2006 Result

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## HITACHI

SPECfp®2006 = **66.2**

BladeSymphony BS520H (Intel Xeon E5-2620)

SPECfp\_base2006 = **62.8**

CPU2006 license: 35

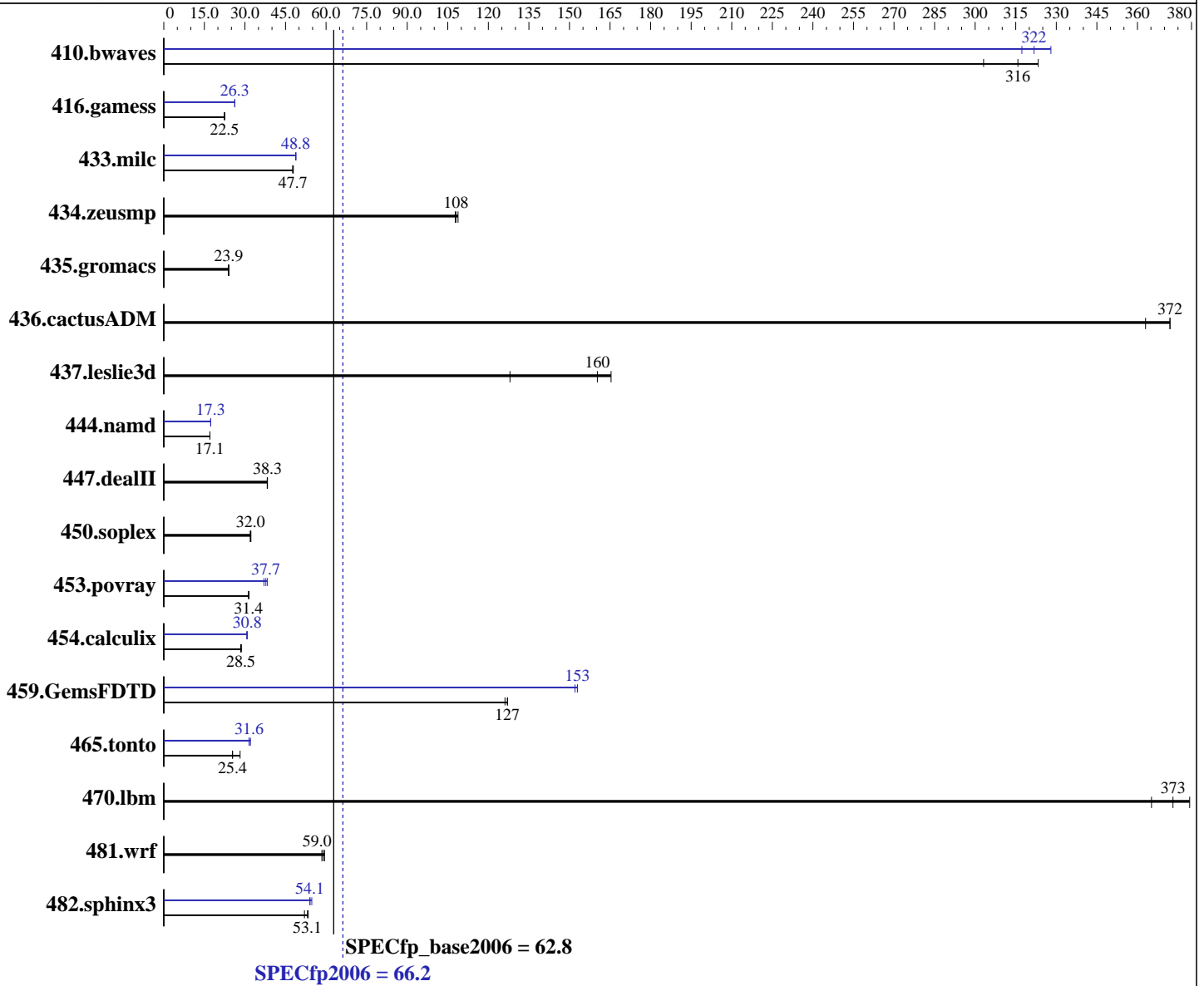
Test sponsor: HITACHI

Tested by: HITACHI

Test date: May-2012

Hardware Availability: Apr-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2620  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2, Kernel 2.6.32-220.4.2.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)

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L3 Cache: 15 MB I+D on chip per chip  
Other Cache: None  
Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz)  
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	44.8	303	<b>43.0</b>	<b>316</b>	42.0	323	41.4	328	<b>42.2</b>	<b>322</b>	42.8	317
416.gamess	873	22.4	871	22.5	<b>871</b>	<b>22.5</b>	<b>746</b>	<b>26.3</b>	745	26.3	746	26.2
433.milc	<b>192</b>	<b>47.7</b>	193	47.7	192	47.9	188	48.8	<b>188</b>	<b>48.8</b>	188	48.8
434.zeusmp	84.4	108	<b>84.2</b>	<b>108</b>	83.6	109	84.4	108	<b>84.2</b>	<b>108</b>	83.6	109
435.gromacs	<b>298</b>	<b>23.9</b>	299	23.9	296	24.1	<b>298</b>	<b>23.9</b>	299	23.9	296	24.1
436.cactusADM	32.9	363	<b>32.1</b>	<b>372</b>	32.1	372	32.9	363	<b>32.1</b>	<b>372</b>	32.1	372
437.leslie3d	73.4	128	56.8	165	<b>58.6</b>	<b>160</b>	73.4	128	56.8	165	<b>58.6</b>	<b>160</b>
444.namd	470	17.1	470	17.1	<b>470</b>	<b>17.1</b>	462	17.3	462	17.4	<b>462</b>	<b>17.3</b>
447.dealII	<b>299</b>	<b>38.3</b>	299	38.3	299	38.2	<b>299</b>	<b>38.3</b>	299	38.3	299	38.2
450.soplex	259	32.2	<b>260</b>	<b>32.0</b>	261	32.0	259	32.2	<b>260</b>	<b>32.0</b>	261	32.0
453.povray	<b>170</b>	<b>31.4</b>	169	31.5	170	31.3	144	37.1	<b>141</b>	<b>37.7</b>	139	38.3
454.calculix	<b>290</b>	<b>28.5</b>	290	28.5	287	28.7	269	30.7	267	30.9	<b>268</b>	<b>30.8</b>
459.GemsFDTD	83.5	127	<b>83.5</b>	<b>127</b>	84.1	126	<b>69.4</b>	<b>153</b>	69.8	152	69.4	153
465.tonto	388	25.4	349	28.2	<b>387</b>	<b>25.4</b>	<b>312</b>	<b>31.6</b>	307	32.1	312	31.5
470.lbm	<b>36.8</b>	<b>373</b>	36.2	379	37.6	365	<b>36.8</b>	<b>373</b>	36.2	379	37.6	365
481.wrf	191	58.4	<b>189</b>	<b>59.0</b>	188	59.4	191	58.4	<b>189</b>	<b>59.0</b>	188	59.4
482.sphinx3	365	53.4	<b>367</b>	<b>53.1</b>	375	52.0	361	54.0	<b>361</b>	<b>54.1</b>	356	54.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on localhost.localdomain Mon May 14 17:34:32 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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**Test date:** May-2012

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### Platform Notes (Continued)

```

model name : Intel(R) Xeon(R) CPU E5-2620 0 @ 2.00GHz
  2 "physical id"s (chips)
  24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings  : 12
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB

```

```

From /proc/meminfo
MemTotal:      132138108 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-220.4.2.el6.x86_64 #1 SMP Mon Feb 6
16:39:28 EST 2012 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 May 14 17:11

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
OMP_NUM_THREADS = "12"

```

```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

```

HITACHI BladeSymphony BS520H and HITACHI Compute Blade 520H are electronically equivalent. The results have been measured on a HITACHI BladeSymphony BS520H.



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.lelie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias



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Software Availability: Feb-2012

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

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## Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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