



# SPEC® CFP2006 Result

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## Bull SAS

SPECfp®2006 = 45.1

NovaScale R450 F3 (Intel Xeon E5-2403, 1.80 GHz)

SPECfp\_base2006 = 43.4

CPU2006 license: 20

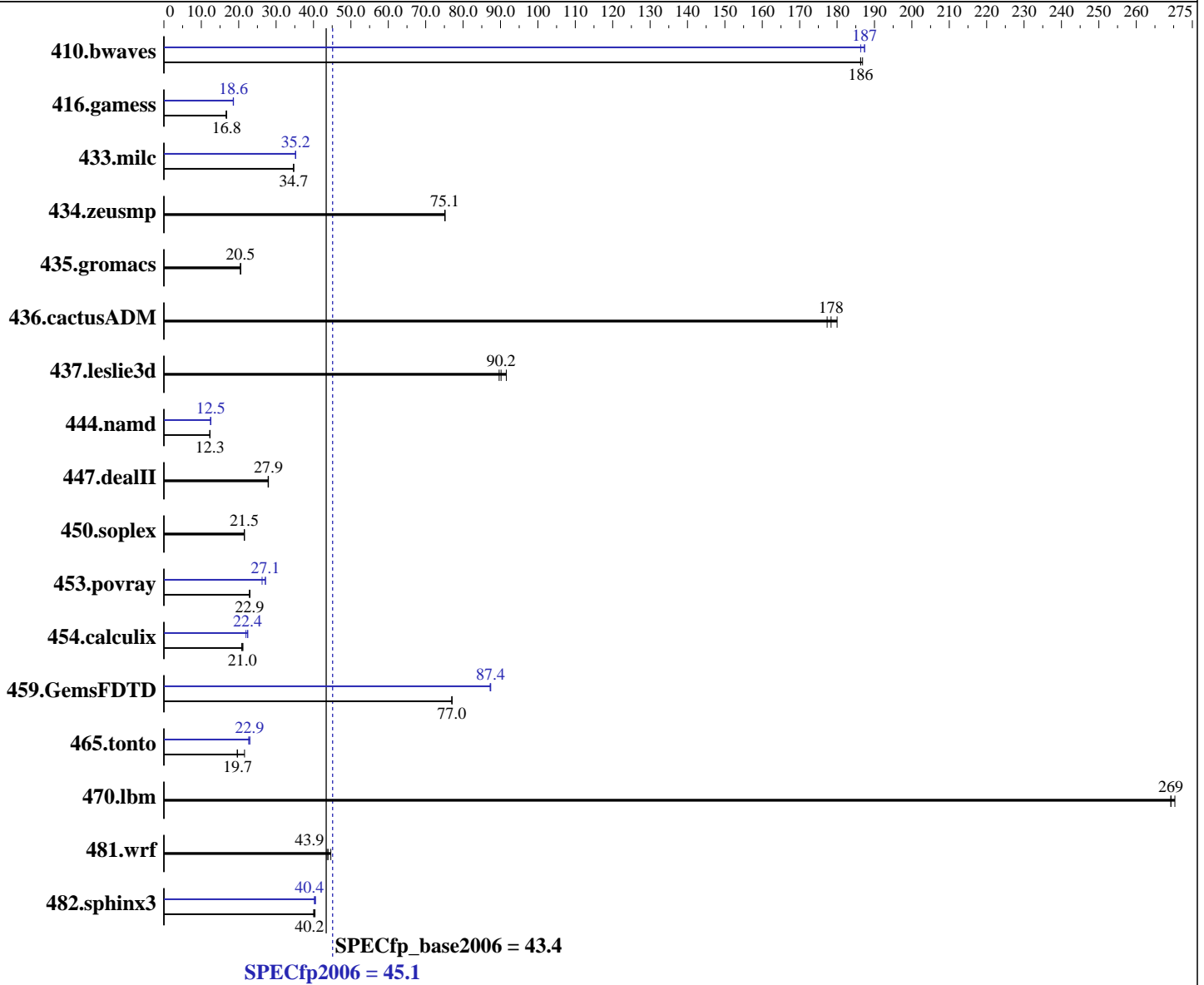
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Mar-2012

Hardware Availability: May-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2403  
 CPU Characteristics:  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.9-default  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (add definition here)

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L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)  
 Disk Subsystem: 2 x 600 GB 15000 RPM SAS, RAID 1  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>72.9</b>	<b>186</b>	72.7	187	72.9	186	72.9	186	72.5	187	<b>72.5</b>	<b>187</b>
416.gamess	<b>1167</b>	<b>16.8</b>	1179	16.6	1166	16.8	1057	18.5	<b>1053</b>	<b>18.6</b>	1053	18.6
433.milc	<b>265</b>	<b>34.7</b>	265	34.7	265	34.7	261	35.2	261	35.2	<b>261</b>	<b>35.2</b>
434.zeusmp	121	75.1	<b>121</b>	<b>75.1</b>	121	75.1	121	75.1	<b>121</b>	<b>75.1</b>	121	75.1
435.gromacs	349	20.5	<b>349</b>	<b>20.5</b>	348	20.5	349	20.5	<b>349</b>	<b>20.5</b>	348	20.5
436.cactusADM	<b>67.0</b>	<b>178</b>	67.4	177	66.4	180	<b>67.0</b>	<b>178</b>	67.4	177	66.4	180
437.leslie3d	105	89.6	103	91.6	<b>104</b>	<b>90.2</b>	105	89.6	103	91.6	<b>104</b>	<b>90.2</b>
444.namd	<b>652</b>	<b>12.3</b>	652	12.3	652	12.3	642	12.5	641	12.5	<b>641</b>	<b>12.5</b>
447.dealII	<b>410</b>	<b>27.9</b>	409	28.0	411	27.9	<b>410</b>	<b>27.9</b>	409	28.0	411	27.9
450.soplex	<b>388</b>	<b>21.5</b>	387	21.5	388	21.5	<b>388</b>	<b>21.5</b>	387	21.5	388	21.5
453.povray	233	22.9	231	23.0	<b>232</b>	<b>22.9</b>	196	27.2	203	26.2	<b>196</b>	<b>27.1</b>
454.calculix	397	20.8	390	21.1	<b>393</b>	<b>21.0</b>	368	22.4	<b>368</b>	<b>22.4</b>	376	21.9
459.GemsFDTD	<b>138</b>	<b>77.0</b>	138	77.0	138	77.1	121	87.4	122	87.2	<b>121</b>	<b>87.4</b>
465.tonto	502	19.6	456	21.6	<b>500</b>	<b>19.7</b>	<b>429</b>	<b>22.9</b>	434	22.7	429	23.0
470.lbm	50.8	270	<b>51.0</b>	<b>269</b>	51.0	269	50.8	270	<b>51.0</b>	<b>269</b>	51.0	269
481.wrf	256	43.6	250	44.6	<b>254</b>	<b>43.9</b>	256	43.6	250	44.6	<b>254</b>	<b>43.9</b>
482.sphinx3	487	40.0	<b>485</b>	<b>40.2</b>	482	40.4	<b>483</b>	<b>40.4</b>	481	40.5	485	40.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

CPU Power Management set to Maximum Performance  
 Memory Frequency set to Maximum Performance  
 Turbo Boost set to Enabled  
 C States/C1E set to Enabled  
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on Silk-2P Fri Mar 16 02:08:20 2012

This section contains SUT (System Under Test) info as seen by

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### Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2403 0 @ 1.80GHz
  2 "physical id"s (chips)
  8 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 4
    siblings  : 4
    physical 0: cores 0 1 2 3
    physical 1: cores 0 1 2 3
  cache size : 10240 KB

```

```

From /proc/meminfo
MemTotal:      49381468 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux Silk-2P 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
(54ddfaf) x86_64 x86_64 x86_64 GNU/Linux

```

```
run-level 3 Mar 15 15:20 last=S
```

```

SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext3  266G  9.1G  244G   4% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

### General Notes

```

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "8"

```

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
 Transparent Huge Pages disabled with:  
 echo never > /sys/kernel/mm/transparent\_hugepage/enabled  
 Filesystem page cache cleared with:  
 echo 1> /proc/sys/vm/drop\_caches  
 The Dell PowerEdge R520 and the Bull NovaScale R450 F3 models are electronically equivalent.  
 The results have been measured on a Dell PowerEdge R520 model

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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