



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6072R-73DARF (X9DRD-7LN4F, Intel Xeon E5-2650L)

SPECint®2006 = 36.6

SPECint\_base2006 = 34.3

CPU2006 license: 001176

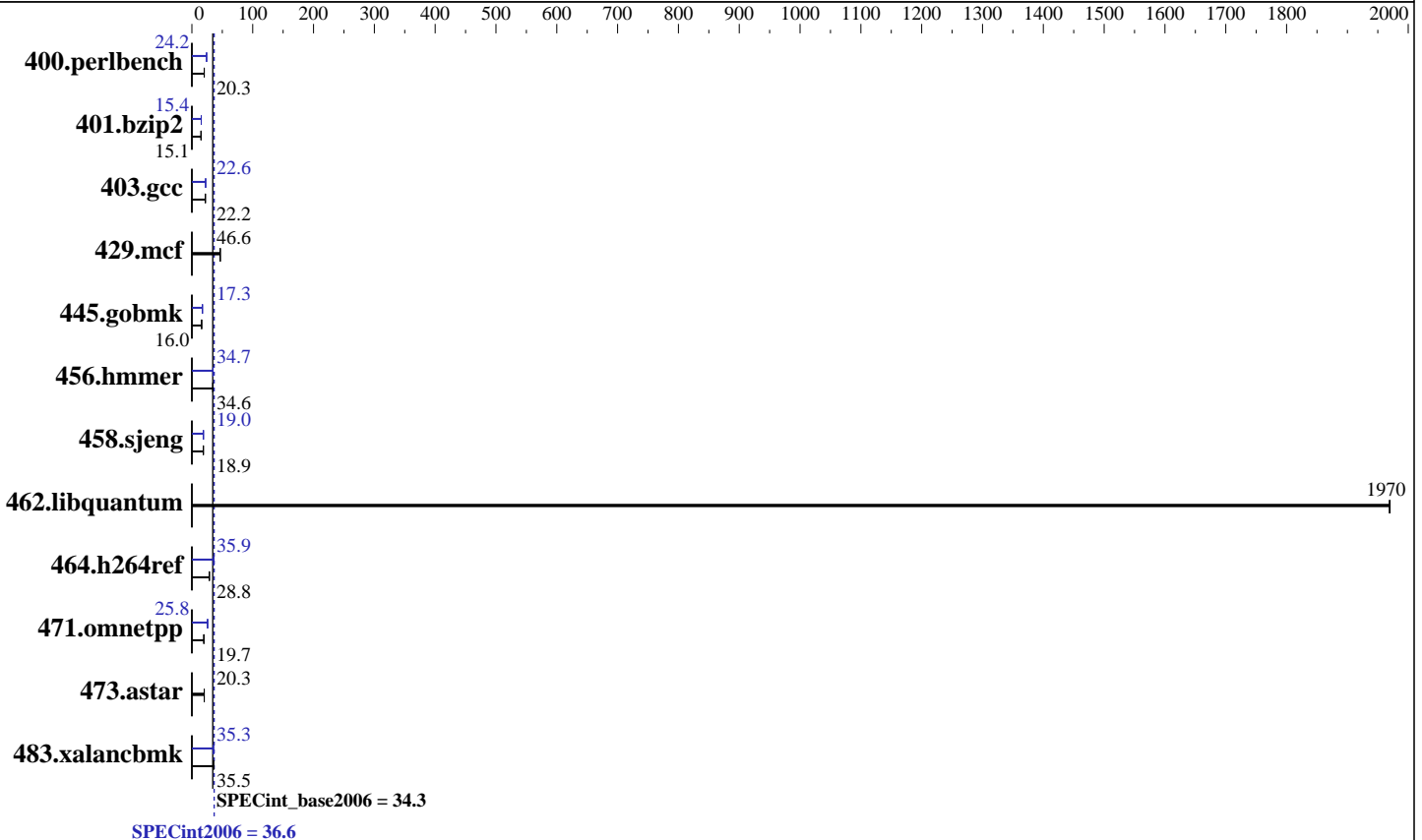
Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2650L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 750 GB SATA III, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86\_64  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6072R-73DARF (X9DRD-7LN4F, Intel Xeon E5-2650L)

SPECint2006 = 36.6

SPECint\_base2006 = 34.3

CPU2006 license: 001176  
Test sponsor: Supermicro  
Tested by: Supermicro

Test date: May-2012  
Hardware Availability: Mar-2012  
Software Availability: Dec-2011

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	483	20.2	480	20.4	<b>481</b>	<b>20.3</b>	404	24.2	407	24.0	<b>404</b>	<b>24.2</b>
401.bzip2	<b>640</b>	<b>15.1</b>	640	15.1	643	15.0	<b>628</b>	<b>15.4</b>	629	15.3	627	15.4
403.gcc	363	22.2	363	22.1	<b>363</b>	<b>22.2</b>	356	22.6	<b>356</b>	<b>22.6</b>	357	22.6
429.mcf	197	46.2	<b>196</b>	<b>46.6</b>	195	46.9	197	46.2	<b>196</b>	<b>46.6</b>	195	46.9
445.gobmk	654	16.0	655	16.0	<b>655</b>	<b>16.0</b>	605	17.3	<b>605</b>	<b>17.3</b>	605	17.3
456.hammer	271	34.4	<b>270</b>	<b>34.6</b>	269	34.6	268	34.8	<b>269</b>	<b>34.7</b>	269	34.7
458.sjeng	641	18.9	642	18.8	<b>641</b>	<b>18.9</b>	630	19.2	<b>637</b>	<b>19.0</b>	652	18.6
462.libquantum	10.5	1970	10.5	1970	<b>10.5</b>	<b>1970</b>	10.5	1970	10.5	1970	<b>10.5</b>	<b>1970</b>
464.h264ref	776	28.5	<b>769</b>	<b>28.8</b>	768	28.8	618	35.8	616	35.9	<b>617</b>	<b>35.9</b>
471.omnetpp	316	19.8	<b>317</b>	<b>19.7</b>	318	19.6	245	25.5	240	26.1	<b>242</b>	<b>25.8</b>
473.astar	346	20.3	<b>346</b>	<b>20.3</b>	346	20.3	346	20.3	<b>346</b>	<b>20.3</b>	346	20.3
483.xalancbmk	<b>195</b>	<b>35.5</b>	195	35.3	192	35.9	196	35.3	193	35.7	<b>195</b>	<b>35.3</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"  
OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6072R-73DARF (X9DRD-7LN4F, Intel Xeon E5-2650L)

SPECint2006 = 36.6

SPECint\_base2006 = 34.3

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011

## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6072R-73DARF (X9DRD-7LN4F, Intel Xeon E5-2650L)

SPECint2006 = 36.6

SPECint\_base2006 = 34.3

**CPU2006 license:** 001176  
**Test sponsor:** Supermicro  
**Tested by:** Supermicro

**Test date:** May-2012  
**Hardware Availability:** Mar-2012  
**Software Availability:** Dec-2011

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
-ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 6072R-73DARF (X9DRD-7LN4F, Intel Xeon E5-2650L)

SPECint2006 = 36.6

SPECint\_base2006 = 34.3

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011

## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 04:39:46 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 6 June 2012.