



# SPEC® CFP2006 Result

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## Bull SAS

SPECfp®2006 = 59.9

NovaScale R430 F3 (Intel Xeon E5-2430L, 2.00 GHz)

SPECfp\_base2006 = 57.4

CPU2006 license: 20

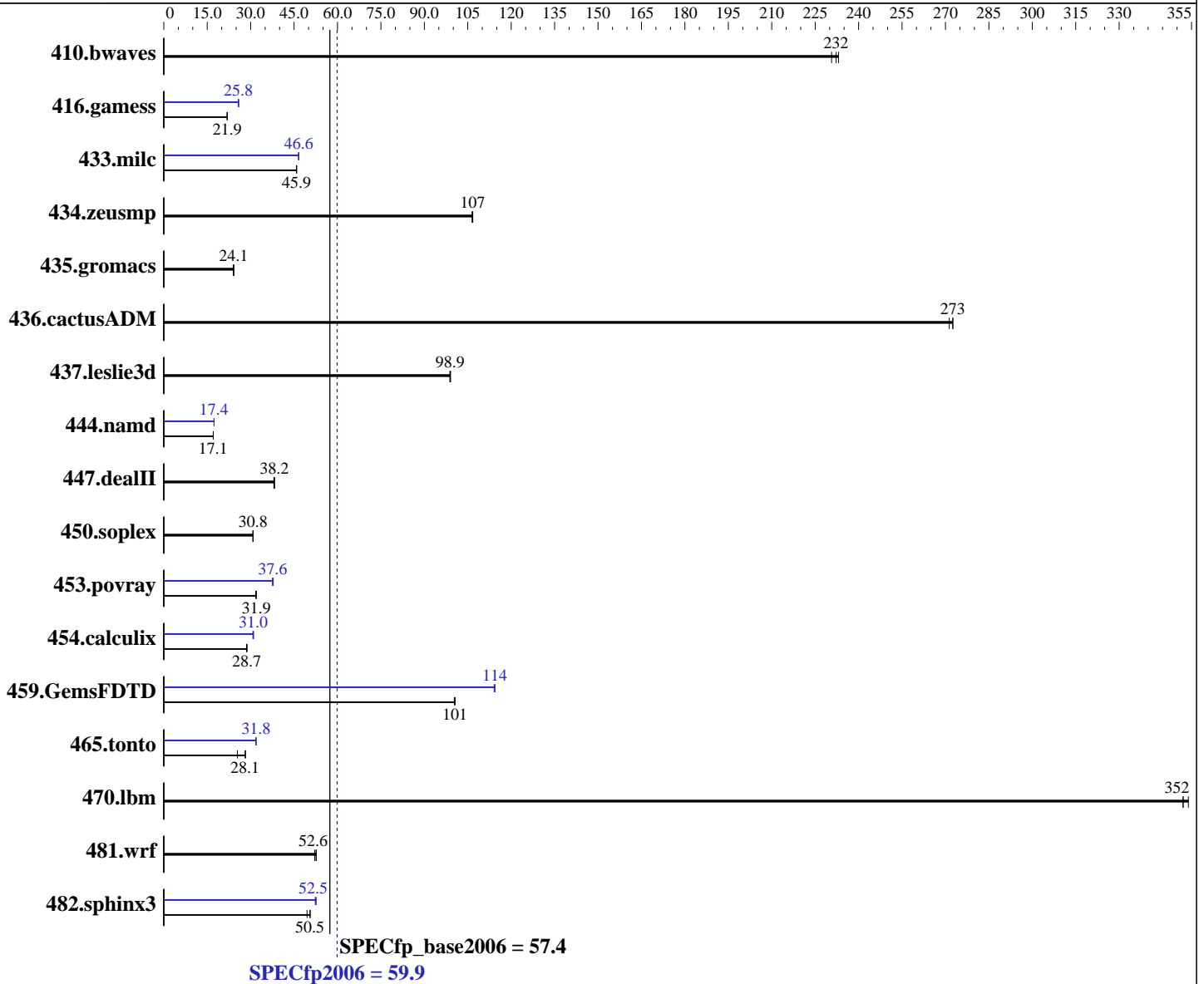
Test date: Feb-2012

Test sponsor: Bull SAS

Hardware Availability: May-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2430L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.9-default  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (add definition here)

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 2 x 600 GB 15000 RPM SAS, RAID 1  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	58.3	233	58.9	231	<b>58.5</b>	<b>232</b>	58.3	233	58.9	231	<b>58.5</b>	<b>232</b>
416.gamess	895	21.9	893	21.9	<b>894</b>	<b>21.9</b>	758	25.8	<b>759</b>	<b>25.8</b>	760	25.8
433.milc	200	45.9	200	45.9	<b>200</b>	<b>45.9</b>	<b>197</b>	<b>46.6</b>	197	46.6	197	46.6
434.zeusmp	85.3	107	85.5	106	<b>85.3</b>	<b>107</b>	85.3	107	85.5	106	<b>85.3</b>	<b>107</b>
435.gromacs	297	24.0	295	24.2	<b>296</b>	<b>24.1</b>	297	24.0	295	24.2	<b>296</b>	<b>24.1</b>
436.cactusADM	<b>43.8</b>	<b>273</b>	44.0	271	43.8	273	<b>43.8</b>	<b>273</b>	44.0	271	43.8	273
437.leslie3d	<b>95.1</b>	<b>98.9</b>	94.9	99.1	95.1	98.9	<b>95.1</b>	<b>98.9</b>	94.9	99.1	95.1	98.9
444.namd	470	17.1	<b>469</b>	<b>17.1</b>	469	17.1	462	17.4	<b>462</b>	<b>17.4</b>	462	17.4
447.dealII	301	38.0	<b>299</b>	<b>38.2</b>	299	38.2	301	38.0	<b>299</b>	<b>38.2</b>	299	38.2
450.soplex	<b>270</b>	<b>30.8</b>	270	30.8	271	30.8	<b>270</b>	<b>30.8</b>	270	30.8	271	30.8
453.povray	166	32.0	168	31.8	<b>167</b>	<b>31.9</b>	141	37.8	142	37.5	<b>142</b>	<b>37.6</b>
454.calculix	287	28.7	288	28.6	<b>288</b>	<b>28.7</b>	268	30.8	266	31.0	<b>266</b>	<b>31.0</b>
459.GemsFDTD	<b>106</b>	<b>101</b>	106	100	106	101	92.8	114	93.0	114	<b>92.8</b>	<b>114</b>
465.tonto	387	25.4	348	28.2	<b>351</b>	<b>28.1</b>	<b>309</b>	<b>31.8</b>	310	31.8	309	31.9
470.lbm	<b>39.0</b>	<b>352</b>	38.8	354	39.0	352	<b>39.0</b>	<b>352</b>	38.8	354	39.0	352
481.wrf	212	52.6	<b>212</b>	<b>52.6</b>	214	52.1	212	52.6	<b>212</b>	<b>52.6</b>	214	52.1
482.sphinx3	385	50.6	394	49.5	<b>386</b>	<b>50.5</b>	373	52.3	<b>371</b>	<b>52.5</b>	370	52.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

CPU Power Management set to Maximum Performance  
 Memory Frequency set to Maximum Performance  
 Turbo Boost set to Enabled  
 C States/C1E set to Enabled  
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on Slice Fri Feb 24 08:33:23 2012

This section contains SUT (System Under Test) info as seen by

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### Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2430L 0 @ 2.00GHz
    2 "physical id"s (chips)
    24 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 6
    siblings  : 12
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
  cache size : 15360 KB

```

```

From /proc/meminfo
MemTotal:      49381468 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux Slice 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012 (54ddfaf)
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Feb 24 00:34 last=S

```

SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext3  266G  9.1G  244G   4% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "12"

```

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
 Transparent Huge Pages disabled with:  
 echo never > /sys/kernel/mm/transparent\_hugepage/enabled  
 Filesystem page cache cleared with:  
 echo 1> /proc/sys/vm/drop\_caches  
 The Dell PowerEdge R420 and the Bull NovaScale R430 F3 models are electronically equivalent.  
 The results have been measured on a Dell PowerEdge R420 model

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64



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**Hardware Availability:** May-2012

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## Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

`433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

`470.lbm: basepeak = yes`

`482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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