



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

SPECfp<sup>®</sup>2006 = **63.3**

IBM System x3650 M4 (Intel Xeon E5-2650L)

SPECfp\_base2006 = **59.7**

CPU2006 license: 11

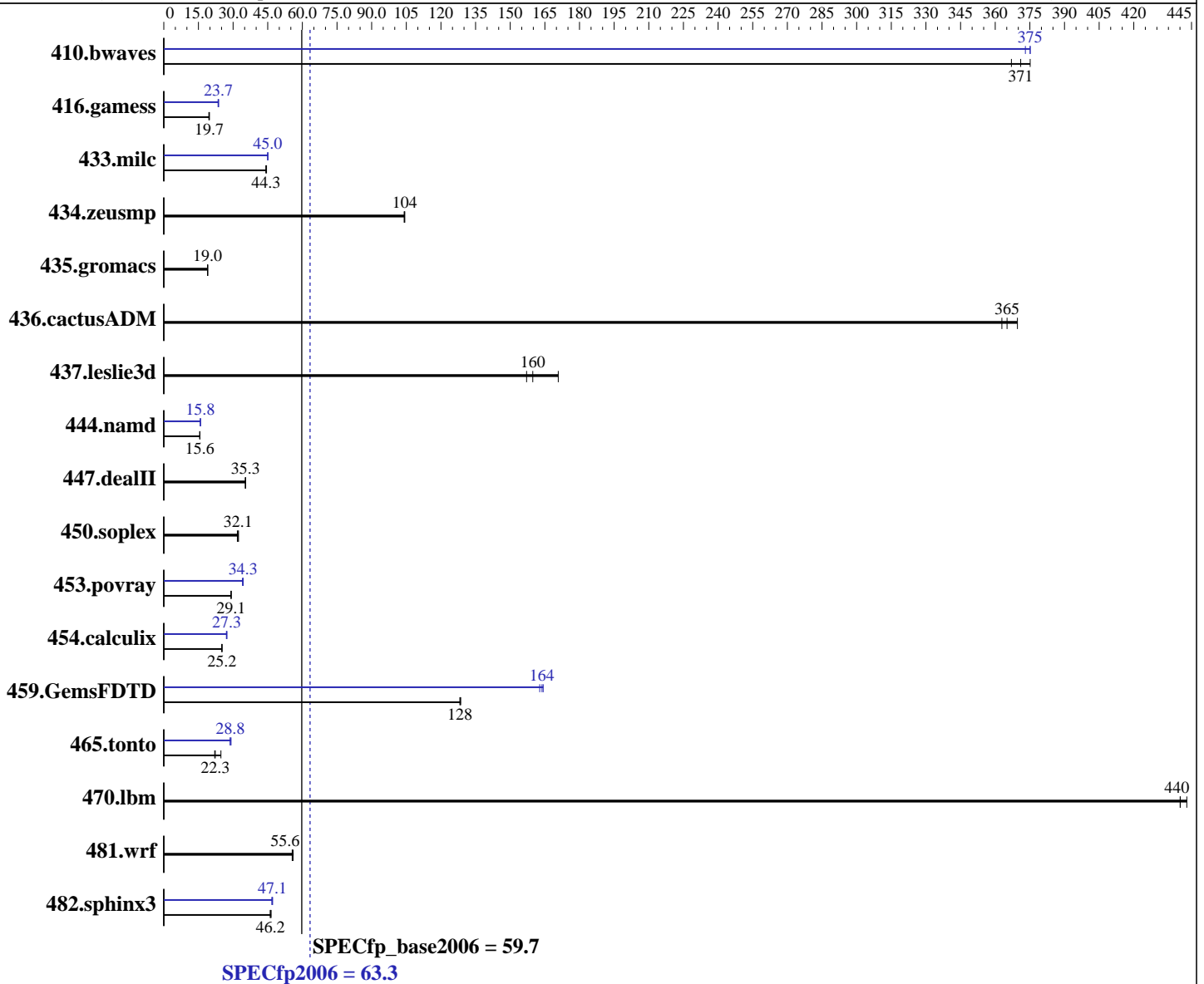
Test date: Apr-2012

Test sponsor: IBM Corporation

Hardware Availability: Mar-2012

Tested by: IBM Corporation

Software Availability: Oct-2011



### Hardware

CPU Name: Intel Xeon E5-2650L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)  
 2.6.32-131.0.15.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 1 TB SAS, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>36.6</b>	<b>371</b>	36.2	375	37.0	367	36.4	373	36.2	375	<b>36.2</b>	<b>375</b>
416.gamess	<b>994</b>	<b>19.7</b>	993	19.7	1004	19.5	<b>827</b>	<b>23.7</b>	825	23.7	834	23.5
433.milc	208	44.2	<b>207</b>	<b>44.3</b>	206	44.5	204	45.0	<b>204</b>	<b>45.0</b>	204	45.0
434.zeusmp	87.4	104	87.2	104	<b>87.2</b>	<b>104</b>	87.4	104	87.2	104	<b>87.2</b>	<b>104</b>
435.gromacs	375	19.0	378	18.9	<b>376</b>	<b>19.0</b>	375	19.0	378	18.9	<b>376</b>	<b>19.0</b>
436.cactusADM	<b>32.7</b>	<b>365</b>	32.3	370	32.9	363	<b>32.7</b>	<b>365</b>	32.3	370	32.9	363
437.leslie3d	55.0	171	59.8	157	<b>58.8</b>	<b>160</b>	55.0	171	59.8	157	<b>58.8</b>	<b>160</b>
444.namd	514	15.6	514	15.6	<b>514</b>	<b>15.6</b>	506	15.8	<b>506</b>	<b>15.8</b>	506	15.9
447.dealII	323	35.5	<b>324</b>	<b>35.3</b>	324	35.3	323	35.5	<b>324</b>	<b>35.3</b>	324	35.3
450.soplex	258	32.4	<b>260</b>	<b>32.1</b>	262	31.8	258	32.4	<b>260</b>	<b>32.1</b>	262	31.8
453.povray	182	29.2	<b>183</b>	<b>29.1</b>	183	29.1	156	34.1	<b>155</b>	<b>34.3</b>	155	34.4
454.calculix	327	25.2	<b>328</b>	<b>25.2</b>	329	25.1	302	27.3	304	27.1	<b>302</b>	<b>27.3</b>
459.GemsFDTD	<b>82.7</b>	<b>128</b>	82.5	129	82.7	128	64.6	164	<b>64.8</b>	<b>164</b>	65.2	163
465.tonto	447	22.0	398	24.7	<b>442</b>	<b>22.3</b>	342	28.7	<b>342</b>	<b>28.8</b>	338	29.1
470.lbm	<b>31.2</b>	<b>440</b>	31.2	440	31.0	443	<b>31.2</b>	<b>440</b>	31.2	440	31.0	443
481.wrf	<b>201</b>	<b>55.6</b>	199	56.0	201	55.6	<b>201</b>	<b>55.6</b>	199	56.0	201	55.6
482.sphinx3	<b>422</b>	<b>46.2</b>	423	46.1	419	46.5	<b>414</b>	<b>47.1</b>	414	47.1	417	46.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Zone reclaim mode enabled with:  
 echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS Settings:  
 Operating Mode set to Maximum Performance  
 Sysinfo program /root/SPECcpu-v1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on x3650M4 Sun Apr 15 10:23:27 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2650L 0 @ 1.80GHz
 2 "physical id"s (chips)
 32 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 8
siblings   : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

From /proc/meminfo

```
MemTotal:      132113224 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.1 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux x3650M4 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10 15:42:40 EDT 2011
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 13 15:03

SPEC is set to: /root/SPECcpu-v1.2

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_x3650m4-lv_root
                ext4      790G  69G  681G  10% /
```

Additional information from dmidecode:

Memory:

```
16x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/root/SPECcpu-v1.2/libs/32:/root/SPECcpu-v1.2/libs/64"

OMP\_NUM\_THREADS = "16"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>



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Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 05:12:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 9 May 2012.