



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]2006 = **81.5**

PRIMERGY TX300 S7, Intel Xeon E5-2643, 3.30 GHz

SPECfp_base2006 = **78.0**

CPU2006 license: 19

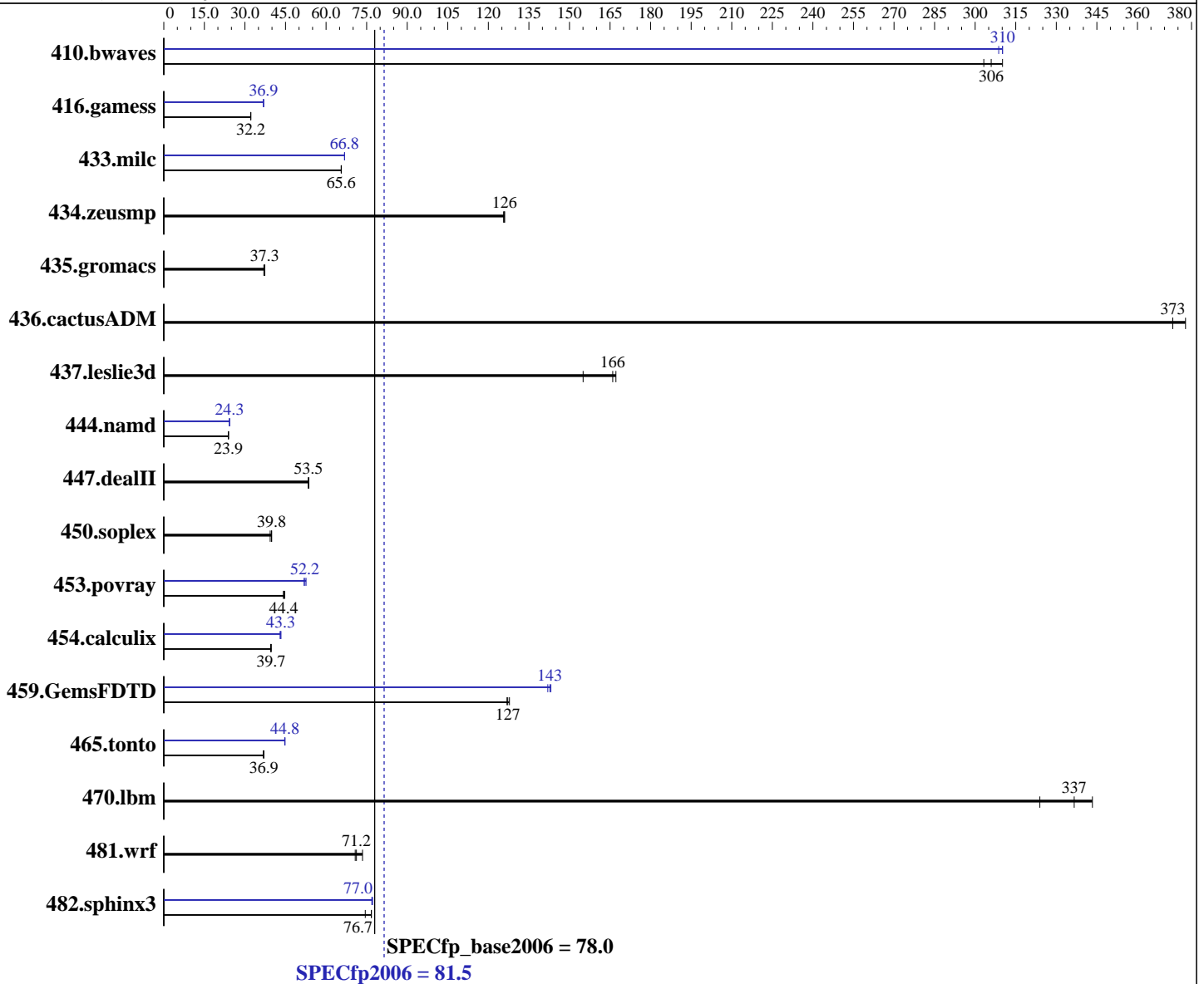
Test date: Feb-2012

Test sponsor: Fujitsu

Hardware Availability: Mar-2012

Tested by: Fujitsu

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2643
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	44.4	306	43.8	310	44.8	303	43.8	310	43.8	310	44.0	309
416.gamess	608	32.2	609	32.2	610	32.1	531	36.9	532	36.8	530	36.9
433.milc	140	65.6	140	65.6	140	65.7	137	66.8	137	66.8	137	66.9
434.zeusmp	72.2	126	72.4	126	72.2	126	72.2	126	72.4	126	72.2	126
435.gromacs	191	37.4	193	37.0	191	37.3	191	37.4	193	37.0	191	37.3
436.cactusADM	32.0	373	32.0	373	31.6	378	32.0	373	32.0	373	31.6	378
437.leslie3d	60.6	155	56.6	166	56.2	167	60.6	155	56.6	166	56.2	167
444.namd	336	23.9	335	23.9	336	23.9	330	24.3	330	24.3	330	24.3
447.dealII	214	53.5	213	53.6	214	53.5	214	53.5	213	53.6	214	53.5
450.soplex	209	39.8	209	39.9	212	39.3	209	39.8	209	39.9	212	39.3
453.povray	119	44.7	120	44.2	120	44.4	102	52.2	103	51.9	101	52.6
454.calculix	208	39.7	209	39.5	208	39.7	191	43.3	192	43.0	191	43.3
459.GemsFDTD	83.0	128	83.4	127	83.6	127	74.3	143	74.1	143	74.7	142
465.tonto	268	36.8	266	36.9	266	37.0	219	44.8	220	44.8	220	44.7
470.lbm	42.4	324	40.8	337	40.0	343	42.4	324	40.8	337	40.0	343
481.wrf	152	73.5	157	71.2	158	70.8	152	73.5	157	71.2	158	70.8
482.sphinx3	254	76.8	262	74.5	254	76.7	253	77.0	253	77.0	252	77.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Intel HT Technology = Disable
 Frequency Floor Override = Enable

General Notes

Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,scatter"
 LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

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General Notes (Continued)

OMP_NUM_THREADS = "8"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

This result was measured on the PRIMERGY RX350 S7. The PRIMERGY RX350 S7 and the PRIMERGY TX300 S7 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel`

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

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