



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 80.1

PowerEdge M620 (Intel Xeon E5-2665, 2.40 GHz)

SPECfp_base2006 = 76.0

CPU2006 license: 55

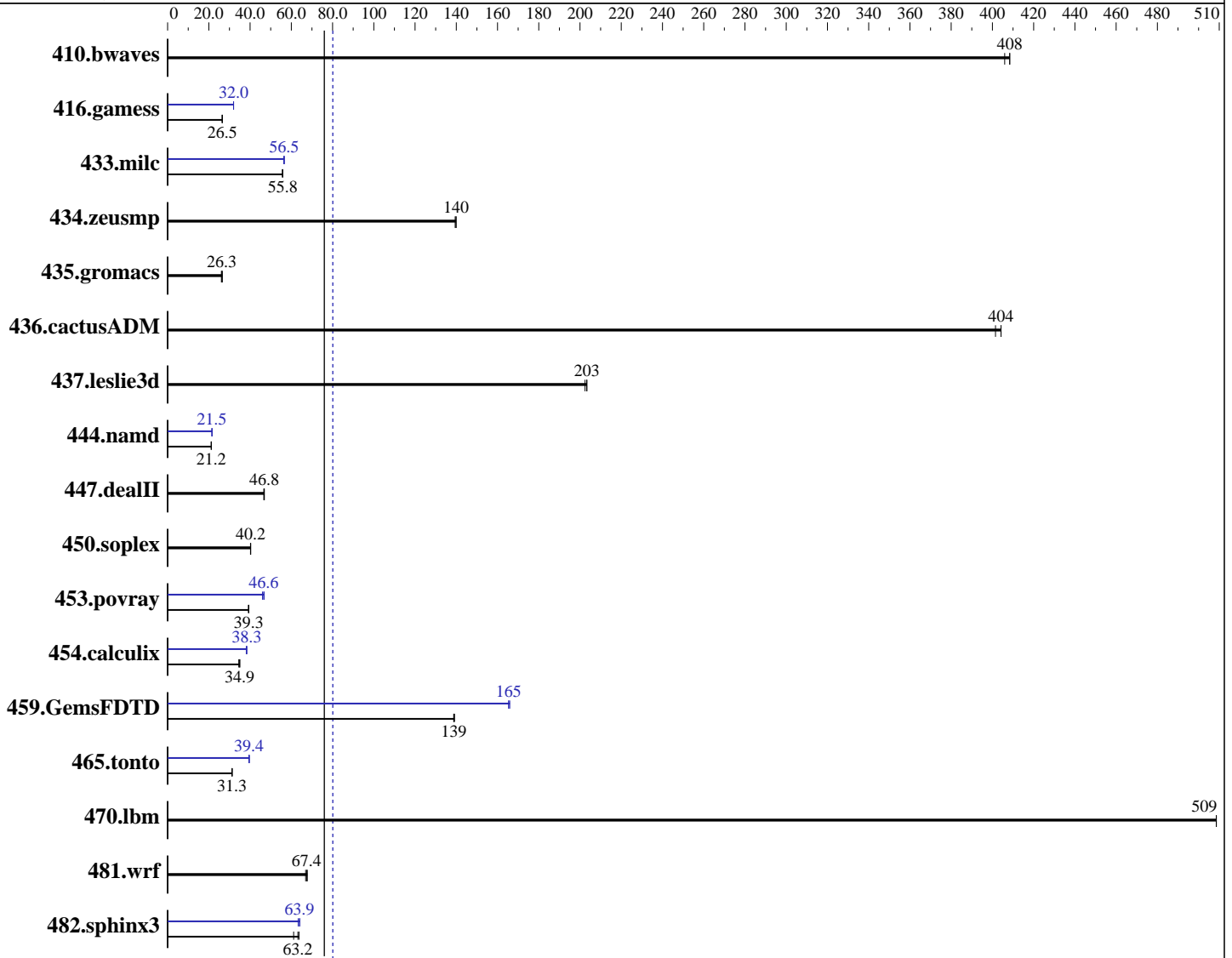
Test date: Feb-2012

Test sponsor: Dell Inc.

Hardware Availability: Mar-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



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Hardware

CPU Name: Intel Xeon E5-2665
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64)
 3.0.13-0.9-default
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (add definition here)

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	33.5	406	33.3	408	<u>33.3</u>	<u>408</u>	33.5	406	33.3	408	<u>33.3</u>	<u>408</u>
416.gamess	737	26.6	740	26.4	<u>738</u>	<u>26.5</u>	<u>612</u>	<u>32.0</u>	612	32.0	612	32.0
433.milc	165	55.7	165	55.8	<u>165</u>	<u>55.8</u>	163	56.3	162	56.6	<u>162</u>	<u>56.5</u>
434.zeusmp	65.1	140	65.3	139	<u>65.1</u>	<u>140</u>	65.1	140	65.3	139	<u>65.1</u>	<u>140</u>
435.gromacs	269	26.5	274	26.1	<u>272</u>	<u>26.3</u>	269	26.5	274	26.1	<u>272</u>	<u>26.3</u>
436.cactusADM	29.6	404	29.8	401	<u>29.6</u>	<u>404</u>	29.6	404	29.8	401	<u>29.6</u>	<u>404</u>
437.leslie3d	46.2	203	46.4	202	<u>46.2</u>	<u>203</u>	46.2	203	46.4	202	<u>46.2</u>	<u>203</u>
444.namd	<u>379</u>	<u>21.2</u>	379	21.2	379	21.2	373	21.5	<u>373</u>	<u>21.5</u>	372	21.5
447.dealII	<u>244</u>	<u>46.8</u>	245	46.7	244	46.9	<u>244</u>	<u>46.8</u>	245	46.7	244	46.9
450.soplex	<u>207</u>	<u>40.2</u>	207	40.2	207	40.3	<u>207</u>	<u>40.2</u>	207	40.2	207	40.3
453.povray	135	39.4	<u>135</u>	<u>39.3</u>	136	39.2	<u>114</u>	<u>46.6</u>	116	46.0	114	46.8
454.calculix	239	34.5	<u>236</u>	<u>34.9</u>	236	35.0	<u>215</u>	<u>38.3</u>	215	38.4	216	38.2
459.GemsFDTD	76.4	139	76.2	139	<u>76.4</u>	<u>139</u>	64.1	165	<u>64.1</u>	<u>165</u>	63.9	166
465.tonto	<u>315</u>	<u>31.3</u>	314	31.3	316	31.2	<u>250</u>	<u>39.4</u>	250	39.4	248	39.7
470.lbm	27.0	509	<u>27.0</u>	<u>509</u>	27.0	509	27.0	509	<u>27.0</u>	<u>509</u>	27.0	509
481.wrf	167	66.9	<u>166</u>	<u>67.4</u>	165	67.8	167	66.9	<u>166</u>	<u>67.4</u>	165	67.8
482.sphinx3	319	61.1	306	63.7	<u>308</u>	<u>63.2</u>	308	63.3	304	64.1	<u>305</u>	<u>63.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

System Profile set to Custom
 CPU Power Management set to Maximum Performance
 Memory Frequency set to Maximum Performance
 Turbo Boost set to Enabled
 C States/C1E set to Enabled
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
 running on Mojo-SPA-01 Mon Feb 13 23:44:55 2012

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2665 0 @ 2.40GHz
  2 "physical id"s (chips)
  32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 8
    siblings  : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      132089864 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux Mojo-SPA-01 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
(54ddfaf) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Feb 13 17:12 last=S

```

SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext3  133G   69G   63G  53% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

```

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "16"

```

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages disabled with:

```
echo never > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.xml>



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