



SPEC® CFP2006 Result

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Intel Corporation

SPECfp®2006 = **51.3**

Intel DH61WW motherboard (Intel Core i5-2400)

SPECfp_base2006 = **49.5**

CPU2006 license: 13

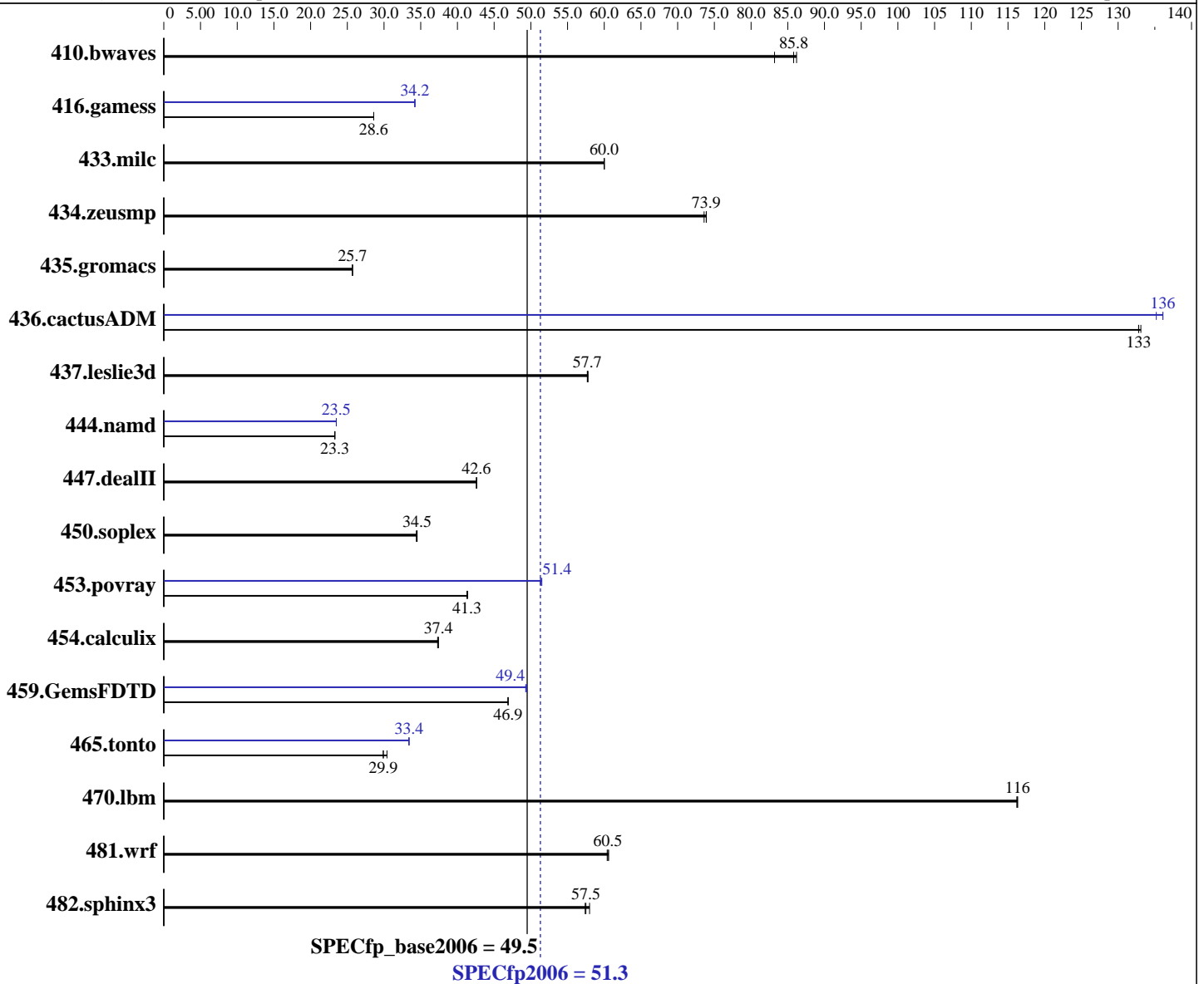
Test date: Oct-2011

Test sponsor: Intel Corporation

Hardware Availability: Jan-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i5-2400
 CPU Characteristics: Intel Turbo Boost Technology up to 3.4 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Windows 7 Ultimate SP1 (64-bit)
 Compiler: C/C++: Version 12.0.3.176 of Intel C++ Studio XE for Windows;
 Fortran: Version 12.0.3.176 of Intel Visual Fortran Studio XE for Windows;
 Libraries: Version 15.00.30729.01 of Microsoft Visual Studio 2008 Professional SP1
 Auto Parallel: Yes
 File System: NTFS

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L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 250GB HDD, 7200 rpm
 Other Hardware: None

System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	163	83.2	158	85.8	158	86.2	163	83.2	158	85.8	158	86.2
416.gamess	684	28.6	684	28.6	685	28.6	572	34.2	572	34.2	572	34.2
433.milc	153	60.0	153	60.0	153	60.0	153	60.0	153	60.0	153	60.0
434.zeusmp	123	73.9	123	73.9	124	73.6	123	73.9	123	73.9	124	73.6
435.gromacs	278	25.7	278	25.7	278	25.7	278	25.7	278	25.7	278	25.7
436.cactusADM	90.0	133	89.8	133	90.0	133	87.8	136	88.4	135	87.8	136
437.leslie3d	163	57.7	163	57.8	163	57.7	163	57.7	163	57.8	163	57.7
444.namd	345	23.3	345	23.3	345	23.3	341	23.5	341	23.5	341	23.5
447.dealII	269	42.6	269	42.6	269	42.6	269	42.6	269	42.6	269	42.6
450.soplex	242	34.5	241	34.5	242	34.4	242	34.5	241	34.5	242	34.4
453.povray	129	41.4	129	41.3	129	41.3	104	51.4	104	51.3	103	51.5
454.calculix	221	37.3	221	37.4	221	37.4	221	37.3	221	37.4	221	37.4
459.GemsFDTD	226	46.9	226	46.9	226	46.9	215	49.3	215	49.4	215	49.4
465.tonto	323	30.4	329	29.9	329	29.9	294	33.4	295	33.4	294	33.4
470.lbm	118	116	118	116	118	116	118	116	118	116	118	116
481.wrf	185	60.5	185	60.4	184	60.6	185	60.5	185	60.4	184	60.6
482.sphinx3	340	57.4	339	57.5	336	58.0	340	57.4	339	57.5	336	58.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Component Notes

Tested systems can be used with Shin-G ATX case, PC Power and Cooling 1200W power supply

General Notes

OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

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Base Compiler Invocation (Continued)

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
 416.gamess: -DSPEC_CPU_P64
 433.milc: -DSPEC_CPU_P64
 434.zeusmp: -DSPEC_CPU_P64
 435.gromacs: -DSPEC_CPU_P64
 436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
 437.leslie3d: -DSPEC_CPU_P64
 444.namd: -DSPEC_CPU_P64 /TP
 447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 450.soplex: -DSPEC_CPU_P64
 453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
 459.GemsFDTD: -DSPEC_CPU_P64
 465.tonto: -DSPEC_CPU_P64
 470.lbm: -DSPEC_CPU_P64
 481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

C++ benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
-Qcxx-features -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:

-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000



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Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 sh1W64M.lib
-link /FORCE:MULTIPLE

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep-
/F1000000000

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel
/F1000000000

465.tonto: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc
/F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2
-Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Thu Jul 24 01:19:08 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 6 December 2011.