



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®2006 = 56.4

PowerEdge M610 (Intel Xeon X5560, 2.80 GHz)

SPECfp_base2006 = 52.1

CPU2006 license: 55

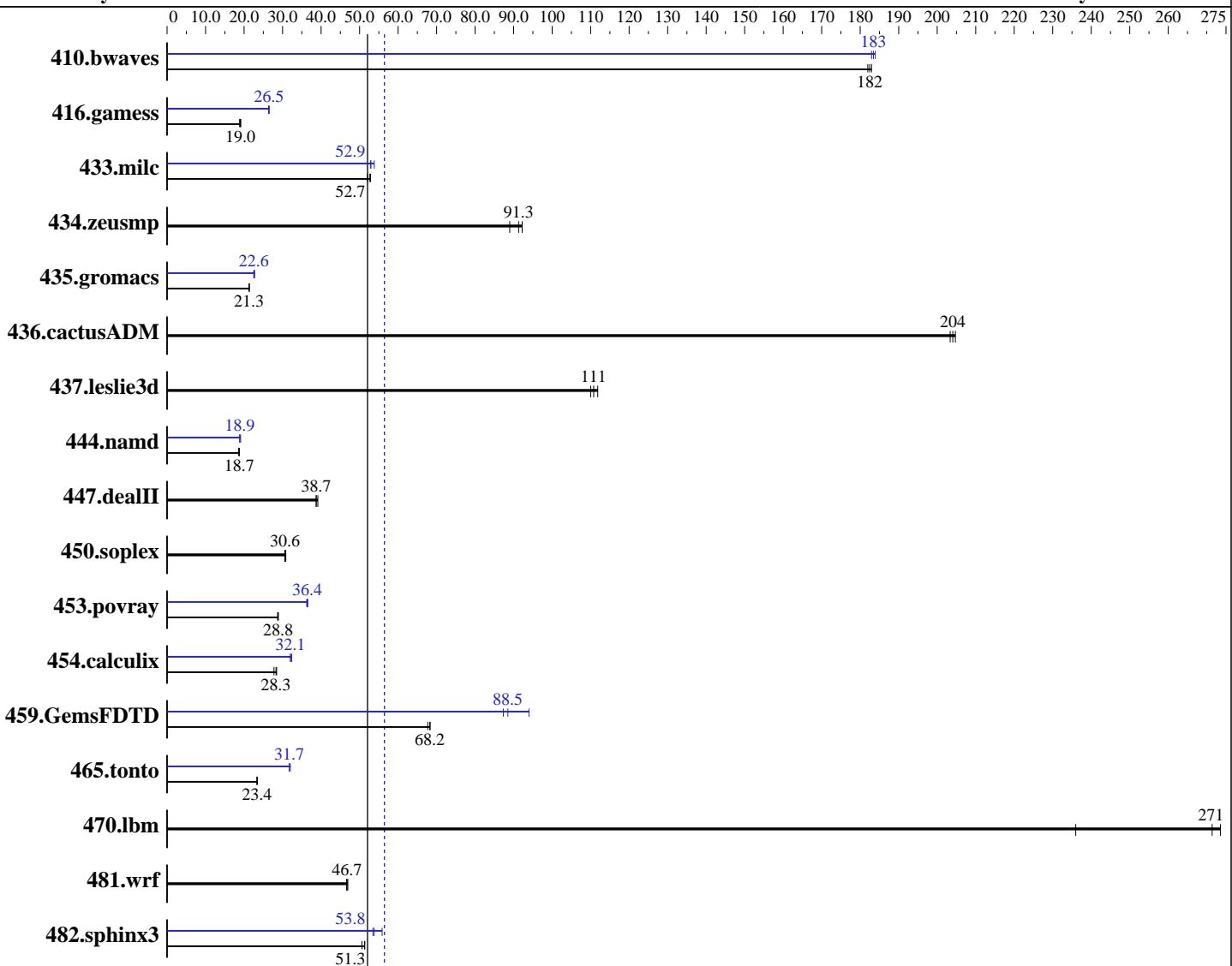
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Jul-2011

Hardware Availability: Mar-2009

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon X5560
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
Auto Parallel:
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 146 GB 10000 RPM SAS
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	74.3	183	74.5	182	74.7	182	74.3	183	74.1	183	73.9	184
416.gamess	1029	19.0	1039	18.8	1022	19.1	739	26.5	743	26.3	739	26.5
433.milc	174	52.8	176	52.1	174	52.7	173	52.9	171	53.8	174	52.9
434.zeusmp	98.7	92.2	99.7	91.3	102	89.0	98.7	92.2	99.7	91.3	102	89.0
435.gromacs	336	21.3	334	21.4	336	21.3	317	22.5	314	22.8	317	22.6
436.cactusADM	58.4	205	58.6	204	58.8	203	58.4	205	58.6	204	58.8	203
437.leslie3d	84.8	111	85.4	110	84.0	112	84.8	111	85.4	110	84.0	112
444.namd	428	18.7	432	18.5	427	18.8	425	18.9	420	19.1	425	18.9
447.dealII	295	38.7	295	38.7	292	39.2	295	38.7	295	38.7	292	39.2
450.soplex	272	30.6	270	30.8	273	30.6	272	30.6	270	30.8	273	30.6
453.povray	184	28.9	185	28.7	184	28.8	146	36.4	147	36.3	145	36.6
454.calculix	297	27.8	290	28.5	292	28.3	255	32.4	257	32.0	257	32.1
459.GemsFDTD	157	67.7	156	68.2	155	68.3	113	94.0	120	88.5	121	87.3
465.tonto	422	23.3	421	23.4	420	23.4	307	32.0	310	31.7	310	31.7
470.lbm	50.2	274	50.6	271	58.2	236	50.2	274	50.6	271	58.2	236
481.wrf	240	46.6	239	46.7	238	46.9	240	46.6	239	46.7	238	46.9
482.sphinx3	380	51.3	380	51.3	385	50.6	349	55.8	362	53.8	365	53.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:

Power Management = Maximum Performance (Default = Active Power Controller)
 Logical Processor = Disabled (Default = Enabled)



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General Notes

OMP_NUM_THREADS set to number of cores
Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
 -ansi-alias

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```
453.povray: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias
            -B /usr/share/libhugetlbfsl -Wl,-melf_x86_64 -Wl,-hugetlbfsl-link=BDT
```

Fortran benchmarks:

```
410.bwaves: -xsse4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
            -static
```

```
416.gamess: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
            -inline-level=0 -scalar-rep -static
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
            -inline-level=0 -opt-prefetch -parallel
            -B /usr/share/libhugetlbfsl -Wl,-melf_x86_64 -Wl,-hugetlbfsl-link=BDT
```

```
465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll14
            -B /usr/share/libhugetlbfsl -Wl,-melf_x86_64 -Wl,-hugetlbfsl-link=BDT
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
            -ansi-alias
```

436.cactusADM: basepeak = yes

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>



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