



SPEC® CFP2006 Result

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Intel Corporation

SPECfp®2006 = 32.6

Intel DH57JG Motherboard (Intel Core i3-550)

SPECfp_base2006 = 31.7

CPU2006 license: 13

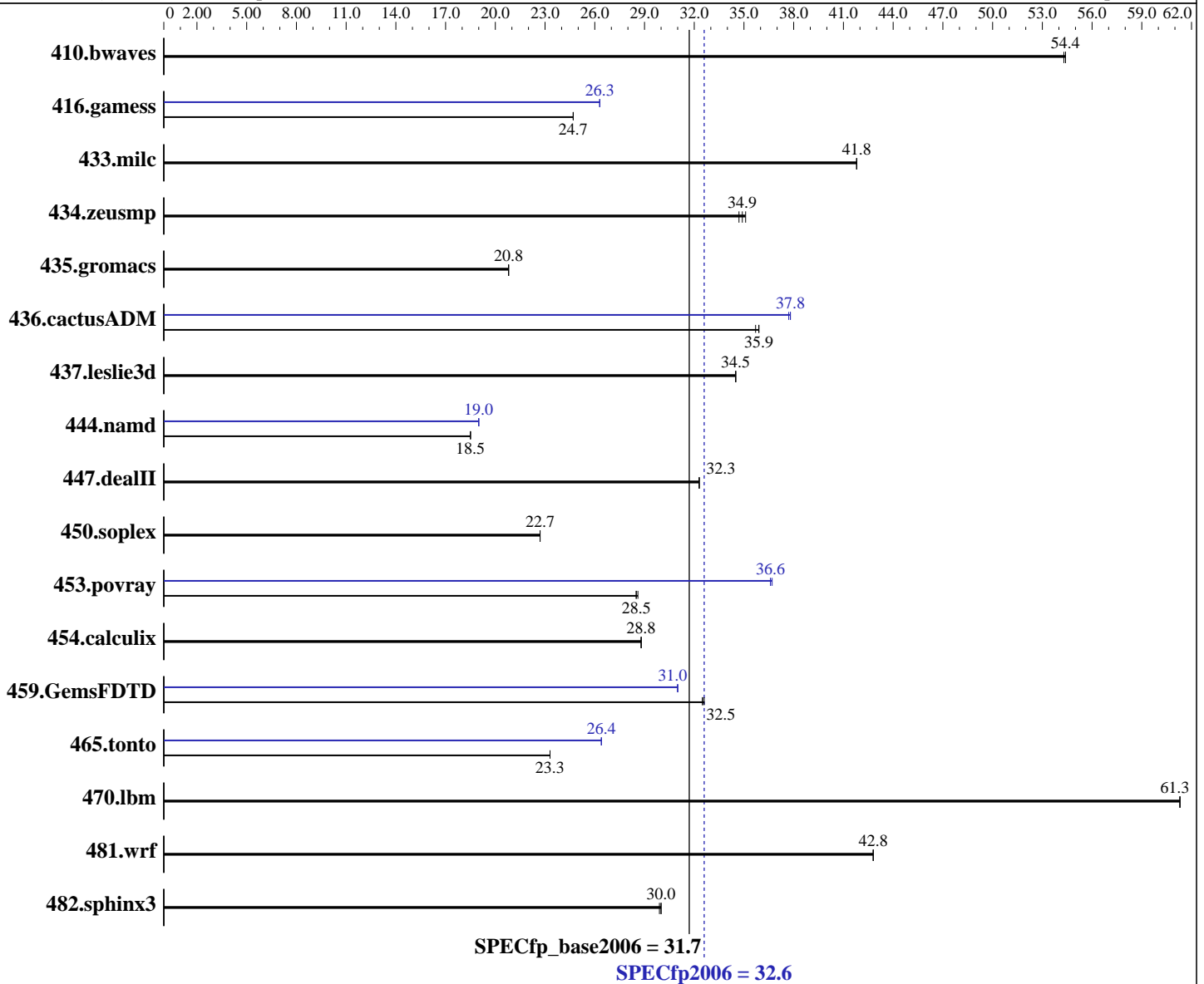
Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: May-2010

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i3-550
 CPU Characteristics:
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel64
 Version 12.0.3.176 Build 20110309
 Intel Visual Fortran Compiler XE for Intel64
 Version 12.0.3.176 Build 20110309
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)

Auto Parallel: Yes
 File System: NTFS

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 250 | 54.3 | 250 | 54.4 | 250 | 54.4 | 250 | 54.3 | 250 | 54.4 | 250 | 54.4 |
| 416.gamess | 794 | 24.7 | 794 | 24.7 | 794 | 24.7 | 746 | 26.3 | 745 | 26.3 | 746 | 26.3 |
| 433.milc | 220 | 41.8 | 220 | 41.8 | 220 | 41.8 | 220 | 41.8 | 220 | 41.8 | 220 | 41.8 |
| 434.zeusmp | 260 | 34.9 | 262 | 34.7 | 260 | 35.1 | 260 | 34.9 | 262 | 34.7 | 260 | 35.1 |
| 435.gromacs | 343 | 20.8 | 343 | 20.8 | 343 | 20.8 | 343 | 20.8 | 343 | 20.8 | 343 | 20.8 |
| 436.cactusADM | 333 | 35.9 | 333 | 35.9 | 335 | 35.7 | 316 | 37.8 | 316 | 37.8 | 317 | 37.7 |
| 437.leslie3d | 273 | 34.5 | 273 | 34.5 | 273 | 34.5 | 273 | 34.5 | 273 | 34.5 | 273 | 34.5 |
| 444.namd | 433 | 18.5 | 433 | 18.5 | 433 | 18.5 | 422 | 19.0 | 423 | 19.0 | 423 | 19.0 |
| 447.dealII | 354 | 32.3 | 354 | 32.3 | 354 | 32.3 | 354 | 32.3 | 354 | 32.3 | 354 | 32.3 |
| 450.soplex | 367 | 22.7 | 367 | 22.7 | 367 | 22.7 | 367 | 22.7 | 367 | 22.7 | 367 | 22.7 |
| 453.povray | 186 | 28.5 | 186 | 28.6 | 186 | 28.5 | 145 | 36.7 | 145 | 36.6 | 145 | 36.6 |
| 454.calculix | 286 | 28.8 | 287 | 28.8 | 286 | 28.8 | 286 | 28.8 | 287 | 28.8 | 286 | 28.8 |
| 459.GemsFDTD | 326 | 32.6 | 326 | 32.5 | 326 | 32.5 | 342 | 31.0 | 342 | 31.0 | 343 | 31.0 |
| 465.tonto | 422 | 23.3 | 422 | 23.3 | 422 | 23.3 | 373 | 26.4 | 373 | 26.4 | 373 | 26.4 |
| 470.lbm | 224 | 61.3 | 224 | 61.3 | 224 | 61.3 | 224 | 61.3 | 224 | 61.3 | 224 | 61.3 |
| 481.wrf | 261 | 42.8 | 261 | 42.8 | 261 | 42.8 | 261 | 42.8 | 261 | 42.8 | 261 | 42.8 |
| 482.sphinx3 | 653 | 29.9 | 650 | 30.0 | 651 | 30.0 | 653 | 29.9 | 650 | 30.0 | 651 | 30.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
 416.gamess: -DSPEC_CPU_P64
 433.milc: -DSPEC_CPU_P64
 434.zeusmp: -DSPEC_CPU_P64
 435.gromacs: -DSPEC_CPU_P64
 436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
 437.leslie3d: -DSPEC_CPU_P64
 444.namd: -DSPEC_CPU_P64 /TP
 447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 450.soplex: -DSPEC_CPU_P64
 453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
 459.GemsFDTD: -DSPEC_CPU_P64
 465.tonto: -DSPEC_CPU_P64
 470.lbm: -DSPEC_CPU_P64
 481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qauto-ilp32 /F1000000000

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qcxx-features -Qauto-ilp32 /F1000000000 shlw64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qauto-ilp32 /F1000000000



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Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
sh1W64M.lib -link /FORCE:MULTIPLE

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias
-Qscalar-rep- /F1000000000

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel
/F1000000000

465.tonto: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc
/F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2
-Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml>

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For other inquiries, please contact webmaster@spec.org.

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