SPEC® CFP2006 Result

ASUSTeK Computer Inc.

(Test Sponsor: Intel Corporation)

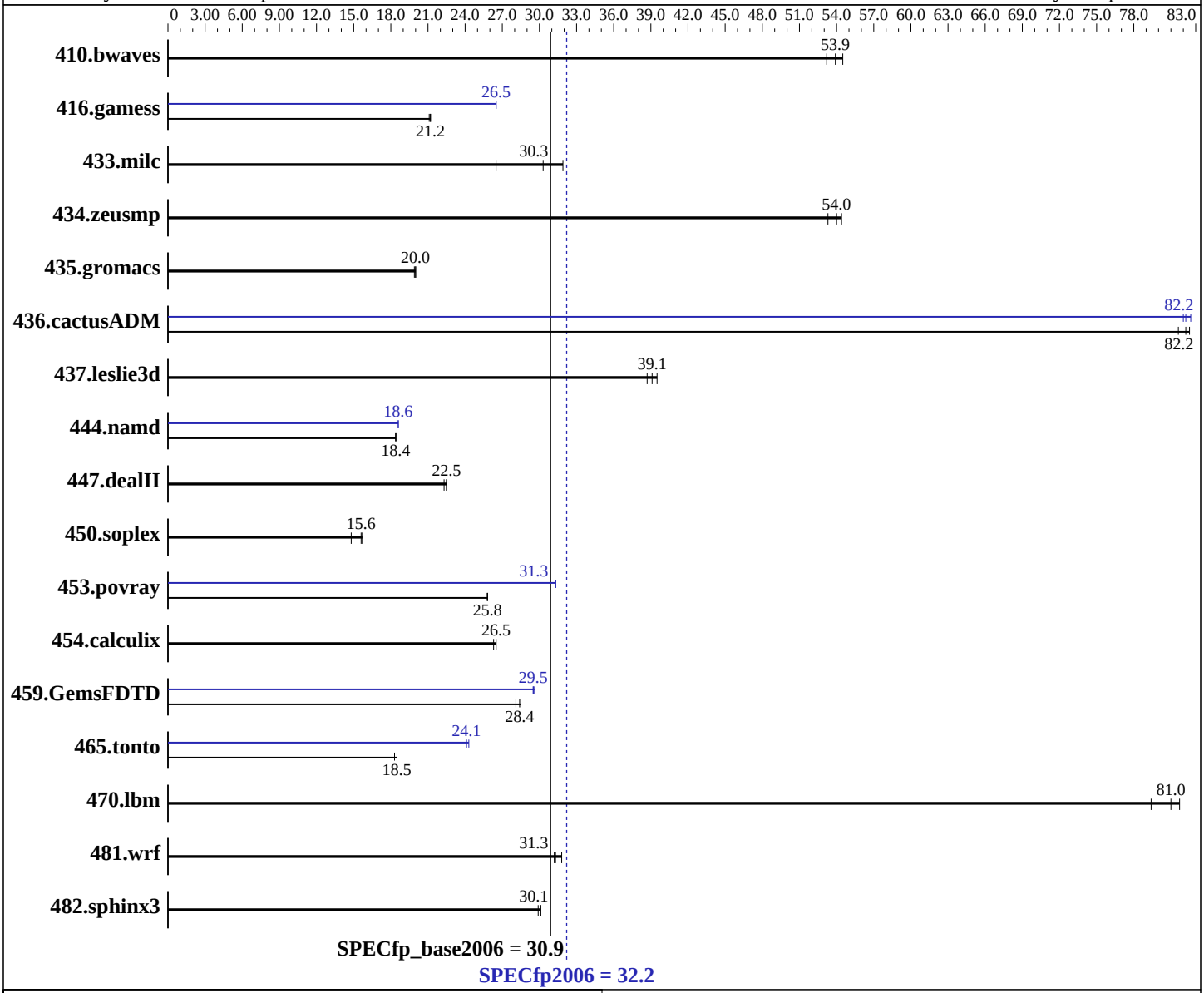
ASUSTek M4A89GTD PRO/USB3 (Phenom II X6 1075T)

SPECfp®2006 = 32.2

SPECfp_base2006 = 30.9

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2011
Hardware Availability: Feb-2011
Software Availability: Apr-2011



Hardware

CPU Name:	AMD Phenom II X6 1075T
CPU Characteristics:	AMD Turbo CORE technology up to 3.5 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

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Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Intel Visual Fortran Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx4 PC3-10600U-9)
Disk Subsystem: 1 TB Seagate SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	255	53.2	**252**	**53.9**	249	54.5	255	53.2	**252**	**53.9**	249	54.5
416.gamess	927	21.1	**925**	**21.2**	924	21.2	740	26.5	**740**	**26.5**	739	26.5
433.milc	347	26.5	**303**	**30.3**	288	31.9	347	26.5	**303**	**30.3**	288	31.9
434.zeusmp	171	53.3	**168**	**54.0**	167	54.4	171	53.3	**168**	**54.0**	167	54.4
435.gromacs	358	19.9	**358**	**20.0**	357	20.0	358	19.9	**358**	**20.0**	357	20.0
436.cactusADM	146	81.6	**145**	**82.2**	145	82.5	146	82.0	**145**	**82.2**	145	82.6
437.leslie3d	243	38.7	**240**	**39.1**	238	39.5	243	38.7	**240**	**39.1**	238	39.5
444.namd	437	18.4	437	18.4	**437**	**18.4**	433	18.5	**432**	**18.6**	432	18.6
447.dealII	513	22.3	**509**	**22.5**	508	22.5	513	22.3	**509**	**22.5**	508	22.5
450.soplex	563	14.8	**536**	**15.6**	531	15.7	563	14.8	**536**	**15.6**	531	15.7
453.povray	206	25.8	**206**	**25.8**	206	25.8	170	31.3	**170**	**31.3**	170	31.3
454.calculix	313	26.3	**312**	**26.5**	311	26.5	313	26.3	**312**	**26.5**	311	26.5
459.GemsFDTD	377	28.1	**373**	**28.4**	373	28.5	360	29.5	**360**	**29.5**	359	29.6
465.tonto	536	18.3	532	18.5	**533**	**18.5**	**408**	**24.1**	408	24.1	405	24.3
470.lbm	173	79.4	**170**	**81.0**	168	81.7	173	79.4	**170**	**81.0**	168	81.7
481.wrf	358	31.2	351	31.8	**357**	**31.3**	358	31.2	351	31.8	**357**	**31.3**
482.sphinx3	**648**	**30.1**	647	30.1	651	29.9	**648**	**30.1**	647	30.1	651	29.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

C++ benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qcxx-features -Qauto-ilp32 -Qprec-div- /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
shlW64M.lib -link /FORCE:MULTIPLE

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias
-Qscalar-rep- /F1000000000

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel /F1000000000

465.tonto: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2 -Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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