SPEC® CFP2006 Result

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ASUSTeK Computer Inc.

(Test Sponsor: Intel Corporation)

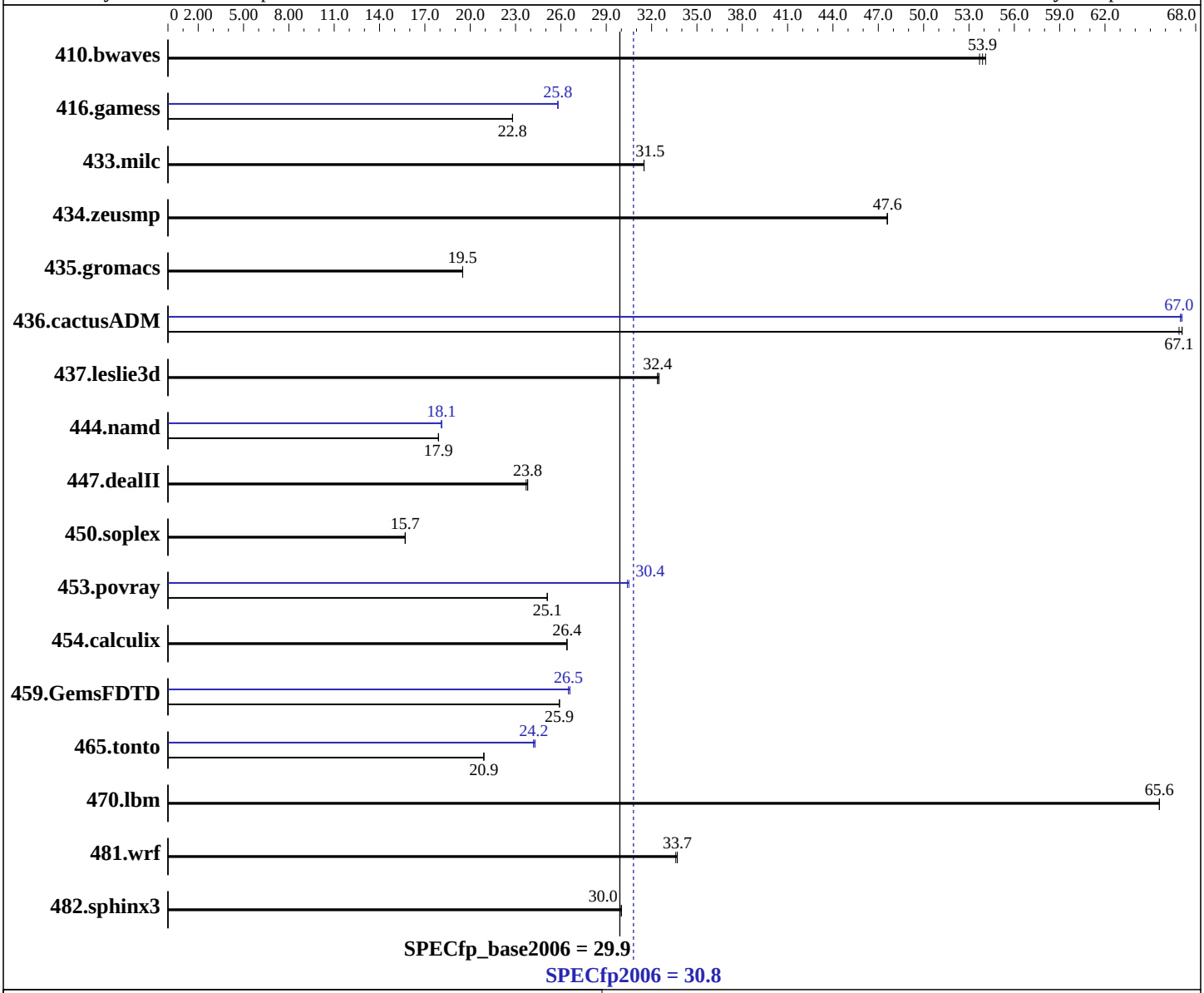
ASUSTek M4A89GTD PRO/USB3 (Phenom II X4 965)

SPECfp®2006 = 30.8

SPECfp_base2006 = 29.9

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2011
Hardware Availability: Feb-2011
Software Availability: Apr-2011



Hardware

CPU Name:	AMD Phenom II X4 965
CPU Characteristics:	
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

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Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Intel Visual Fortran Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache:	6 MB I+D on chip per chip	System State:	Default
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	4 GB (2 x 2 GB 2Rx4 PC3-10600U-9)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 TB Seagate SATA, 7200 RPM	Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	253	53.7	251	54.1	**252**	**53.9**	253	53.7	251	54.1	**252**	**53.9**
416.gamess	**860**	**22.8**	859	22.8	860	22.8	758	25.8	759	25.8	**759**	**25.8**
433.milc	291	31.5	**291**	**31.5**	291	31.5	291	31.5	**291**	**31.5**	291	31.5
434.zeusmp	191	47.6	**191**	**47.6**	191	47.6	191	47.6	**191**	**47.6**	191	47.6
435.gromacs	**366**	**19.5**	366	19.5	366	19.5	**366**	**19.5**	366	19.5	366	19.5
436.cactusADM	178	67.1	**178**	**67.1**	179	66.9	**178**	**67.0**	178	67.0	178	67.1
437.leslie3d	290	32.5	290	32.4	**290**	**32.4**	290	32.5	290	32.4	**290**	**32.4**
444.namd	448	17.9	448	17.9	**448**	**17.9**	444	18.1	443	18.1	**443**	**18.1**
447.dealII	480	23.8	482	23.7	**480**	**23.8**	480	23.8	482	23.7	**480**	**23.8**
450.soplex	**531**	**15.7**	531	15.7	531	15.7	**531**	**15.7**	531	15.7	531	15.7
453.povray	212	25.1	212	25.1	**212**	**25.1**	175	30.5	175	30.4	**175**	**30.4**
454.calculix	313	26.4	**313**	**26.4**	313	26.4	313	26.4	**313**	**26.4**	313	26.4
459.GemsFDTD	**410**	**25.9**	410	25.9	409	25.9	401	26.5	399	26.6	**400**	**26.5**
465.tonto	470	20.9	**470**	**20.9**	470	20.9	406	24.2	406	24.3	**406**	**24.2**
470.lbm	**209**	**65.6**	209	65.6	209	65.6	**209**	**65.6**	209	65.6	209	65.6
481.wrf	332	33.6	332	33.7	**332**	**33.7**	332	33.6	332	33.7	**332**	**33.7**
482.sphinx3	650	30.0	**651**	**30.0**	651	30.0	650	30.0	**651**	**30.0**	651	30.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

C++ benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qcxx-features -Qauto-ilp32 -Qprec-div- /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:
/arch:SSE3 -Qipo -O3 -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `/arch:SSE3`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000`
`shlW64M.lib -link /FORCE:MULTIPLE`

447.dealII: `basepeak = yes`

450.soplex: `basepeak = yes`

453.povray: `/arch:SSE3`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32`
`/F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `/arch:SSE3`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias`
`-Qscalar-rep- /F1000000000`

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel /F1000000000

465.tonto: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2 -Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml

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For other inquiries, please contact webmaster@spec.org.

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