



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®2006 = 56.8

PowerEdge R510 (Intel Xeon X5660, 2.80 GHz)

SPECfp_base2006 = 55.2

CPU2006 license: 55

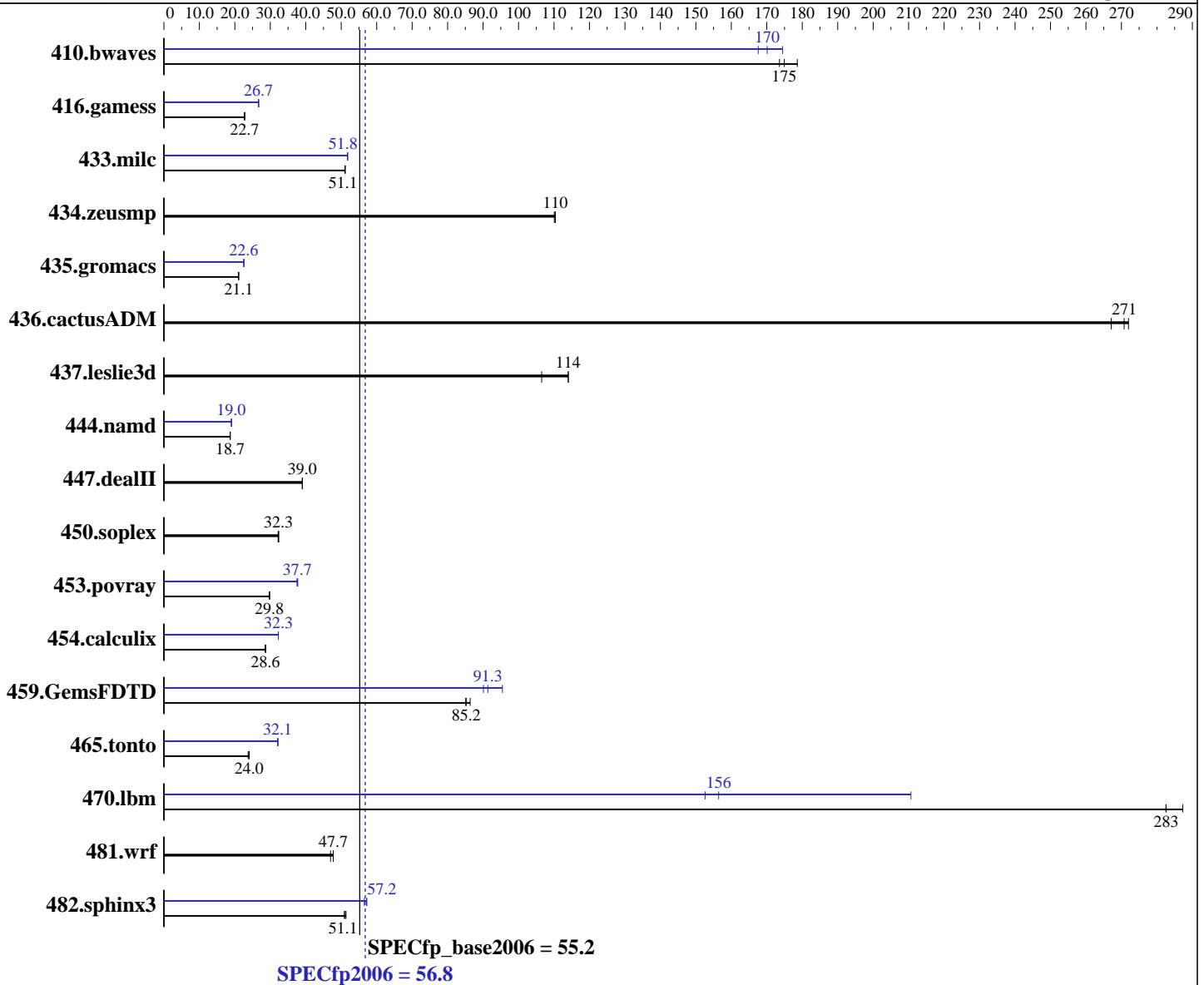
Test date: Jun-2011

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon X5660
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 2 x 146 GB 15000 RPM SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	78.3	174	<u>77.7</u>	<u>175</u>	76.1	179	<u>79.9</u>	<u>170</u>	81.1	168	77.9	174
416.gamess	859	22.8	862	22.7	<u>861</u>	<u>22.7</u>	<u>733</u>	<u>26.7</u>	735	26.6	733	26.7
433.milc	180	51.1	180	51.1	<u>180</u>	<u>51.1</u>	<u>177</u>	<u>51.8</u>	177	51.8	177	51.8
434.zeusmp	82.7	110	82.5	110	<u>82.5</u>	<u>110</u>	82.7	110	82.5	110	<u>82.5</u>	<u>110</u>
435.gromacs	338	21.1	<u>339</u>	<u>21.1</u>	340	21.0	316	22.6	<u>316</u>	<u>22.6</u>	317	22.5
436.cactusADM	<u>44.1</u>	<u>271</u>	43.9	272	44.7	267	<u>44.1</u>	<u>271</u>	43.9	272	44.7	267
437.leslie3d	88.2	107	<u>82.4</u>	<u>114</u>	82.4	114	88.2	107	<u>82.4</u>	<u>114</u>	82.4	114
444.namd	430	18.7	429	18.7	<u>429</u>	<u>18.7</u>	421	19.0	<u>421</u>	<u>19.0</u>	422	19.0
447.dealII	293	39.0	293	39.0	<u>293</u>	<u>39.0</u>	293	39.0	293	39.0	<u>293</u>	<u>39.0</u>
450.soplex	<u>258</u>	<u>32.3</u>	258	32.4	259	32.2	<u>258</u>	<u>32.3</u>	258	32.4	259	32.2
453.povray	<u>179</u>	<u>29.8</u>	179	29.7	178	29.8	142	37.5	141	37.7	<u>141</u>	<u>37.7</u>
454.calculix	<u>288</u>	<u>28.6</u>	289	28.6	287	28.7	256	32.3	<u>256</u>	<u>32.3</u>	256	32.3
459.GemsFDTD	125	85.1	<u>124</u>	<u>85.2</u>	123	86.4	118	90.1	111	95.5	<u>116</u>	<u>91.3</u>
465.tonto	415	23.7	409	24.1	<u>410</u>	<u>24.0</u>	306	32.1	307	32.1	<u>307</u>	<u>32.1</u>
470.lbm	47.8	287	48.6	283	<u>48.6</u>	<u>283</u>	<u>87.8</u>	<u>156</u>	90.0	153	65.2	211
481.wrf	234	47.7	238	47.0	<u>234</u>	<u>47.7</u>	234	47.7	238	47.0	<u>234</u>	<u>47.7</u>
482.sphinx3	383	50.9	<u>381</u>	<u>51.1</u>	380	51.4	341	57.2	<u>341</u>	<u>57.2</u>	345	56.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

```
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:

Power Management = Maximum Performance (Default = Active Power Controller)

Data Reuse = Disabled (Default = Enabled)

Logical Processor = Disabled (Default = Enabled)



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General Notes

OMP_NUM_THREADS set to number of cores
Binaries were compiled on RHEL5.5
The Dell PowerEdge R510 and
the Bull NovaScale R450 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R510 model.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -parallel
-ansi-alias -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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