SPEC® CFP2006 Result

Fujitsu

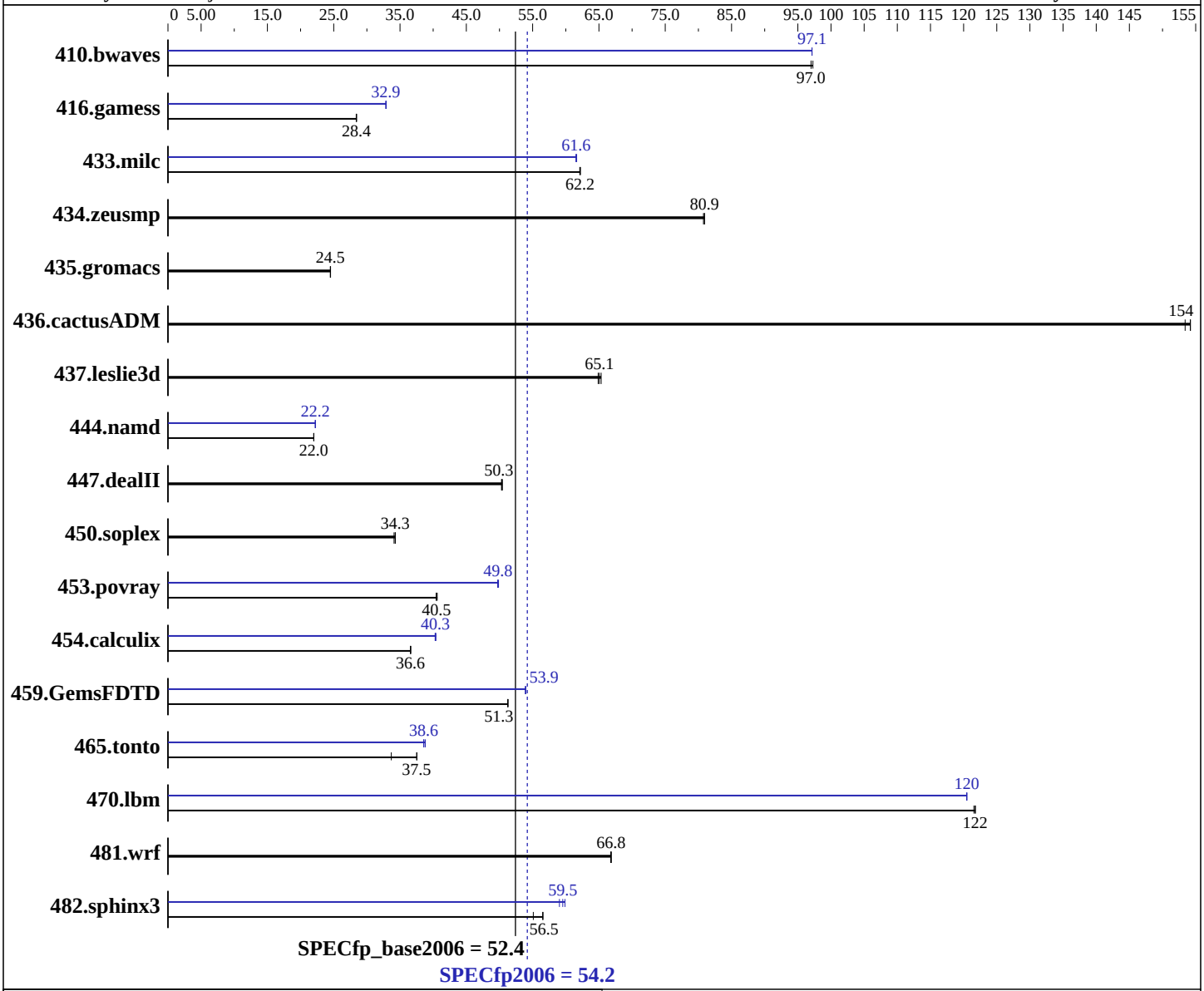
CELSIUS W410, Intel Core i5-2400

SPECfp®2006 = 54.2

SPECfp_base2006 = 52.4

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jun-2011
Hardware Availability: Mar-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Core i5-2400
CPU Characteristics:	Intel Turbo Boost Technology up to 3.4 GHz
CPU MHz:	3100
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.2.137 Build 20110112
Auto Parallel:	Yes
File System:	ext3
System State:	Run Level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	140	97.0	**140**	**97.0**	140	97.3	140	97.1	**140**	**97.1**	140	97.1
416.gamess	690	28.4	**689**	**28.4**	688	28.4	596	32.9	595	32.9	**596**	**32.9**
433.milc	148	62.1	148	62.2	**148**	**62.2**	**149**	**61.6**	149	61.6	149	61.5
434.zeusmp	113	80.8	**112**	**80.9**	112	80.9	113	80.8	**112**	**80.9**	112	80.9
435.gromacs	**291**	**24.5**	292	24.5	291	24.5	**291**	**24.5**	292	24.5	291	24.5
436.cactusADM	77.5	154	**77.5**	**154**	77.9	153	77.5	154	**77.5**	**154**	77.9	153
437.leslie3d	144	65.3	145	64.9	**144**	**65.1**	144	65.3	145	64.9	**144**	**65.1**
444.namd	365	22.0	365	22.0	**365**	**22.0**	**361**	**22.2**	361	22.2	361	22.2
447.dealII	227	50.3	227	50.4	**227**	**50.3**	227	50.3	227	50.4	**227**	**50.3**
450.soplex	245	34.1	243	34.3	**243**	**34.3**	245	34.1	243	34.3	**243**	**34.3**
453.povray	131	40.6	**131**	**40.5**	132	40.5	107	49.9	**107**	**49.8**	107	49.7
454.calculix	226	36.6	225	36.6	**225**	**36.6**	204	40.4	**205**	**40.3**	205	40.3
459.GemsFDTD	**207**	**51.3**	207	51.3	207	51.2	197	54.0	**197**	**53.9**	197	53.9
465.tonto	292	33.7	**262**	**37.5**	262	37.5	255	38.6	254	38.8	**255**	**38.6**
470.lbm	113	122	**113**	**122**	113	122	114	120	**114**	**120**	114	120
481.wrf	167	66.8	167	66.8	**167**	**66.8**	167	66.8	167	66.8	**167**	**66.8**
482.sphinx3	**345**	**56.5**	345	56.6	354	55.1	326	59.9	**327**	**59.5**	330	59.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 Hugepages were enabled by:
 mount -t hugetlbfs nodev /mnt/hugepages
 echo 900 > /proc/sys/vm/nr_hugepages
 export HUGETLB_MORECORE=yes
 export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

General Notes

```
OMP_NUM_THREADS set to number of cores
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -parallel -ansi-alias -static -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Fujitsu_CELSIUS_Platform.20110720.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Fujitsu_CELSIUS_Platform.20110720.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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