



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®2006 = 45.8

IBM BladeCenter HX5 (Intel Xeon E7-8867L)

SPECfp_base2006 = 42.4

CPU2006 license: 11

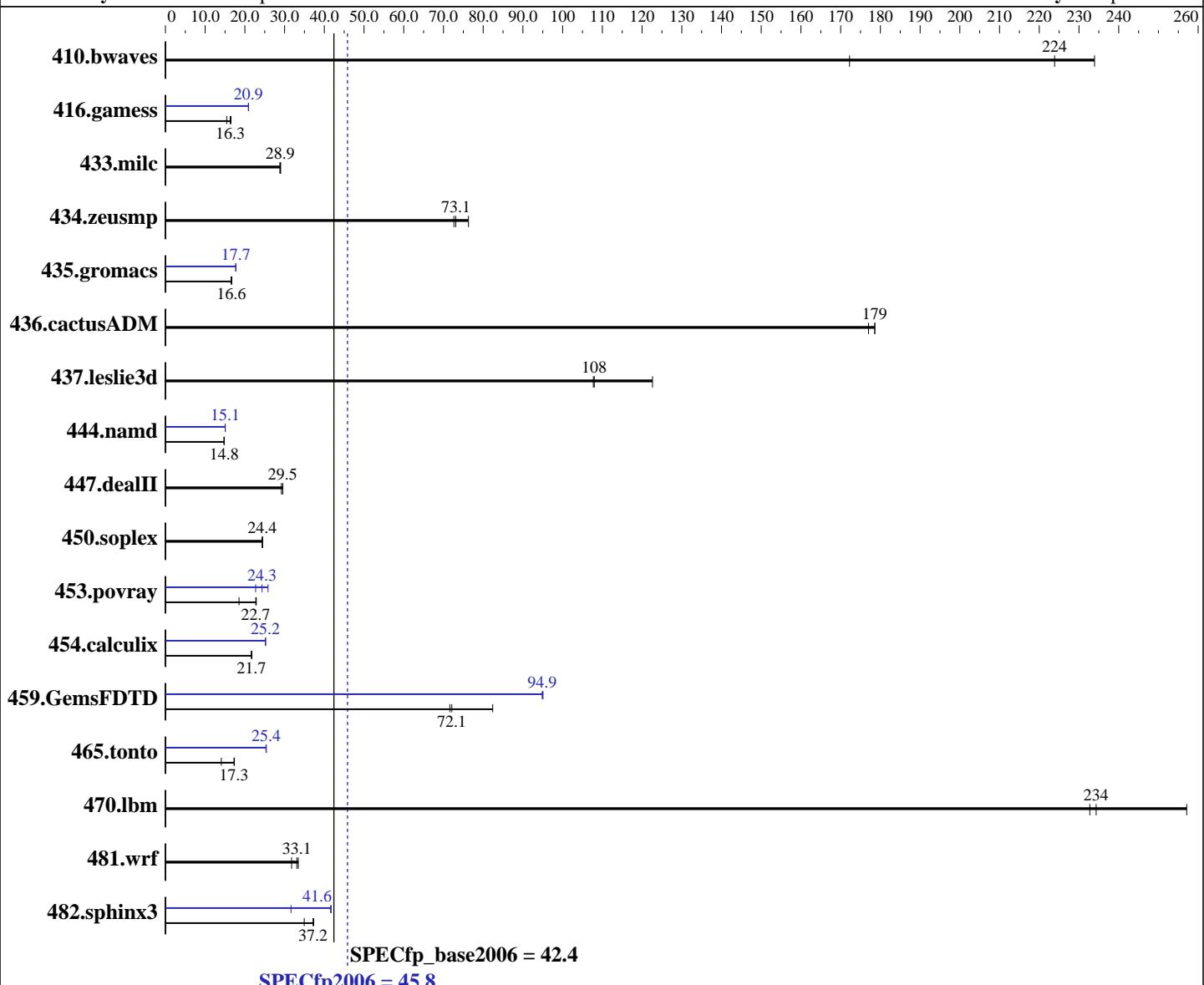
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: May-2011

Hardware Availability: May-2011

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon E7-8867L
CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 4Rx8 PC3-8500R-7, ECC)
 Disk Subsystem: 2 x 50 GB SSD, RAID 0
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio								
410.bwaves	60.7	224	58.1	234	78.9	172	60.7	224	58.1	234	78.9	172
416.gamess	1269	15.4	1187	16.5	1201	16.3	935	20.9	936	20.9	938	20.9
433.milc	318	28.9	316	29.0	318	28.8	318	28.9	316	29.0	318	28.8
434.zeusmp	119	76.3	124	73.1	125	72.6	119	76.3	124	73.1	125	72.6
435.gromacs	432	16.5	427	16.7	429	16.6	402	17.8	403	17.7	403	17.7
436.cactusADM	67.5	177	66.9	179	66.9	179	67.5	177	66.9	179	66.9	179
437.leslie3d	87.1	108	76.7	123	87.3	108	87.1	108	76.7	123	87.3	108
444.namd	543	14.8	542	14.8	543	14.8	532	15.1	533	15.0	533	15.1
447.dealII	392	29.2	387	29.6	387	29.5	392	29.2	387	29.6	387	29.5
450.soplex	341	24.4	342	24.4	342	24.4	341	24.4	342	24.4	342	24.4
453.povray	233	22.9	234	22.7	287	18.5	234	22.7	219	24.3	206	25.8
454.calculix	380	21.7	382	21.6	379	21.8	327	25.2	327	25.2	328	25.2
459.GemsFDTD	148	71.6	129	82.4	147	72.1	112	95.1	112	94.9	112	94.9
465.tonto	699	14.1	570	17.3	567	17.4	389	25.3	387	25.4	388	25.4
470.lbm	53.4	257	58.6	234	59.0	233	53.4	257	58.6	234	59.0	233
481.wrf	338	33.1	352	31.8	334	33.5	338	33.1	352	31.8	334	33.5
482.sphinx3	524	37.2	558	34.9	522	37.3	467	41.7	469	41.6	616	31.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 1 > /proc/sys/vm/zone_reclaim_mode
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
 Turbo Boost Power Optimization set to Traditional



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General Notes

OMP_NUM_THREADS set to number of cores
Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
 -ansi-alias

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

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Peak Optimization Flags (Continued)

450.soplex: basepeak = yes

```
453.povray: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias
             -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep- -static
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
                -inline-level=0 -opt-prefetch -parallel
                -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

```
465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
             -opt-malloc-options=3 -auto -unroll14
             -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
              -ansi-alias
```

436.cactusADM: basepeak = yes

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>



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