



# SPEC® CFP2006 Result

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## Bull SAS

SPECfp®2006 = 46.7

NovaScale R410B F2 (Intel Core i3-2100, 3.10 GHz)

SPECfp\_base2006 = 45.4

CPU2006 license: 20

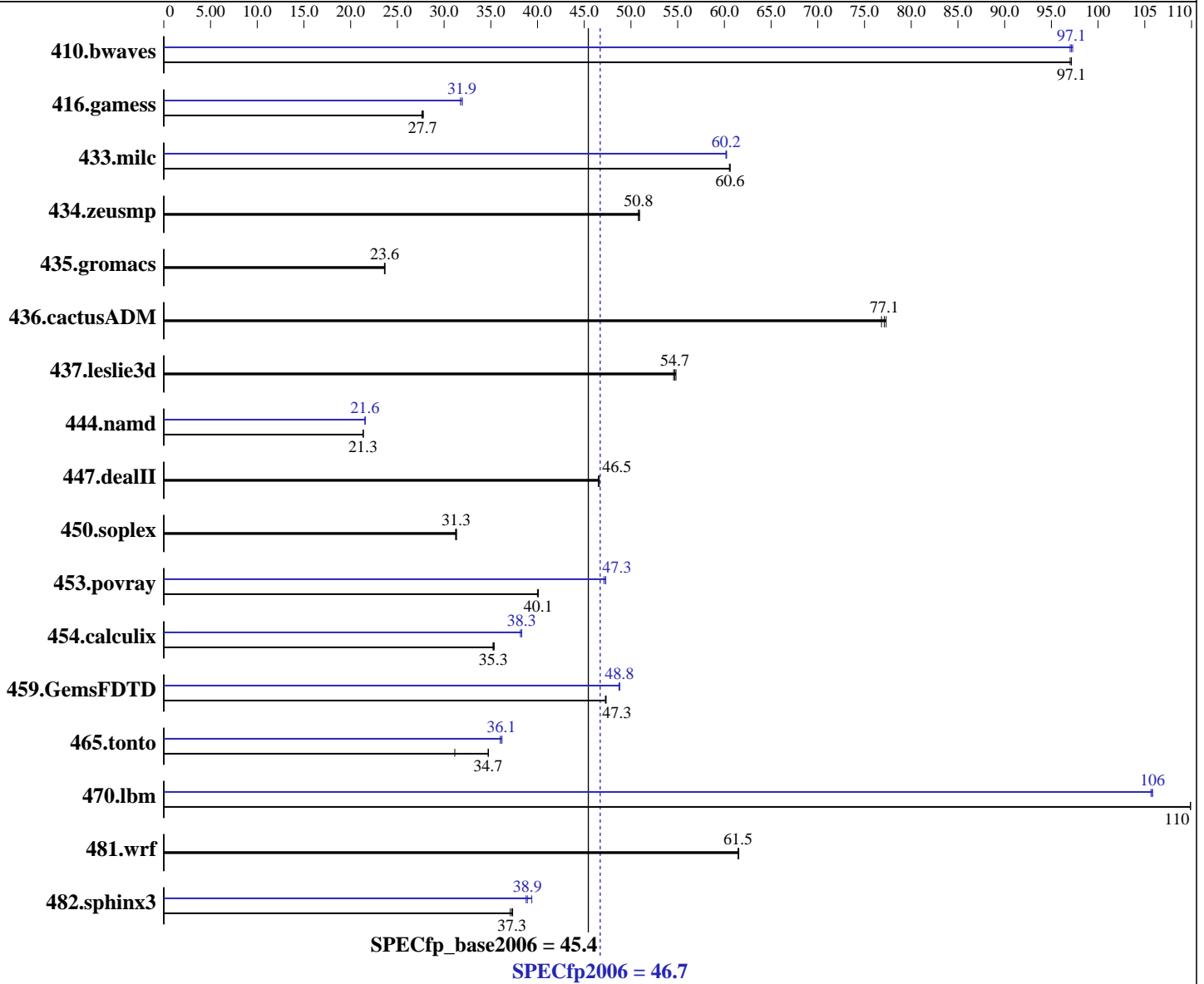
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Mar-2011

Hardware Availability: May-2011

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Core i3-2100  
 CPU Characteristics:  
 CPU MHz: 3100  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12 Alpha Build 20110105  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 3 MB I+D on chip per chip  
Other Cache: None  
Memory: 8 GB (4 x 2 GB 2Rx8 PC3-10600R-9, ECC)  
Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	140	97.0	140	97.1	<b>140</b>	<b>97.1</b>	140	97.0	140	97.3	<b>140</b>	<b>97.1</b>
416.gamess	709	27.6	<b>707</b>	<b>27.7</b>	705	27.8	613	31.9	617	31.7	<b>613</b>	<b>31.9</b>
433.milc	152	60.5	<b>151</b>	<b>60.6</b>	151	60.6	153	60.2	<b>153</b>	<b>60.2</b>	152	60.2
434.zeusmp	179	50.8	<b>179</b>	<b>50.8</b>	179	50.9	179	50.8	<b>179</b>	<b>50.8</b>	179	50.9
435.gromacs	302	23.6	<b>302</b>	<b>23.6</b>	302	23.7	302	23.6	<b>302</b>	<b>23.6</b>	302	23.7
436.cactusADM	155	77.3	<b>155</b>	<b>77.1</b>	156	76.8	155	77.3	<b>155</b>	<b>77.1</b>	156	76.8
437.leslie3d	<b>172</b>	<b>54.7</b>	171	54.8	172	54.6	<b>172</b>	<b>54.7</b>	171	54.8	172	54.6
444.namd	<b>376</b>	<b>21.3</b>	376	21.4	376	21.3	372	21.6	372	21.5	<b>372</b>	<b>21.6</b>
447.dealII	246	46.5	<b>246</b>	<b>46.5</b>	245	46.6	246	46.5	<b>246</b>	<b>46.5</b>	245	46.6
450.soplex	<b>267</b>	<b>31.3</b>	267	31.2	266	31.3	<b>267</b>	<b>31.3</b>	267	31.2	266	31.3
453.povray	133	40.1	<b>133</b>	<b>40.1</b>	133	40.0	<b>113</b>	<b>47.3</b>	112	47.3	113	47.1
454.calculix	233	35.4	<b>234</b>	<b>35.3</b>	234	35.2	216	38.2	<b>215</b>	<b>38.3</b>	215	38.3
459.GemsFDTD	224	47.3	<b>224</b>	<b>47.3</b>	224	47.3	<b>218</b>	<b>48.8</b>	218	48.7	217	48.8
465.tonto	<b>284</b>	<b>34.7</b>	316	31.1	283	34.8	272	36.2	273	36.0	<b>272</b>	<b>36.1</b>
470.lbm	125	110	125	110	<b>125</b>	<b>110</b>	<b>130</b>	<b>106</b>	130	106	130	106
481.wrf	182	61.5	181	61.5	<b>182</b>	<b>61.5</b>	182	61.5	181	61.5	<b>182</b>	<b>61.5</b>
482.sphinx3	522	37.3	526	37.1	<b>523</b>	<b>37.3</b>	495	39.4	<b>501</b>	<b>38.9</b>	503	38.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Logical Processor = Disabled (Default = Enabled)



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## General Notes

OMP\_NUM\_THREADS set to number of cores  
The Dell PowerEdge R210 II and the Bull NovaScale R410B F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R210 II model. Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -parallel  
-ansi-alias -static -auto-ilp32`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 18:12:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 7 June 2011.