



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®2006 = 45.7

IBM BladeCenter HX5 (Intel Xeon E7-2830)

SPECfp_base2006 = 42.4

CPU2006 license: 11

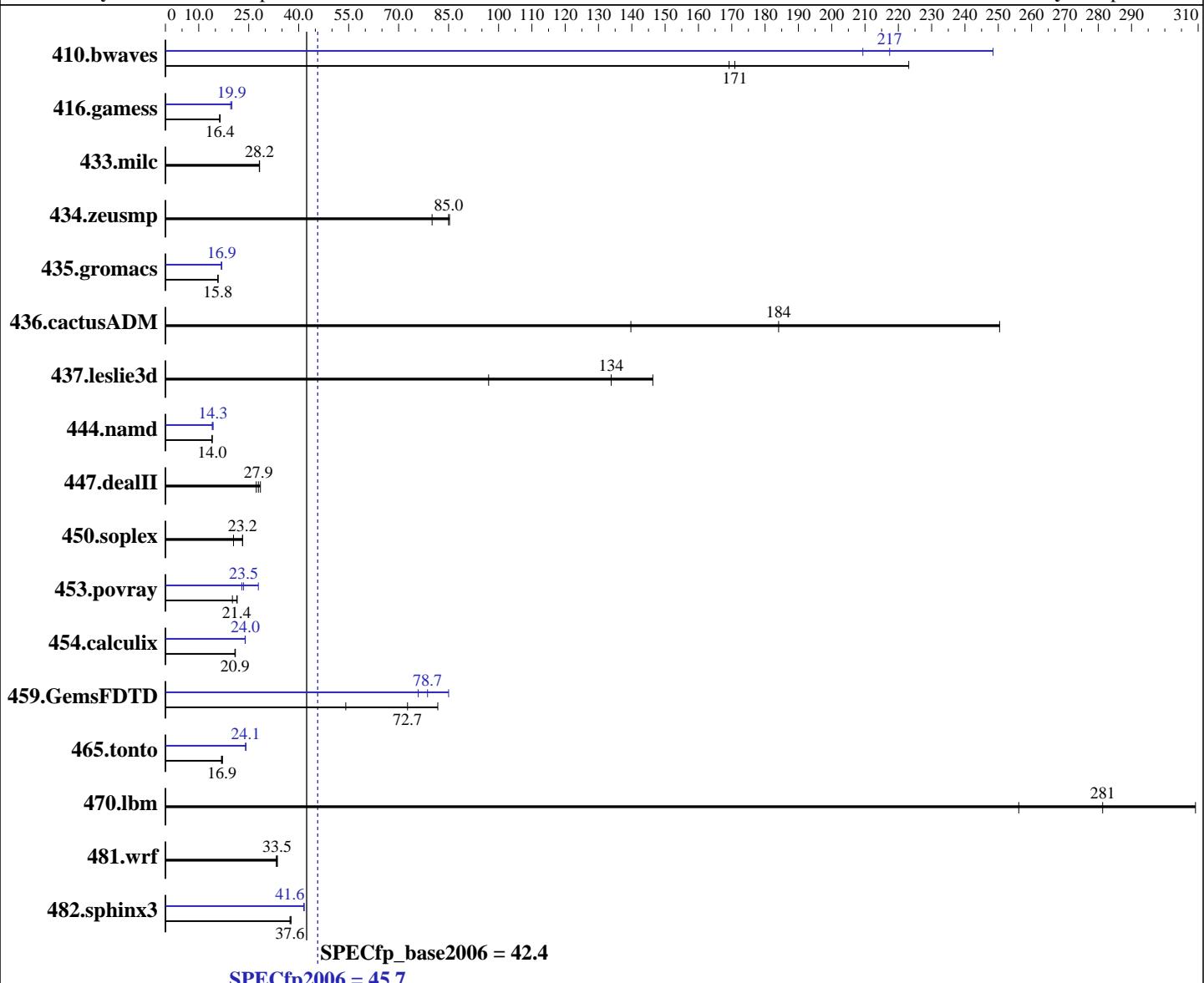
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2011

Hardware Availability: May-2011

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon E7-2830
CPU Characteristics: Intel Turbo Boost Technology up to 2.4 GHz
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 4Rx8 PC3-8500R-7, ECC)
 Disk Subsystem: 2 x 50 GB SSD, RAID 0
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 79.5 | 171 | 80.3 | 169 | 60.9 | 223 | 64.9 | 209 | 62.5 | 217 | 54.7 | 248 |
| 416.gamess | 1195 | 16.4 | 1204 | 16.3 | 1196 | 16.4 | 1000 | 19.6 | 984 | 19.9 | 983 | 19.9 |
| 433.milc | 326 | 28.2 | 325 | 28.3 | 325 | 28.2 | 326 | 28.2 | 325 | 28.3 | 325 | 28.2 |
| 434.zeusmp | 107 | 85.3 | 114 | 80.0 | 107 | 85.0 | 107 | 85.3 | 114 | 80.0 | 107 | 85.0 |
| 435.gromacs | 453 | 15.7 | 452 | 15.8 | 453 | 15.8 | 424 | 16.9 | 423 | 16.9 | 426 | 16.8 |
| 436.cactusADM | 85.5 | 140 | 64.9 | 184 | 47.7 | 250 | 85.5 | 140 | 64.9 | 184 | 47.7 | 250 |
| 437.leslie3d | 96.9 | 97.0 | 70.3 | 134 | 64.2 | 146 | 96.9 | 97.0 | 70.3 | 134 | 64.2 | 146 |
| 444.namd | 572 | 14.0 | 572 | 14.0 | 572 | 14.0 | 562 | 14.3 | 571 | 14.0 | 562 | 14.3 |
| 447.dealII | 410 | 27.9 | 401 | 28.5 | 420 | 27.3 | 410 | 27.9 | 401 | 28.5 | 420 | 27.3 |
| 450.soplex | 408 | 20.4 | 360 | 23.2 | 360 | 23.2 | 408 | 20.4 | 360 | 23.2 | 360 | 23.2 |
| 453.povray | 248 | 21.4 | 247 | 21.5 | 265 | 20.1 | 191 | 27.9 | 232 | 22.9 | 227 | 23.5 |
| 454.calculix | 393 | 21.0 | 395 | 20.9 | 395 | 20.9 | 344 | 24.0 | 345 | 23.9 | 344 | 24.0 |
| 459.GemsFDTD | 130 | 81.8 | 196 | 54.1 | 146 | 72.7 | 135 | 78.7 | 125 | 85.0 | 140 | 75.9 |
| 465.tonto | 585 | 16.8 | 581 | 16.9 | 574 | 17.1 | 408 | 24.1 | 408 | 24.1 | 408 | 24.1 |
| 470.lbm | 48.8 | 281 | 53.6 | 256 | 44.4 | 309 | 48.8 | 281 | 53.6 | 256 | 44.4 | 309 |
| 481.wrf | 333 | 33.5 | 336 | 33.3 | 333 | 33.6 | 333 | 33.5 | 336 | 33.3 | 333 | 33.6 |
| 482.sphinx3 | 521 | 37.4 | 517 | 37.7 | 518 | 37.6 | 469 | 41.6 | 469 | 41.6 | 468 | 41.6 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 1 > /proc/sys/vm/zone_reclaim_mode
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:

Turbo Boost Power Optimization set to Traditional



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| | | |
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General Notes

OMP_NUM_THREADS set to number of cores
Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

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Peak Optimization Flags (Continued)

450.soplex: basepeak = yes

```
453.povray: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias
             -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Fortran benchmarks:

```
410.bwaves: -xsSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
             -static
```

```
416.gamess: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep- -static
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
                -inline-level=0 -opt-prefetch -parallel
                -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

```
465.tonto: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
             -opt-malloc-options=3 -auto -unroll14
             -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
              -ansi-alias
```

436.cactusADM: basepeak = yes

454.calculix: -xsSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>



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