



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®2006 = 50.9

IBM System x3690 X5 (Intel Xeon E7-2870)

SPECfp_base2006 = 46.5

CPU2006 license: 11

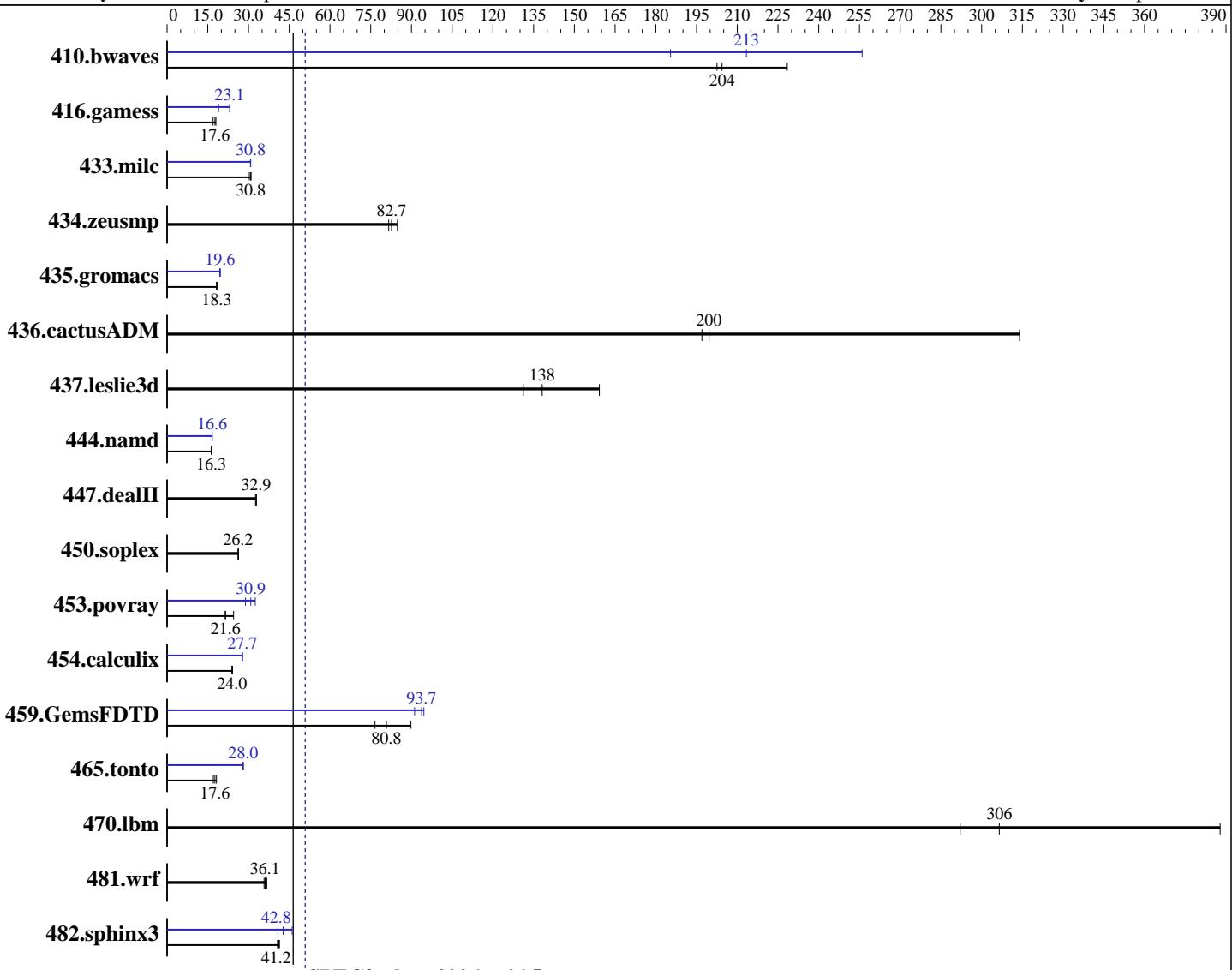
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Mar-2011

Hardware Availability: May-2011

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon E7-2870
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 4Rx8 PC3-8500R-7, ECC)
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	67.1	203	66.5	204	59.5	228	63.7	213	73.3	185	53.1	256
416.gamess	1155	17.0	1110	17.6	1088	18.0	845	23.2	849	23.1	1031	19.0
433.milc	303	30.3	298	30.8	297	30.9	299	30.7	298	30.8	298	30.8
434.zeusmp	107	84.8	111	81.6	110	82.7	107	84.8	111	81.6	110	82.7
435.gromacs	391	18.3	390	18.3	390	18.3	365	19.6	365	19.6	368	19.4
436.cactusADM	38.1	314	60.7	197	59.9	200	38.1	314	60.7	197	59.9	200
437.leslie3d	59.0	159	71.6	131	68.0	138	59.0	159	71.6	131	68.0	138
444.namd	492	16.3	492	16.3	492	16.3	483	16.6	483	16.6	483	16.6
447.dealII	348	32.9	348	32.9	351	32.6	348	32.9	348	32.9	351	32.6
450.soplex	317	26.3	319	26.2	318	26.2	317	26.3	319	26.2	318	26.2
453.povray	247	21.6	249	21.4	217	24.5	164	32.5	184	28.9	172	30.9
454.calculix	345	23.9	344	24.0	343	24.1	298	27.7	297	27.7	297	27.7
459.GemsFDTD	131	80.8	118	89.8	139	76.6	113	93.7	112	94.6	116	91.1
465.tonto	541	18.2	577	17.0	558	17.6	351	28.1	351	28.0	352	28.0
470.lbm	47.0	292	35.4	388	44.8	306	47.0	292	35.4	388	44.8	306
481.wrf	309	36.1	313	35.7	304	36.7	309	36.1	313	35.7	304	36.7
482.sphinx3	473	41.2	479	40.7	471	41.4	423	46.1	477	40.8	456	42.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

Load Default BIOS Settings and then change the following
 Turbo Boost set to Traditional



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General Notes

OMP_NUM_THREADS set to number of cores
Binaries compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias
 -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT

Fortran benchmarks:

410.bwaves: -xsSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
 -static

416.gamess: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
 -inline-level=0 -opt-prefetch -parallel
 -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT

465.tonto: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
 -opt-malloc-options=3 -auto -unroll14
 -B /usr/share/libhugetlbfss/ -Wl,-melf_x86_64 -Wl,-hugetlbfss-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
 -ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xsSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>



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