SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon E5603, 1.60 GHz

SPECint®_rate2006 =   125

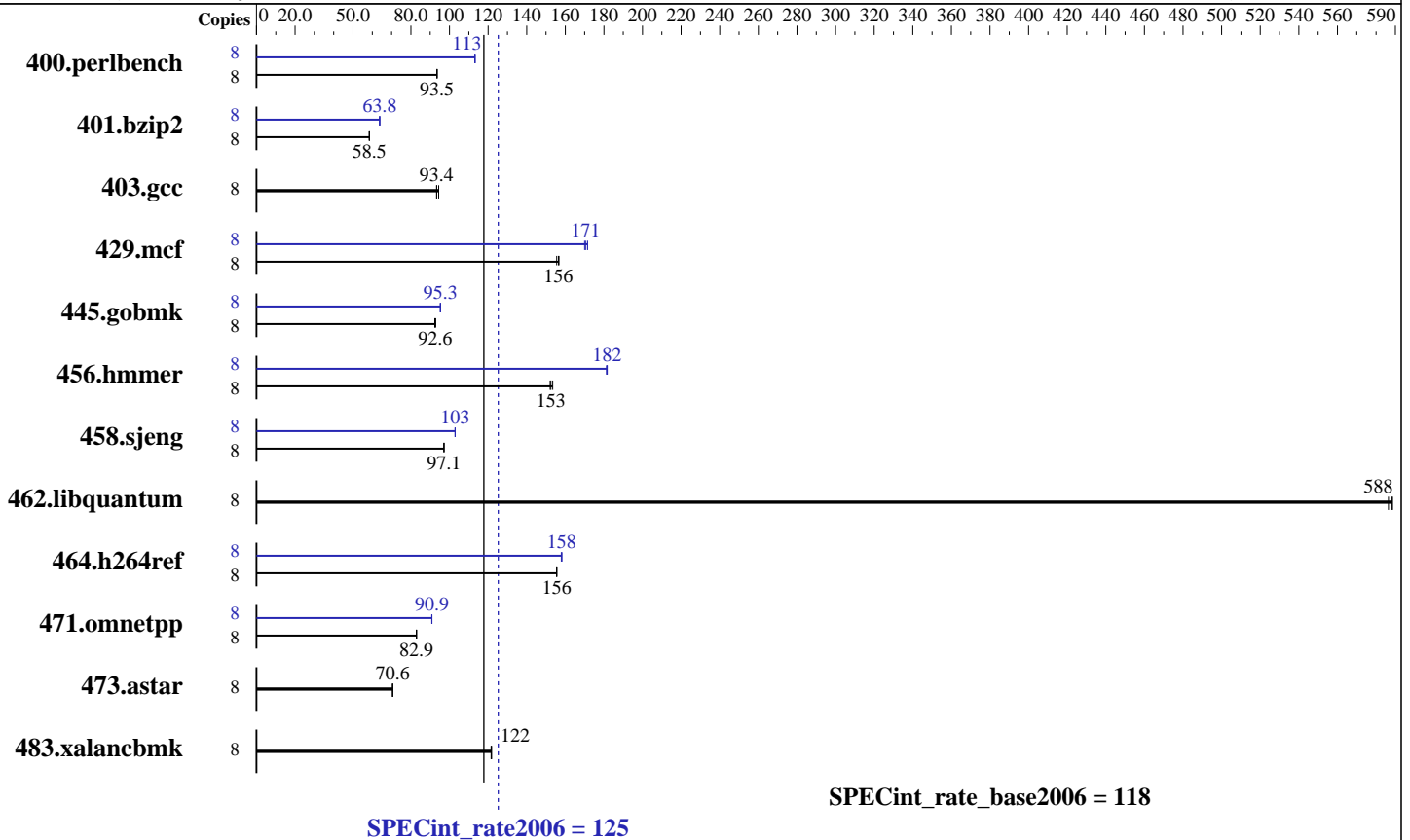SPECint_rate_base2006 =   118

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

SPECint_rate2006 = 125

SPECint_rate_base2006 = 118

Hardware

CPU Name:	Intel Xeon E5603
CPU Characteristics:	
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz and CL7)
Disk Subsystem:	1 x SAS, 300 GB, 10000 RPM
Other Hardware:	--

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon E5603, 1.60 GHz

SPECint_rate2006 = 125

SPECint_rate_base2006 = 118

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	835	93.6	836	93.5	**836**	**93.5**	8	**690**	**113**	690	113	691	113
401.bzip2	8	1319	58.5	**1320**	**58.5**	1321	58.4	8	**1209**	**63.8**	1211	63.8	1208	63.9
403.gcc	8	**690**	**93.4**	690	93.3	682	94.4	8	**690**	**93.4**	690	93.3	682	94.4
429.mcf	8	469	156	466	157	**466**	**156**	8	425	172	429	170	**428**	**171**
445.gobmk	8	**907**	**92.6**	909	92.4	904	92.8	8	881	95.2	**880**	**95.3**	880	95.3
456.hmmer	8	**489**	**153**	491	152	486	153	8	412	181	**411**	**182**	411	182
458.sjeng	8	997	97.1	996	97.1	**997**	**97.1**	8	940	103	**940**	**103**	941	103
462.libquantum	8	283	586	282	588	**282**	**588**	8	283	586	282	588	**282**	**588**
464.h264ref	8	1139	155	1138	156	**1138**	**156**	8	1119	158	1121	158	**1119**	**158**
471.omnetpp	8	603	82.9	**603**	**82.9**	604	82.8	8	**550**	**90.9**	550	90.9	551	90.8
473.astar	8	797	70.4	796	70.6	**796**	**70.6**	8	797	70.4	796	70.6	**796**	**70.6**
483.xalancbmk	8	453	122	454	122	**453**	**122**	8	453	122	454	122	**453**	**122**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
     Hugepages were not configured on the system
```

Platform Notes

```
     BIOS configuration:
     Data Reuse Optimization = Disable
     Performance/Power Setting = Traditional
```

General Notes

```
     For information about Fujitsu please visit: http://www.fujitsu.com
     Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX924 S2, Intel Xeon E5603, 1.60 GHz

SPECint_rate2006 =   125

SPECint_rate_base2006 = 118

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/smartheap -lsmartheap
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

Fujitsu

PRIMERGY BX924 S2, Intel Xeon E5603, 1.60 GHz

SPECint_rate2006 =   125

SPECint_rate_base2006 =   118

CPU2006 license: 19	**Test date:** Feb-2011
Test sponsor: Fujitsu	**Hardware Availability:** Feb-2011
Tested by: Fujitsu	**Software Availability:** Jan-2011

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

     403.gcc: basepeak = yes

     429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -auto-ilp32

   445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
              -ansi-alias -auto-ilp32

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
              -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

   458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -unroll4 -auto-ilp32
              -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

  464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                -L/smartheap -lsmartheap
```

Fujitsu

PRIMERGY BX924 S2, Intel Xeon E5603, 1.60 GHz

SPECint_rate2006 = 125

SPECint_rate_base2006 = 118

CPU2006 license: 19		**Test date:**	Feb-2011
Test sponsor:	Fujitsu	**Hardware Availability:**	Feb-2011
Tested by:	Fujitsu	**Software Availability:**	Jan-2011

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.xml

SPEC and SPECint are registered trademarks of the Standard Performance
Evaluation Corporation. All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 16:32:31 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 March 2011.