



# SPEC® CINT2006 Result

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## Oracle Corporation

Sun Blade X6270 M2 Server Module (Intel Xeon X5690 3.47 GHz)

SPECint®2006 = 46.0

SPECint\_base2006 = 43.8

CPU2006 license: 6

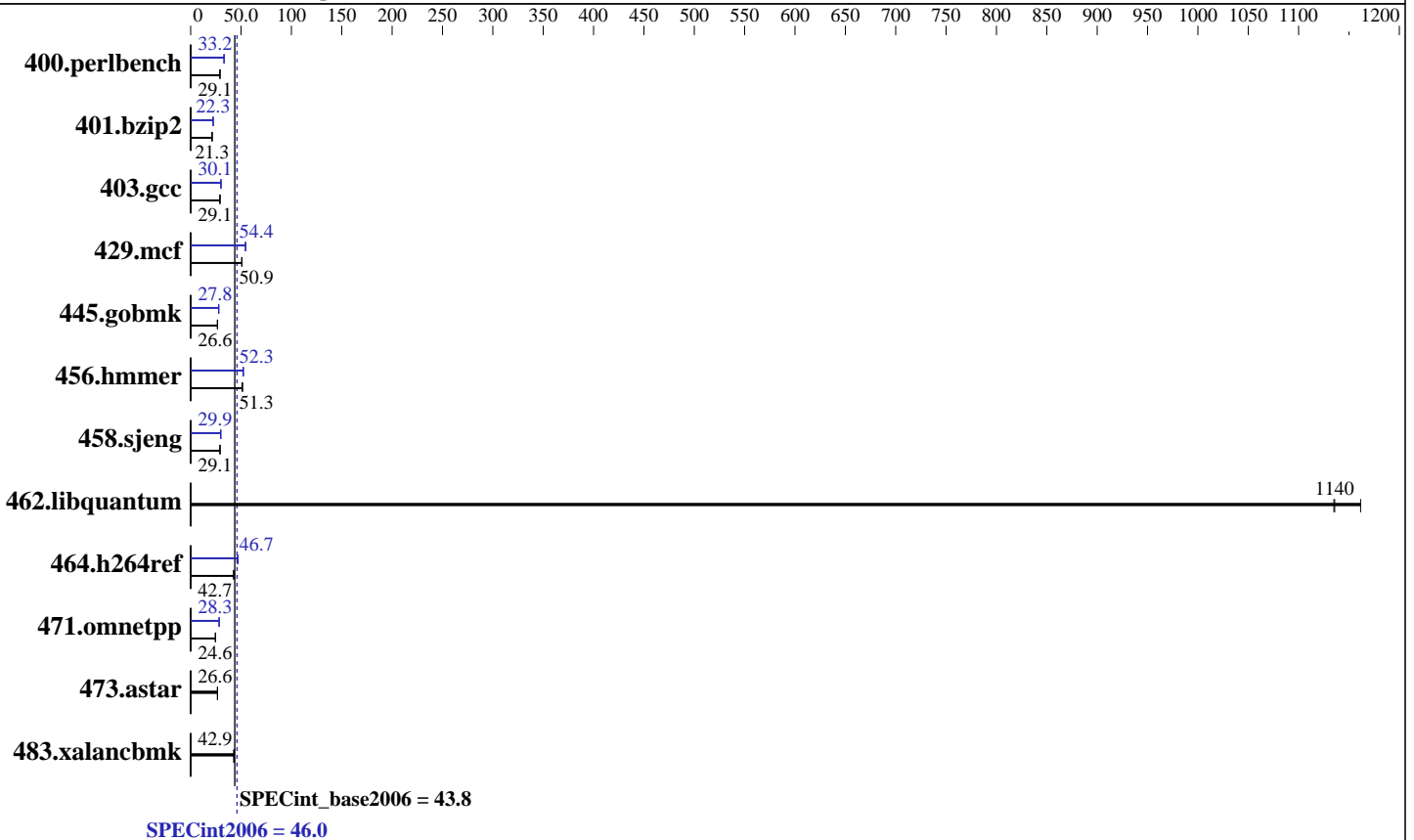
Test sponsor: Oracle Corporation

Tested by: Oracle Corporation

Test date: Jan-2011

Hardware Availability: Mar-2011

Software Availability: Nov-2010



### Hardware

CPU Name: Intel Xeon X5690  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz  
 CPU MHz: 3467  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 300 GB 10000 RPM SAS2  
 Other Hardware: None

### Software

Operating System: Oracle Linux 5.5 kernel 2.6.18-194.el5  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>336</b>	<b>29.1</b>	336	29.1	337	29.0	295	33.2	<b>295</b>	<b>33.2</b>	295	33.1
401.bzip2	453	21.3	<b>454</b>	<b>21.3</b>	454	21.3	433	22.3	433	22.3	<b>433</b>	<b>22.3</b>
403.gcc	<b>276</b>	<b>29.1</b>	276	29.1	278	28.9	268	30.1	<b>268</b>	<b>30.1</b>	268	30.0
429.mcf	180	50.6	<b>179</b>	<b>50.9</b>	179	50.9	168	54.4	167	54.6	<b>168</b>	<b>54.4</b>
445.gobmk	<b>395</b>	<b>26.6</b>	396	26.5	393	26.7	377	27.8	<b>377</b>	<b>27.8</b>	377	27.9
456.hammer	182	51.3	182	51.3	<b>182</b>	<b>51.3</b>	<b>178</b>	<b>52.3</b>	178	52.3	179	52.1
458.sjeng	417	29.0	414	29.2	<b>416</b>	<b>29.1</b>	405	29.8	405	29.9	<b>405</b>	<b>29.9</b>
462.libquantum	18.3	1140	<b>18.2</b>	<b>1140</b>	17.8	1160	18.3	1140	<b>18.2</b>	<b>1140</b>	17.8	1160
464.h264ref	517	42.8	<b>518</b>	<b>42.7</b>	519	42.6	473	46.8	474	46.7	<b>473</b>	<b>46.7</b>
471.omnetpp	255	24.5	253	24.7	<b>254</b>	<b>24.6</b>	222	28.2	<b>221</b>	<b>28.3</b>	221	28.3
473.astar	<b>264</b>	<b>26.6</b>	263	26.7	264	26.6	<b>264</b>	<b>26.6</b>	263	26.7	264	26.6
483.xalancbmk	<b>161</b>	<b>42.9</b>	160	43.1	161	42.9	<b>161</b>	<b>42.9</b>	160	43.1	161	42.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Hugepages was enabled with the following:
'nODEV /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
Load Default BIOS Settings and then change the following
Hardware Prefetch Enabled
Adjacent Cache Line Prefetch Enabled
L1 Data Prefetch Enabled
Intel Hyperthreading Options Disabled
```

## General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
Binaries were compiled on RHEL5.5 with Binutils binutils-2.17.50.0.6-14.el5
```

## Base Compiler Invocation

```
C benchmarks:
icc -m64
```

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap64  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32

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## Peak Compiler Invocation (Continued)

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

456.hammer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

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## Peak Optimization Flags (Continued)

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

[http://www.spec.org/cpu2006/flags/Oracle-platform-x86\\_64.20101027.html](http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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