



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 64.0

PowerEdge T610 (Intel Xeon X5687, 3.60 GHz)

SPECfp_base2006 = 61.0

CPU2006 license: 55

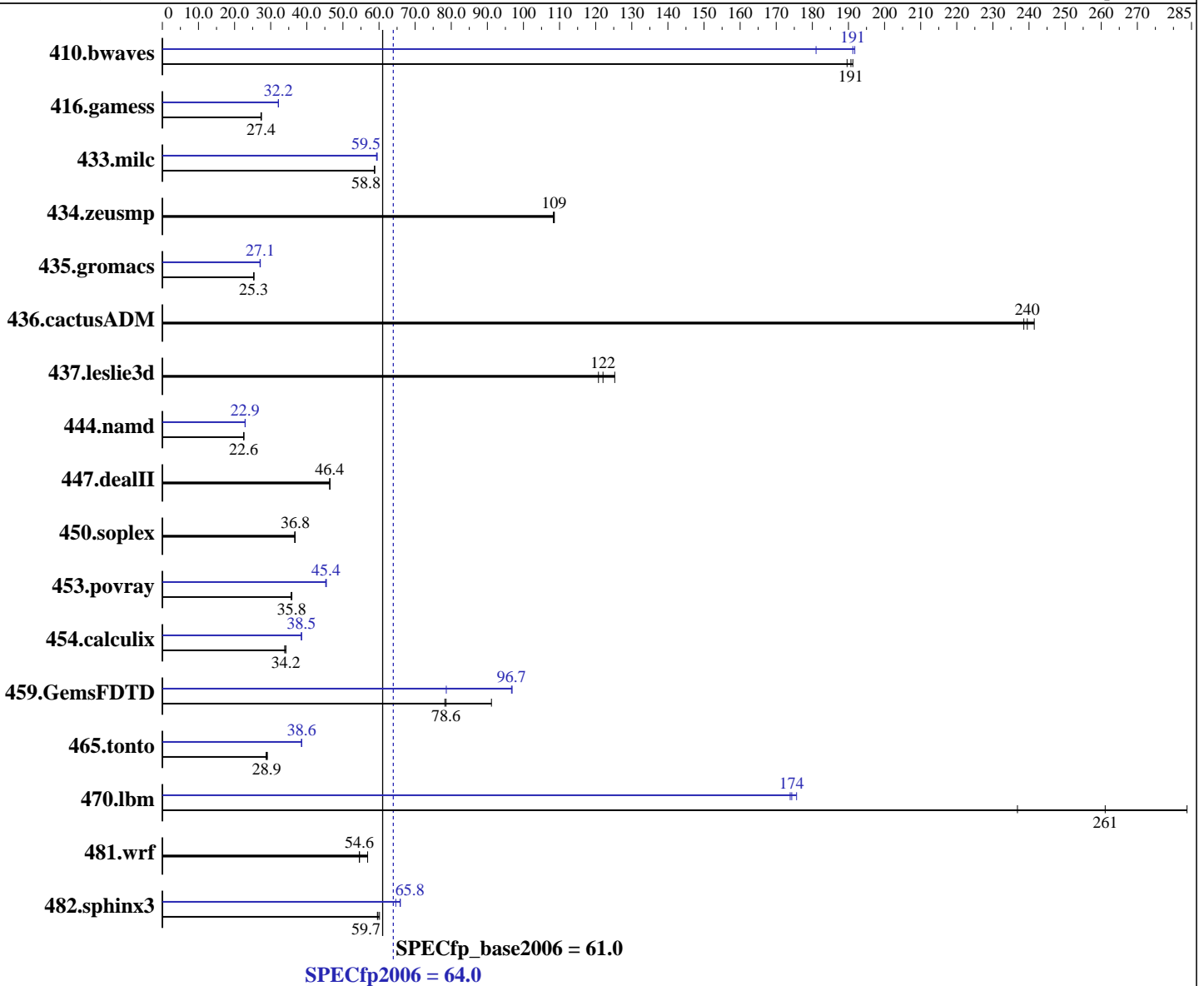
Test date: Feb-2011

Test sponsor: Dell Inc.

Hardware Availability: Feb-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon X5687
 CPU Characteristics: Intel Turbo Boost Technology up to 3.86 GHz
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binaries compiled on RHEL5.5 with
 binutils-2.17.50.0.6-14.el5

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	71.7	190	<u>71.3</u>	<u>191</u>	71.1	191	75.1	181	<u>71.1</u>	<u>191</u>	70.9	192
416.gamess	717	27.3	<u>715</u>	<u>27.4</u>	714	27.4	<u>609</u>	<u>32.2</u>	609	32.2	610	32.1
433.milc	156	58.8	<u>156</u>	<u>58.8</u>	156	58.7	<u>154</u>	<u>59.5</u>	154	59.5	155	59.3
434.zeusmp	83.9	109	<u>83.9</u>	<u>109</u>	84.1	108	83.9	109	<u>83.9</u>	<u>109</u>	84.1	108
435.gromacs	282	25.4	<u>282</u>	<u>25.3</u>	282	25.3	263	27.1	264	27.0	<u>263</u>	<u>27.1</u>
436.cactusADM	49.5	241	<u>49.9</u>	<u>240</u>	50.1	239	49.5	241	<u>49.9</u>	<u>240</u>	50.1	239
437.leslie3d	77.8	121	75.0	125	<u>77.0</u>	<u>122</u>	77.8	121	75.0	125	<u>77.0</u>	<u>122</u>
444.namd	356	22.5	<u>356</u>	<u>22.6</u>	356	22.6	<u>350</u>	<u>22.9</u>	350	22.9	349	23.0
447.dealII	<u>247</u>	<u>46.4</u>	247	46.4	247	46.4	<u>247</u>	<u>46.4</u>	247	46.4	247	46.4
450.soplex	227	36.8	228	36.6	<u>227</u>	<u>36.8</u>	227	36.8	228	36.6	<u>227</u>	<u>36.8</u>
453.povray	<u>149</u>	<u>35.8</u>	149	35.6	148	35.9	118	45.2	<u>117</u>	<u>45.4</u>	117	45.4
454.calculix	241	34.2	<u>241</u>	<u>34.2</u>	244	33.9	214	38.5	<u>214</u>	<u>38.5</u>	214	38.5
459.GemsFDTD	136	78.2	<u>135</u>	<u>78.6</u>	116	91.1	135	78.6	<u>110</u>	<u>96.7</u>	110	96.9
465.tonto	338	29.1	<u>340</u>	<u>28.9</u>	343	28.7	255	38.6	<u>255</u>	<u>38.6</u>	255	38.5
470.lbm	48.4	284	58.0	237	<u>52.6</u>	<u>261</u>	78.2	176	<u>78.8</u>	<u>174</u>	79.0	174
481.wrf	<u>205</u>	<u>54.6</u>	205	54.6	196	56.9	<u>205</u>	<u>54.6</u>	205	54.6	196	56.9
482.sphinx3	324	60.1	<u>326</u>	<u>59.7</u>	327	59.6	301	64.6	296	65.9	<u>296</u>	<u>65.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
 Power Management = Maximum Performance (Default = Active Power Controller)
 Data Reuse = Disabled (Default = Enabled)
 Logical Processor = Disabled (Default = Enabled)



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General Notes

OMP_NUM_THREADS set to number of cores
The Dell PowerEdge T610 and
the Bull NovaScale T840 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge T610 model.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -parallel
-ansi-alias -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.xml>

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For other inquiries, please contact webmaster@spec.org.

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