



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®\_rate2006 = 202

PRIMERGY BX924 S2, Intel Xeon X5675, 3.07 GHz

SPECint\_rate\_base2006 = 190

CPU2006 license: 19

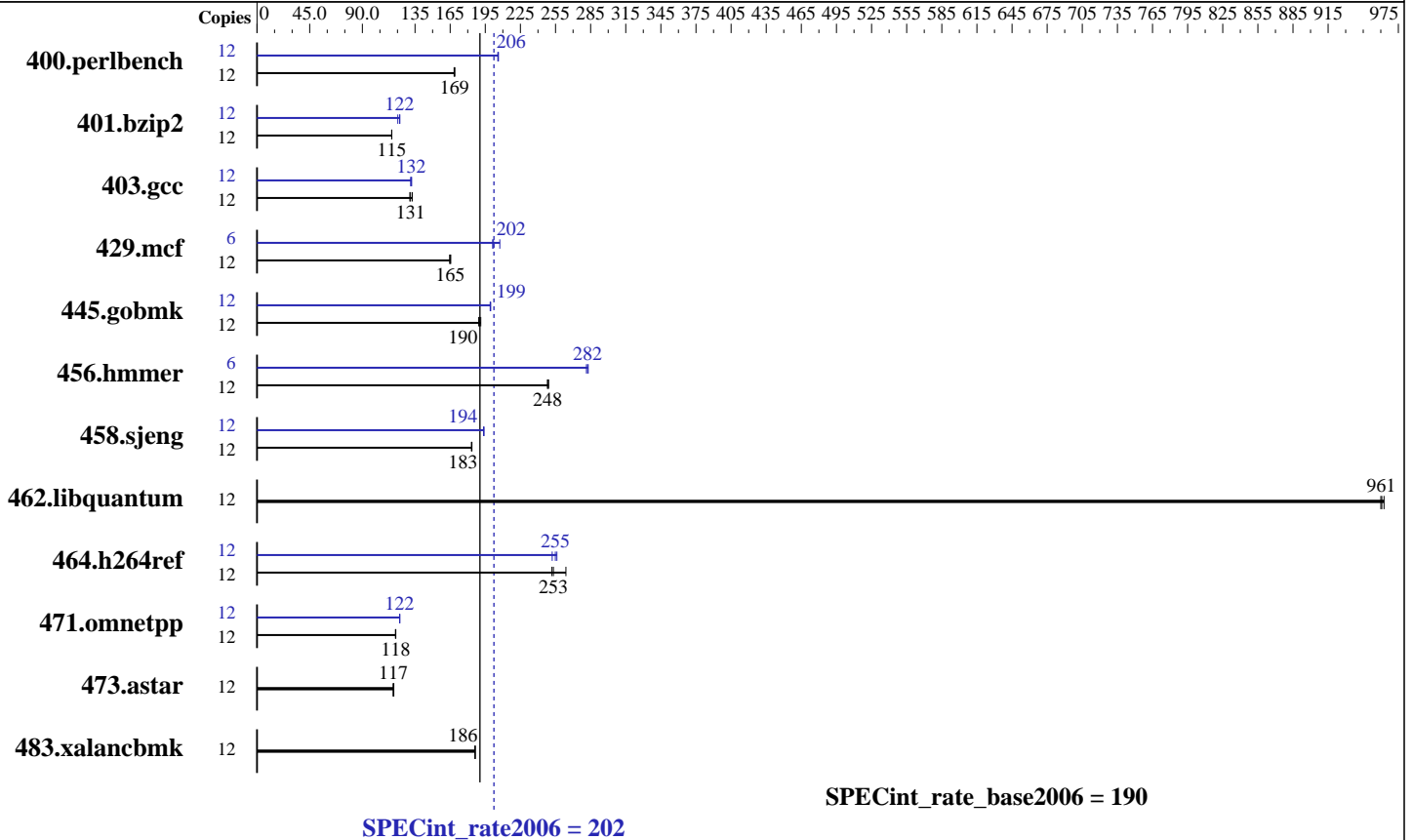
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon X5675  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.47 GHz  
 CPU MHz: 3067  
 FPU: Integrated  
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM  
 Other Hardware: --

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	694	169	<b><u>695</u></b>	<b><u>169</u></b>	696	168	12	<b><u>569</u></b>	<b><u>206</u></b>	570	206	569	206
401.bzip2	12	1006	115	<b><u>1006</u></b>	<b><u>115</u></b>	1006	115	12	963	120	<b><u>950</u></b>	<b><u>122</u></b>	949	122
403.gcc	12	<b><u>736</u></b>	<b><u>131</u></b>	728	133	740	131	12	731	132	736	131	<b><u>733</u></b>	<b><u>132</u></b>
429.mcf	12	661	166	<b><u>663</u></b>	<b><u>165</u></b>	665	165	6	264	207	<b><u>271</u></b>	<b><u>202</u></b>	272	201
445.gobmk	12	659	191	<b><u>663</u></b>	<b><u>190</u></b>	665	189	12	<b><u>631</u></b>	<b><u>199</u></b>	632	199	631	200
456.hammer	12	449	249	451	248	<b><u>451</u></b>	<b><u>248</u></b>	6	199	281	198	283	<b><u>198</u></b>	<b><u>282</u></b>
458.sjeng	12	791	184	792	183	<b><u>792</u></b>	<b><u>183</u></b>	12	750	194	<b><u>750</u></b>	<b><u>194</u></b>	749	194
462.libquantum	12	259	960	<b><u>259</u></b>	<b><u>961</u></b>	258	963	12	259	960	<b><u>259</u></b>	<b><u>961</u></b>	258	963
464.h264ref	12	<b><u>1049</u></b>	<b><u>253</u></b>	1006	264	1054	252	12	1053	252	<b><u>1042</u></b>	<b><u>255</u></b>	1037	256
471.omnetpp	12	<b><u>634</u></b>	<b><u>118</u></b>	634	118	633	119	12	<b><u>615</u></b>	<b><u>122</u></b>	615	122	615	122
473.astar	12	<b><u>722</u></b>	<b><u>117</u></b>	725	116	721	117	12	<b><u>722</u></b>	<b><u>117</u></b>	725	116	721	117
483.xalancbmk	12	445	186	444	186	<b><u>444</u></b>	<b><u>186</u></b>	12	445	186	444	186	<b><u>444</u></b>	<b><u>186</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Hugepages were not configured on the system

## Platform Notes

BIOS configuration:  
Data Reuse Optimization = Disable  
Performance/Power Setting = Traditional

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 16:26:10 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 3 March 2011.