



# SPEC® CINT2006 Result

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Bull SAS

**SPECint®2006 = 33.3**

BL265 (Intel Xeon E5620, 2.40 GHz)

**SPECint\_base2006 = 32.2**

CPU2006 license: 20

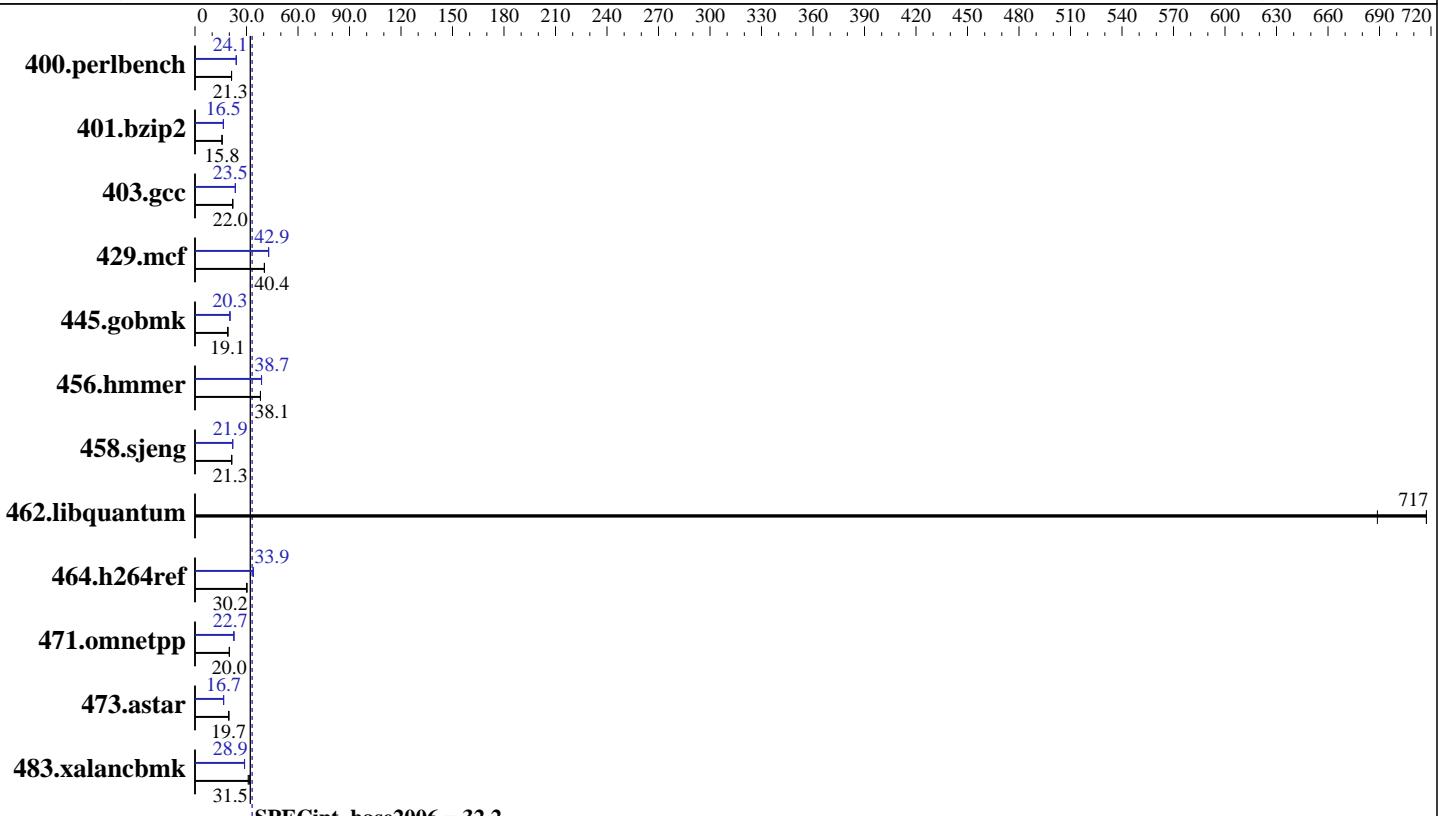
**Test date:** Jan-2011

**Hardware Availability:** May-2010

**Software Availability:** Nov-2010

Test sponsor: Bull SAS

Tested by: Bull SAS



## Hardware

CPU Name: Intel Xeon E5620  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)  
 Disk Subsystem: 2 x 50 GB SATA, SSD  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	459	21.3	<b>459</b>	<b>21.3</b>	458	21.3	<b>406</b>	<b>24.0</b>	<b>406</b>	<b>24.1</b>	<b>406</b>	<b>24.1</b>
401.bzip2	<b>612</b>	<b>15.8</b>	612	15.8	612	15.8	<b>584</b>	<b>16.5</b>	<b>584</b>	<b>16.5</b>	584	16.5
403.gcc	<b>366</b>	<b>22.0</b>	367	22.0	366	22.0	<b>342</b>	<b>23.5</b>	342	23.5	342	23.5
429.mcf	225	40.5	226	40.4	<b>226</b>	<b>40.4</b>	<b>213</b>	<b>42.9</b>	213	42.9	213	42.8
445.gobmk	546	19.2	<b>549</b>	<b>19.1</b>	551	19.0	<b>516</b>	<b>20.3</b>	515	20.4	516	20.3
456.hmmer	245	38.1	<b>245</b>	<b>38.1</b>	245	38.1	241	38.7	240	38.8	<b>241</b>	<b>38.7</b>
458.sjeng	568	21.3	564	21.5	<b>567</b>	<b>21.3</b>	<b>551</b>	<b>21.9</b>	551	22.0	551	21.9
462.libquantum	30.1	689	<b>28.9</b>	<b>717</b>	28.9	717	30.1	689	<b>28.9</b>	<b>717</b>	28.9	717
464.h264ref	735	30.1	<b>732</b>	<b>30.2</b>	731	30.3	652	33.9	<b>652</b>	<b>33.9</b>	652	34.0
471.omnetpp	312	20.0	<b>312</b>	<b>20.0</b>	312	20.0	276	22.7	277	22.6	<b>276</b>	<b>22.7</b>
473.astar	356	19.7	359	19.6	<b>356</b>	<b>19.7</b>	422	16.6	420	16.7	<b>421</b>	<b>16.7</b>
483.xalancbmk	223	31.0	<b>219</b>	<b>31.5</b>	218	31.6	239	28.9	240	28.8	<b>239</b>	<b>28.9</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Hugepages was enabled with the following:

```
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

Turbo Mode enabled in BIOS

Turbo Boost set to Traditional in BIOS

Power C-states enabled in BIOS

Demand Scrub disabled in BIOS

## General Notes

OMP\_NUM\_THREADS set to number of cores

KMP\_AFFINITY set to granularity=fine,scatter

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Base Compiler Invocation

C benchmarks:

icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:

`icpc -m64`

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hammer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smarterheap -lsmarterheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32`

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## Peak Compiler Invocation (Continued)

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

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## Peak Optimization Flags (Continued)

```
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
           -ansi-alias
           -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

```
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -unroll14
```

462.libquantum: basepeak = yes

```
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -unroll12 -ansi-alias
           -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
           -L/smartheap -lsmartheap
           -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/smartheap -lsmartheap64
```

```
483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
               -Wl,-z,muldefs -L/smartheap -lsmartheap
               -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.xml>



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