



# SPEC® CINT2006 Result

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Itautec

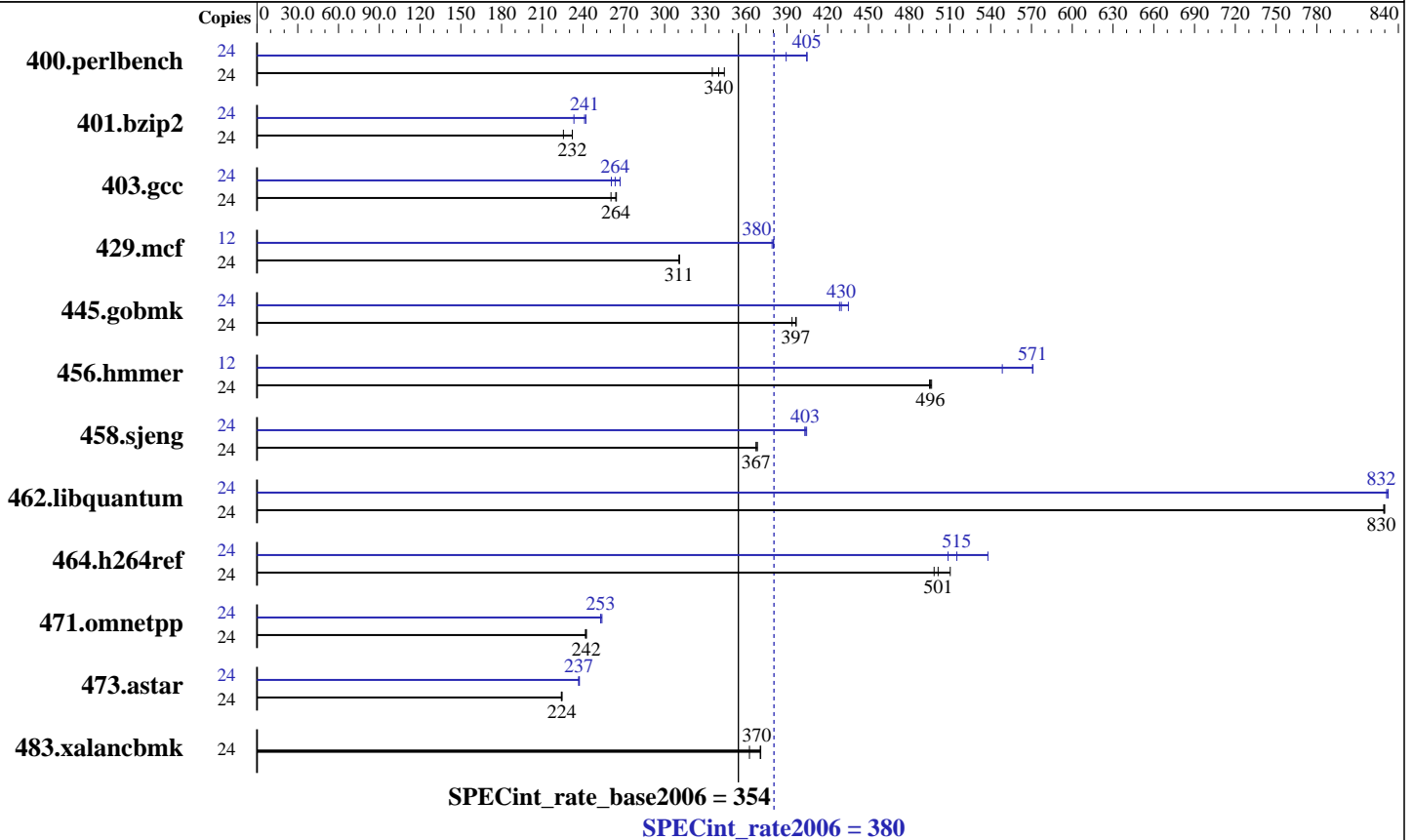
SPECint®\_rate2006 = 380

Servidor Itautec MX223 (Intel Xeon X5680)

SPECint\_rate\_base2006 = 354

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Dec-2010  
Hardware Availability: Apr-2010  
Software Availability: Apr-2010



## Hardware

CPU Name: Intel Xeon X5680  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l\_cproc\_p\_11.1.072  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	700	335	<b>690</b>	<b>340</b>	682	344	24	602	389	579	405	<b>580</b>	<b>405</b>
401.bzip2	24	1027	226	<b>998</b>	<b>232</b>	998	232	24	993	233	<b>961</b>	<b>241</b>	957	242
403.gcc	24	730	265	<b>732</b>	<b>264</b>	742	261	24	723	267	<b>733</b>	<b>264</b>	741	261
429.mcf	24	704	311	<b>705</b>	<b>311</b>	705	311	12	289	379	<b>288</b>	<b>380</b>	288	380
445.gobmk	24	<b>635</b>	<b>397</b>	639	394	634	397	24	578	435	<b>585</b>	<b>430</b>	587	429
456.hammer	24	452	495	<b>452</b>	<b>496</b>	451	496	12	204	549	<b>196</b>	<b>571</b>	196	571
458.sjeng	24	791	367	<b>790</b>	<b>367</b>	788	368	24	718	405	720	403	<b>720</b>	<b>403</b>
462.libquantum	24	599	830	600	829	<b>599</b>	<b>830</b>	24	<b>598</b>	<b>832</b>	597	833	598	831
464.h264ref	24	1066	498	<b>1060</b>	<b>501</b>	1041	510	24	987	538	<b>1031</b>	<b>515</b>	1044	509
471.omnetpp	24	619	242	621	242	<b>619</b>	<b>242</b>	24	591	254	<b>593</b>	<b>253</b>	593	253
473.astar	24	<b>751</b>	<b>224</b>	751	224	752	224	24	710	237	<b>711</b>	<b>237</b>	712	237
483.xalancbmk	24	457	362	<b>447</b>	<b>370</b>	447	371	24	457	362	<b>447</b>	<b>370</b>	447	371

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## Platform Notes

BIOS Settings:  
Data Reuse = Disabled (Default = Enabled)

## General Notes

This result was measured on the Servidor Itaotec MX203.  
The Servidor Itaotec MX223 and the Servidor Itaotec MX203 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):  
-L/opt/sh/SmartHeap\_8/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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