SPEC® CINT2006 Result

Intel Corporation

Intel DH57JG Motherboard (Intel Core i5-650)

SPECint®2006 = 28.9

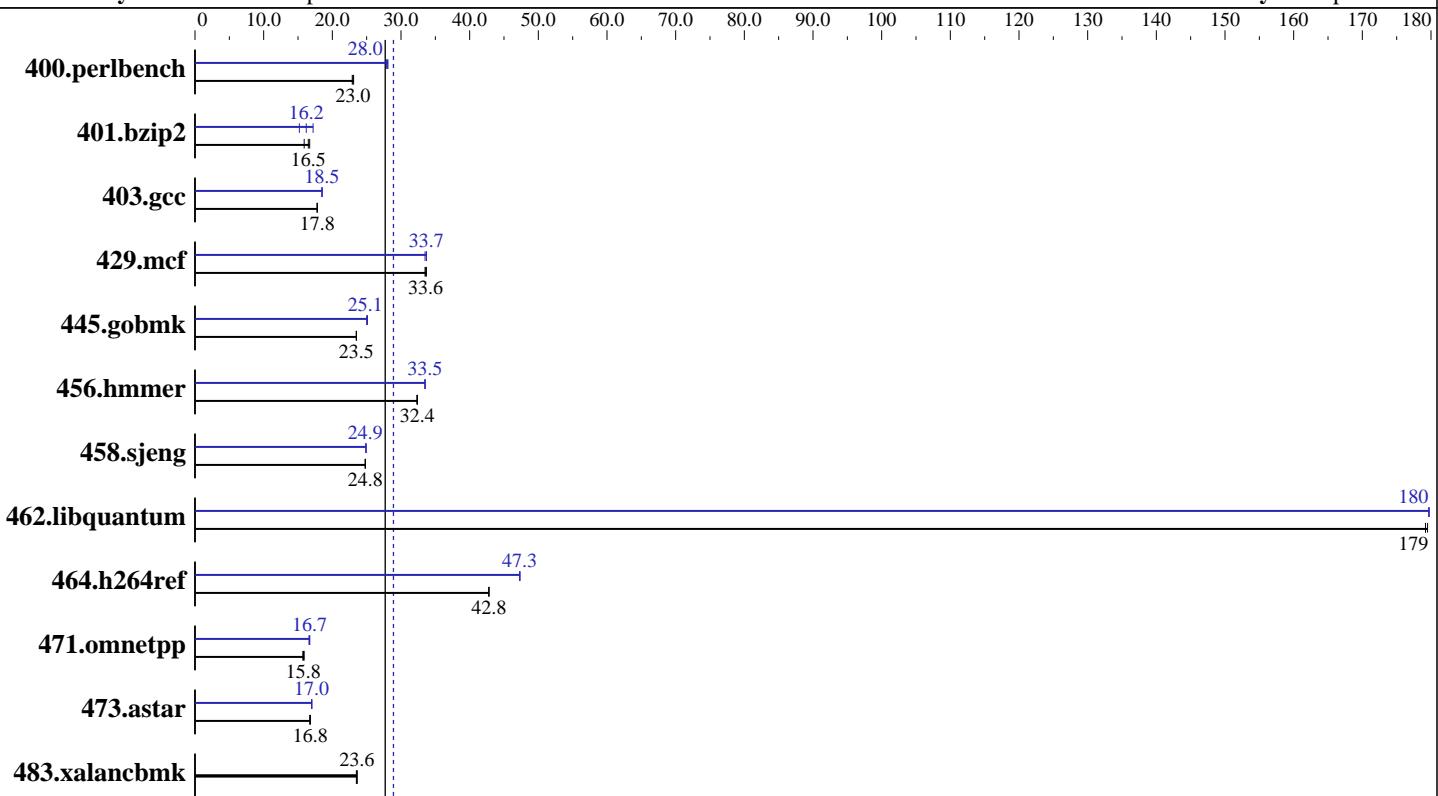SPECint_base2006 = 27.7

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Sep-2010

Hardware Availability: Jan-2010

Software Availability: Sep-2009

Benchmark	Peak	Base
400.perlbench	28.0	23.0
401.bzip2	16.2	16.5
403.gcc	18.5	17.8
429.mcf	33.7	33.6
445.gobmk	25.1	23.5
456.hmmer	33.5	32.4
458.sjeng	24.9	24.8
462.libquantum	180	179
464.h264ref	47.3	42.8
471.omnetpp	16.7	15.8
473.astar	17.0	16.8
483.xalancbmk		23.6

SPECint_base2006 = 27.7

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Hardware

CPU Name:	Intel Core i5-650
CPU Characteristics:	Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	Seagate 1 TB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other Software:	None
	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	426	22.9	**425**	**23.0**	423	23.1	348	28.1	**350**	**28.0**	350	27.9
401.bzip2	608	15.9	578	16.7	**586**	**16.5**	562	17.2	633	15.2	**595**	**16.2**
403.gcc	**452**	**17.8**	452	17.8	452	17.8	434	18.5	**434**	**18.5**	434	18.5
429.mcf	**271**	**33.6**	272	33.5	271	33.7	**271**	**33.7**	272	33.5	271	33.7
445.gobmk	447	23.5	447	23.5	**447**	**23.5**	**419**	**25.1**	419	25.1	419	25.0
456.hmmer	289	32.3	**288**	**32.4**	288	32.4	278	33.5	**278**	**33.5**	278	33.5
458.sjeng	488	24.8	488	24.8	**488**	**24.8**	486	24.9	**485**	**24.9**	485	24.9
462.libquantum	**116**	**179**	115	180	116	179	115	180	**115**	**180**	115	180
464.h264ref	517	42.8	**517**	**42.8**	517	42.8	468	47.3	**468**	**47.3**	468	47.3
471.omnetpp	**395**	**15.8**	394	15.9	397	15.7	376	16.6	**375**	**16.7**	375	16.7
473.astar	419	16.7	417	16.8	**417**	**16.8**	414	17.0	**414**	**17.0**	414	17.0
483.xalancbmk	293	23.5	**293**	**23.6**	292	23.6	293	23.5	**293**	**23.6**	292	23.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
    401.bzip2: -DSPEC_CPU_P64
      403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
      429.mcf: -DSPEC_CPU_P64
    445.gobmk: -DSPEC_CPU_P64
    456.hmmer: -DSPEC_CPU_P64
    458.sjeng: -DSPEC_CPU_P64
```

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Base Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_P64
  464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
  471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
     473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qauto-ilp32 /F512000000
```

C++ benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlW64M.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
               -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
               -Qauto-ilp32 /F512000000 shlW64M.lib
```

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Peak Optimization Flags (Continued)

400.perlbench (continued):
```
-link /FORCE:MULTIPLE
```

401.bzip2: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias`
`-Qauto-ilp32 /F512000000`

403.gcc: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000`

429.mcf: `-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch`
`-Qauto-ilp32 /F512000000`

445.gobmk: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32`
`/F512000000`

456.hmmer: `-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias`
`-Qauto-ilp32 /F512000000`

458.sjeng: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto-ilp32 /F512000000`

462.libquantum: `-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch`
`-Qparallel -Qpar-schedule-static:32768 -Qansi-alias`
`-Qauto-ilp32 /F512000000`

464.h264ref: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias`
`-Qauto-ilp32 /F512000000`

C++ benchmarks:

471.omnetpp: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qansi-alias`
`-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000`
`shlW64M.lib          -link /FORCE:MULTIPLE`

473.astar: `-QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)`
`-Qipo -O3 -Qprec-div- -Qansi-alias`
`-Qopt-ra-region-strategy=routine -Qauto-ilp32 /F512000000`
`shlW64M.lib          -link /FORCE:MULTIPLE`

483.xalancbmk: `basepeak = yes`

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.01.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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