# SPEC® CINT2006 Result



Clevo
(Test Sponsor: Intel Corporation)

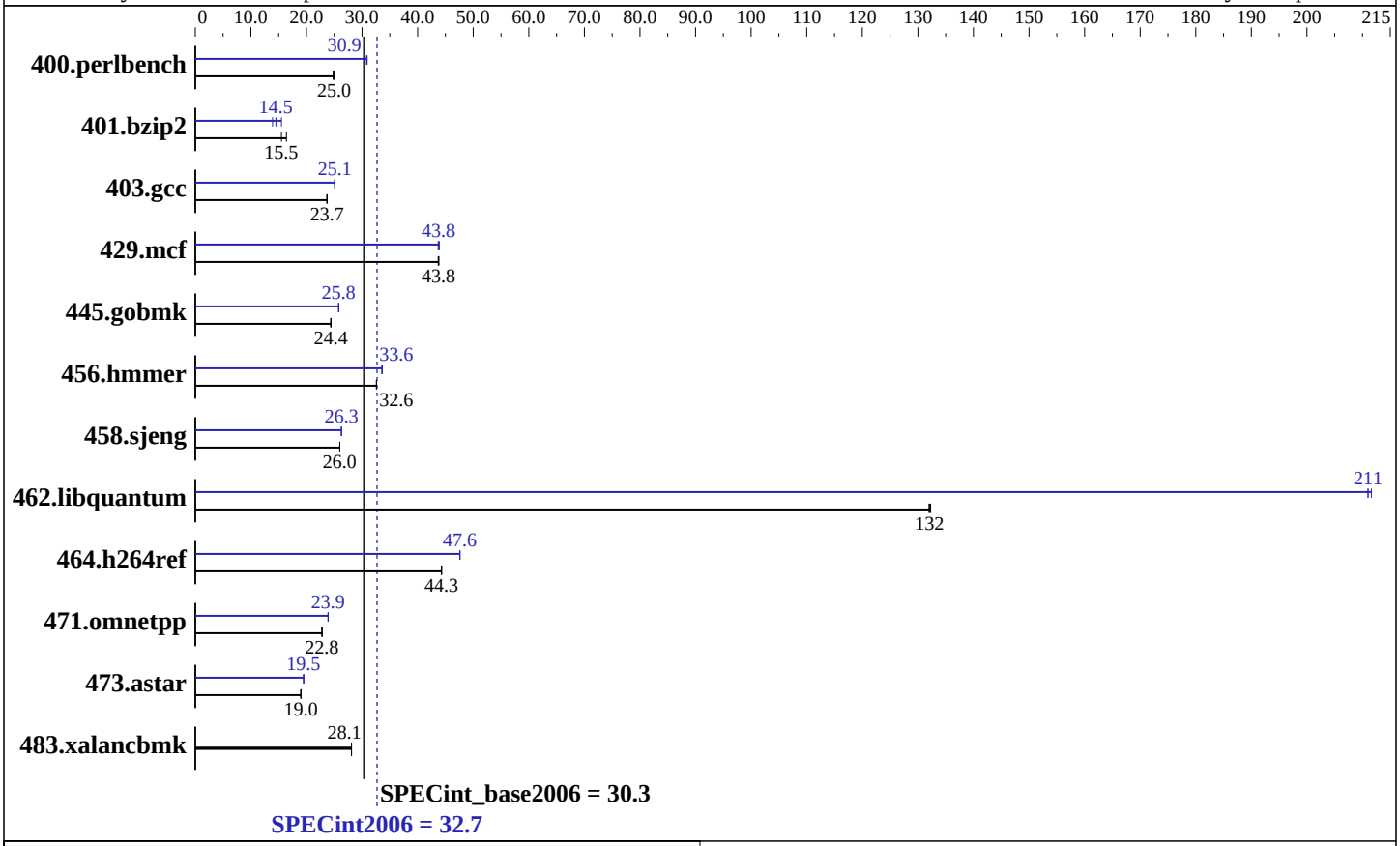
## Clevo STYLE-NOTE

SPECint®2006 = 32.7

SPECint_base2006 = 30.3

**CPU2006 license:** 13
**Test sponsor:** Intel Corporation
**Tested by:** Intel Corporation

**Test date:** Apr-2010
**Hardware Availability:** Jun-2010
**Software Availability:** Sep-2009




### Hardware

| | |
|---|---|
| CPU Name: | Intel Core i7-940XM |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.33 GHz |
| CPU MHz: | 2133 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 8 GB (2 x 4 GB 2Rx8 PC3-10600S-9) |
| Disk Subsystem: | Hitachi 320 GB SATA, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows 7 Ultimate (64-bit) |
| Compiler: | Intel C++ Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045<br>Microsoft Visual Studio 2008 Professional SP1 (for libraries) |
| Auto Parallel: | Yes |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 64-bit |
| Peak Pointers: | 64-bit |
| Other Software: | None<br>SmartHeap Library Version 8.1 from http://www.microquill.com/ |

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 394 | 24.8 | **391** | **25.0** | 390 | 25.0 | 316 | 30.9 | 317 | 30.8 | **317** | **30.9** |
| 401.bzip2 | **621** | **15.5** | 590 | 16.4 | 654 | 14.7 | **664** | **14.5** | 624 | 15.5 | 693 | 13.9 |
| 403.gcc | 339 | 23.7 | **340** | **23.7** | 340 | 23.7 | **321** | **25.1** | 321 | 25.1 | 321 | 25.1 |
| 429.mcf | 208 | 43.8 | 209 | 43.7 | **208** | **43.8** | 208 | 43.9 | **208** | **43.8** | 209 | 43.7 |
| 445.gobmk | 431 | 24.4 | 430 | 24.4 | **430** | **24.4** | 407 | 25.8 | 407 | 25.8 | **407** | **25.8** |
| 456.hmmer | 286 | 32.6 | 286 | 32.6 | **286** | **32.6** | 278 | 33.6 | **278** | **33.6** | 278 | 33.6 |
| 458.sjeng | 465 | 26.0 | **465** | **26.0** | 465 | 26.0 | 461 | 26.3 | 461 | 26.3 | **461** | **26.3** |
| 462.libquantum | 157 | 132 | 157 | 132 | **157** | **132** | **98.2** | **211** | 97.9 | 212 | 98.2 | 211 |
| 464.h264ref | **500** | **44.3** | 499 | 44.3 | 500 | 44.3 | 465 | 47.6 | **465** | **47.6** | 465 | 47.6 |
| 471.omnetpp | **275** | **22.8** | 275 | 22.8 | 275 | 22.8 | 261 | 23.9 | 262 | 23.9 | **261** | **23.9** |
| 473.astar | **369** | **19.0** | 369 | 19.0 | 369 | 19.0 | 359 | 19.5 | 359 | 19.5 | **359** | **19.5** |
| 483.xalancbmk | 245 | 28.1 | **245** | **28.1** | 245 | 28.1 | 245 | 28.1 | **245** | **28.1** | 245 | 28.1 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
The Windows command 'start' was used to bind copies to cores.
(For details, please see the config file.)
```

## General Notes

```
OMP_NUM_THREADS set to number of processor cores
KMP_AFFINITY set to granularity=fine,scatter
```

## Base Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64 -DSPEC_CPU_NO_NEED_VA_COPY`
401.bzip2: `-DSPEC_CPU_P64`

Continued on next page

## Base Portability Flags (Continued)

403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

## Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F512000000

C++ benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32 /F512000000

403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F512000000

445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F512000000

458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto-ilp32 /F512000000

462.libquantum: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qpar-schedule-static:32768 -Qansi-alias -Qauto-ilp32 /F512000000

464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

473.astar: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20101221.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20101221.xml`



For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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