



SPEC® CFP2006 Result

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Clevo

(Test Sponsor: Intel Corporation)

SPECfp®2006 = 34.4

Clevo STYLE-NOTE

SPECfp_base2006 = 32.0

CPU2006 license: 13

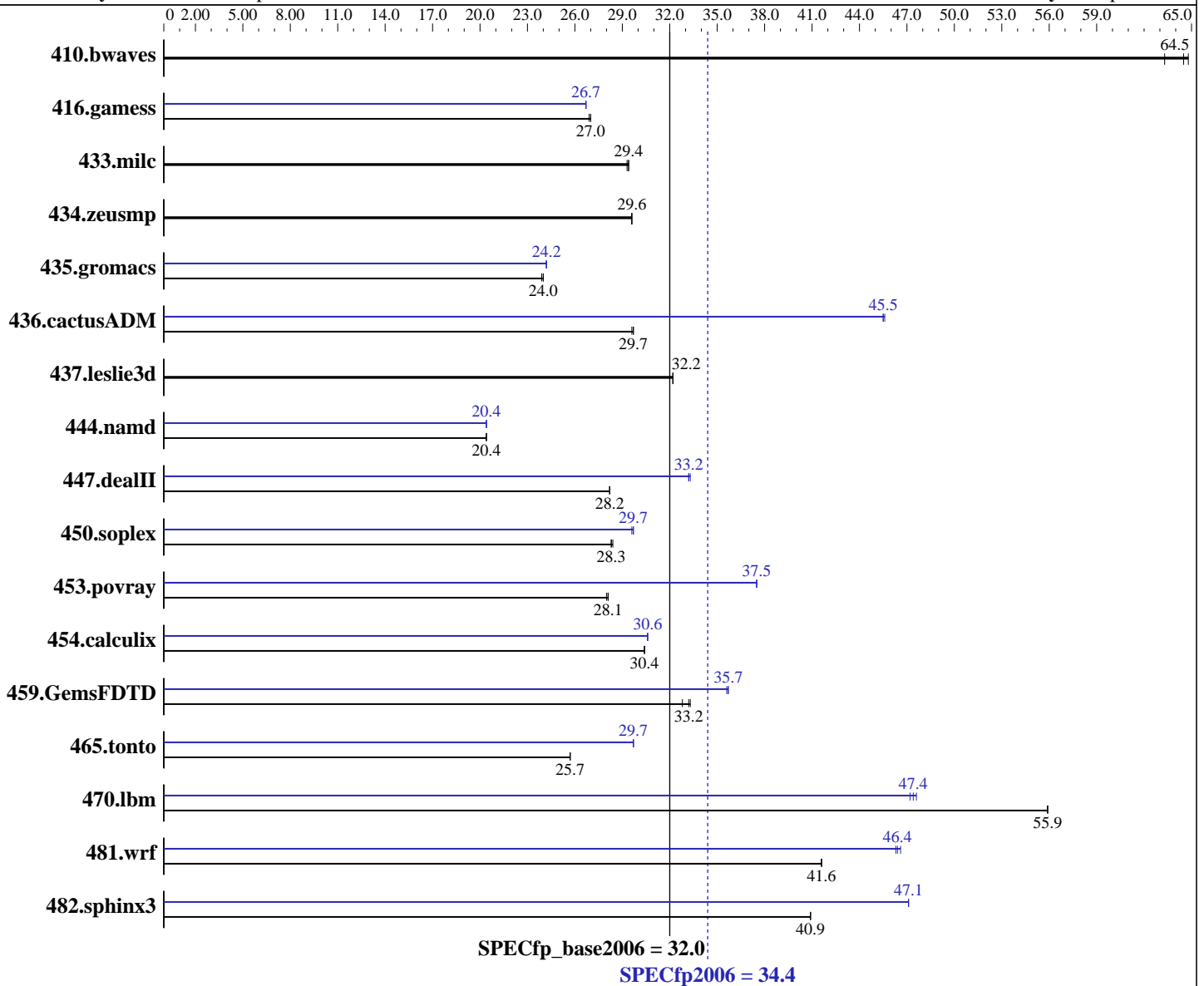
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Apr-2010

Hardware Availability: Jun-2010

Software Availability: Sep-2009



Hardware

CPU Name: Intel Core i7-940XM
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler Professional 11.1 for Intel 64
 Build 20090903 Package ID: w_cproc_p_11.1.045
 Intel Visual Fortran Compiler Professional 11.1 for Intel 64
 Build 20090903 Package ID: w_cproc_p_11.1.045, w_cprof_p_11.1.045
 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
 Auto Parallel: Yes

Continued on next page



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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600S-9)
 Disk Subsystem: Hitachi 320 GB SATA, 7200 RPM
 Other Hardware: None

File System: NTFS
 System State: Default
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other Software: None
 SmartHeap Library Version 8.1 from
<http://www.microquill.com/>

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	215	63.3	210	64.8	211	64.5	215	63.3	210	64.8	211	64.5
416.gamess	727	26.9	725	27.0	726	27.0	734	26.7	733	26.7	733	26.7
433.milc	313	29.3	312	29.4	313	29.4	313	29.3	312	29.4	313	29.4
434.zeusmp	308	29.6	308	29.6	308	29.6	308	29.6	308	29.6	308	29.6
435.gromacs	299	23.9	298	24.0	298	24.0	295	24.2	295	24.2	295	24.2
436.cactusADM	403	29.6	403	29.7	403	29.7	262	45.6	263	45.5	263	45.5
437.leslie3d	292	32.2	292	32.2	292	32.2	292	32.2	292	32.2	292	32.2
444.namd	394	20.4	394	20.4	394	20.4	393	20.4	393	20.4	393	20.4
447.dealII	406	28.2	406	28.2	406	28.2	344	33.2	344	33.3	344	33.2
450.soplex	294	28.3	294	28.4	294	28.3	282	29.6	281	29.7	281	29.7
453.povray	190	28.0	190	28.1	190	28.1	142	37.5	142	37.5	142	37.5
454.calculix	271	30.4	271	30.4	271	30.4	269	30.6	269	30.6	270	30.6
459.GemsFDTD	323	32.8	319	33.2	319	33.3	298	35.7	298	35.6	298	35.7
465.tonto	383	25.7	383	25.7	383	25.7	331	29.7	331	29.7	331	29.7
470.lbm	246	55.9	246	55.9	246	55.9	289	47.6	290	47.4	291	47.2
481.wrf	269	41.6	269	41.6	269	41.6	241	46.3	241	46.4	240	46.6
482.sphinx3	477	40.9	477	40.9	477	40.9	414	47.1	414	47.1	414	47.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 The Windows command 'start' was used to bind copies to cores.
 (For details, please see the config file.)

General Notes

OMP_NUM_THREADS set to number of processor cores
 KMP_AFFINITY set to granularity=fine,scatter



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Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 /Qlowercase
 416.gamess: -DSPEC_CPU_P64
 433.milc: -DSPEC_CPU_P64
 434.zeusmp: -DSPEC_CPU_P64
 435.gromacs: -DSPEC_CPU_P64
 436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
 437.leslie3d: -DSPEC_CPU_P64
 444.namd: -DSPEC_CPU_P64 /TP
 447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 450.soplex: -DSPEC_CPU_P64
 453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
 459.GemsFDTD: -DSPEC_CPU_P64
 465.tonto: -DSPEC_CPU_P64
 470.lbm: -DSPEC_CPU_P64
 481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Continued on next page



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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qparallel
-Qauto-ilp32 /F1000000000
```

```
482.sphinx3: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qauto-ilp32 /F1000000000
```

C++ benchmarks:

```
444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
shlW64M.lib -link /FORCE:MULTIPLE
```

```
447.dealII: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch
-Qansi-alias -Qscalar-rep- -Qauto-ilp32 /F1000000000
shlW64M.lib -link /FORCE:MULTIPLE
```

Continued on next page

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Page 4



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Peak Optimization Flags (Continued)

450.soplex: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qauto-ilp32 /F1000000000 sh1W64M.lib
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias
-Qscalar-rep- /F1000000000

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel
/F1000000000

465.tonto: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc
/F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000

436.cactusADM: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2
-Qauto-ilp32 /F1000000000

454.calculix: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20101221.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20101221.xml>



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