SPEC® CFP2006 Result

HITACHI

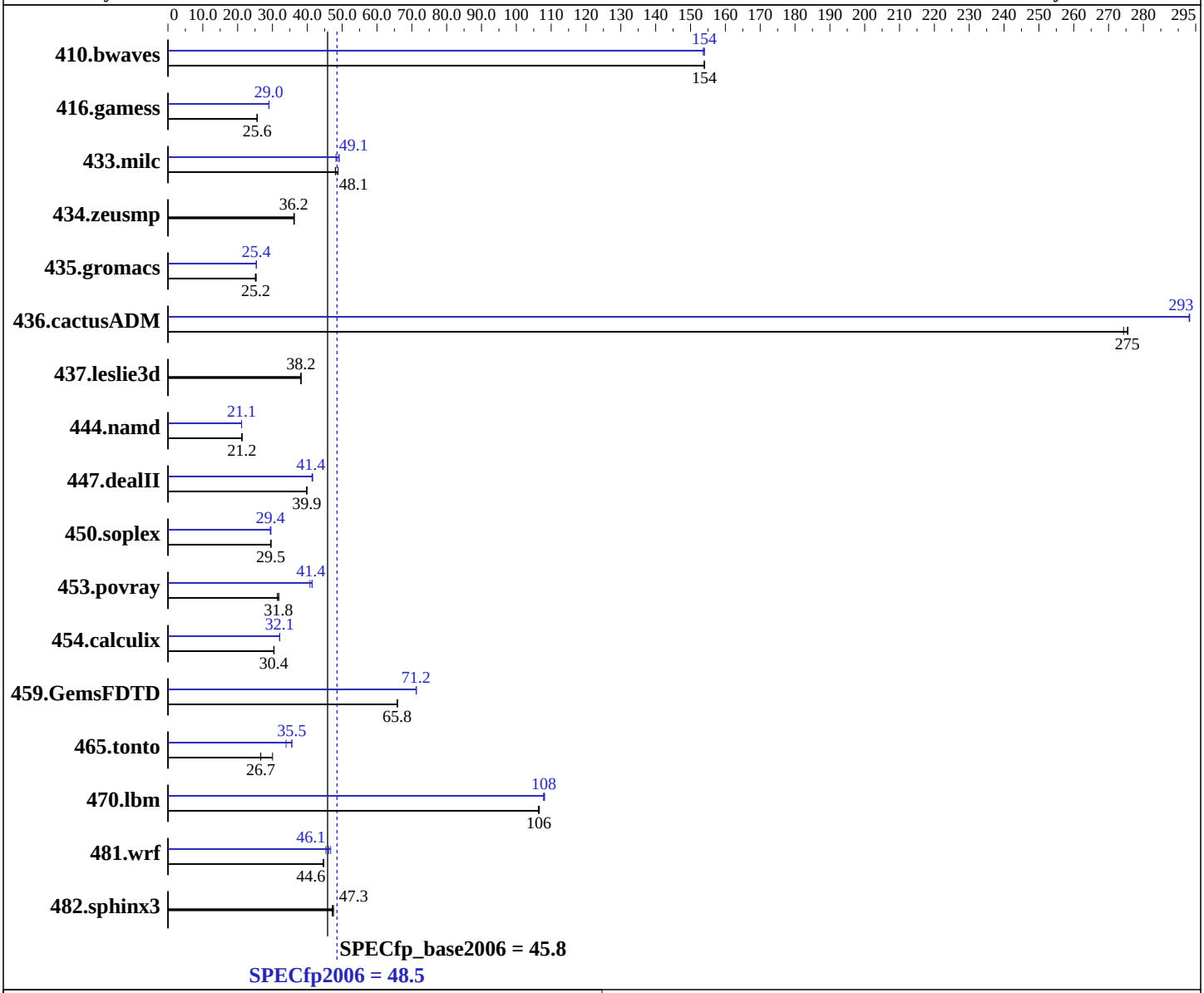
BladeSymphony 320 (Intel Xeon X5680)

SPECfp®2006 = 48.5

SPECfp_base2006 = 45.8

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Sep-2010
Hardware Availability: May-2010
Software Availability: Dec-2009



Hardware

CPU Name:	Intel Xeon X5680
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Compiler 11.1 for Linux Build 20091130 Package ID: l_cproc_p_11.1.064 Intel Fortran Compiler 11.1 for Linux Build 20091130 Package ID: l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-user run level 3

Continued on next page

SPEC CFP2006 Result

HITACHI

BladeSymphony 320 (Intel Xeon X5680)

SPECfp2006 = 48.5

SPECfp_base2006 = 45.8

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Sep-2010
Hardware Availability: May-2010
Software Availability: Dec-2009

L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB(6 x 8 GB PC3-10600R, 2 rank, CL9-9-9)
Disk Subsystem: 2 x 146 GB 10000 rpm Fibre Channel RAID1 configuration
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**88.3**	**154**	88.3	154	88.3	154	88.5	154	**88.3**	**154**	88.3	154
416.gamess	**765**	**25.6**	764	25.6	766	25.6	676	29.0	**676**	**29.0**	674	29.0
433.milc	191	48.1	188	48.8	**191**	**48.1**	187	49.2	190	48.2	**187**	**49.1**
434.zeusmp	**252**	**36.2**	252	36.1	251	36.3	**252**	**36.2**	252	36.1	251	36.3
435.gromacs	282	25.3	**283**	**25.2**	285	25.1	281	25.4	282	25.3	**281**	**25.4**
436.cactusADM	43.6	274	43.4	276	**43.4**	**275**	**40.8**	**293**	40.8	293	40.7	293
437.leslie3d	246	38.2	**246**	**38.2**	246	38.2	246	38.2	**246**	**38.2**	246	38.2
444.namd	377	21.3	**377**	**21.2**	378	21.2	379	21.1	380	21.1	**380**	**21.1**
447.dealII	288	39.8	287	39.9	**287**	**39.9**	**276**	**41.4**	275	41.6	277	41.4
450.soplex	283	29.4	**283**	**29.5**	281	29.6	284	29.4	**283**	**29.4**	282	29.6
453.povray	**167**	**31.8**	169	31.4	167	31.8	129	41.4	130	40.8	**129**	**41.4**
454.calculix	272	30.4	271	30.5	**271**	**30.4**	257	32.1	257	32.0	**257**	**32.1**
459.GemsFDTD	161	65.9	**161**	**65.8**	161	65.7	149	71.3	149	71.2	**149**	**71.2**
465.tonto	328	30.0	**369**	**26.7**	370	26.6	277	35.6	**277**	**35.5**	290	33.9
470.lbm	129	107	**129**	**106**	129	106	127	108	**127**	**108**	127	108
481.wrf	251	44.6	**250**	**44.6**	250	44.7	239	46.7	**242**	**46.1**	246	45.3
482.sphinx3	**412**	**47.3**	410	47.5	413	47.1	**412**	**47.3**	410	47.5	413	47.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

Platform Notes

```
BIOS Settings:
Intel HT Technology = Disabled
NUMA = Disabled
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -parallel -ansi-alias -auto-ilp32

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Continued on next page

Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel`

416.gamess: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll2 -Ob0 -opt-prefetch -parallel`

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-inline-calloc -opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -auto-ilp32`

436.cactusADM: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `Same as 454.calculix`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.03.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.03.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 14:53:26 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 October 2010.