



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

### SPECfp<sup>®</sup>\_rate2006 = 89.9

PRIMERGY TX200 S6, Intel Xeon E5630, 2.53 GHz

### SPECfp\_rate\_base2006 = 86.6

CPU2006 license: 19

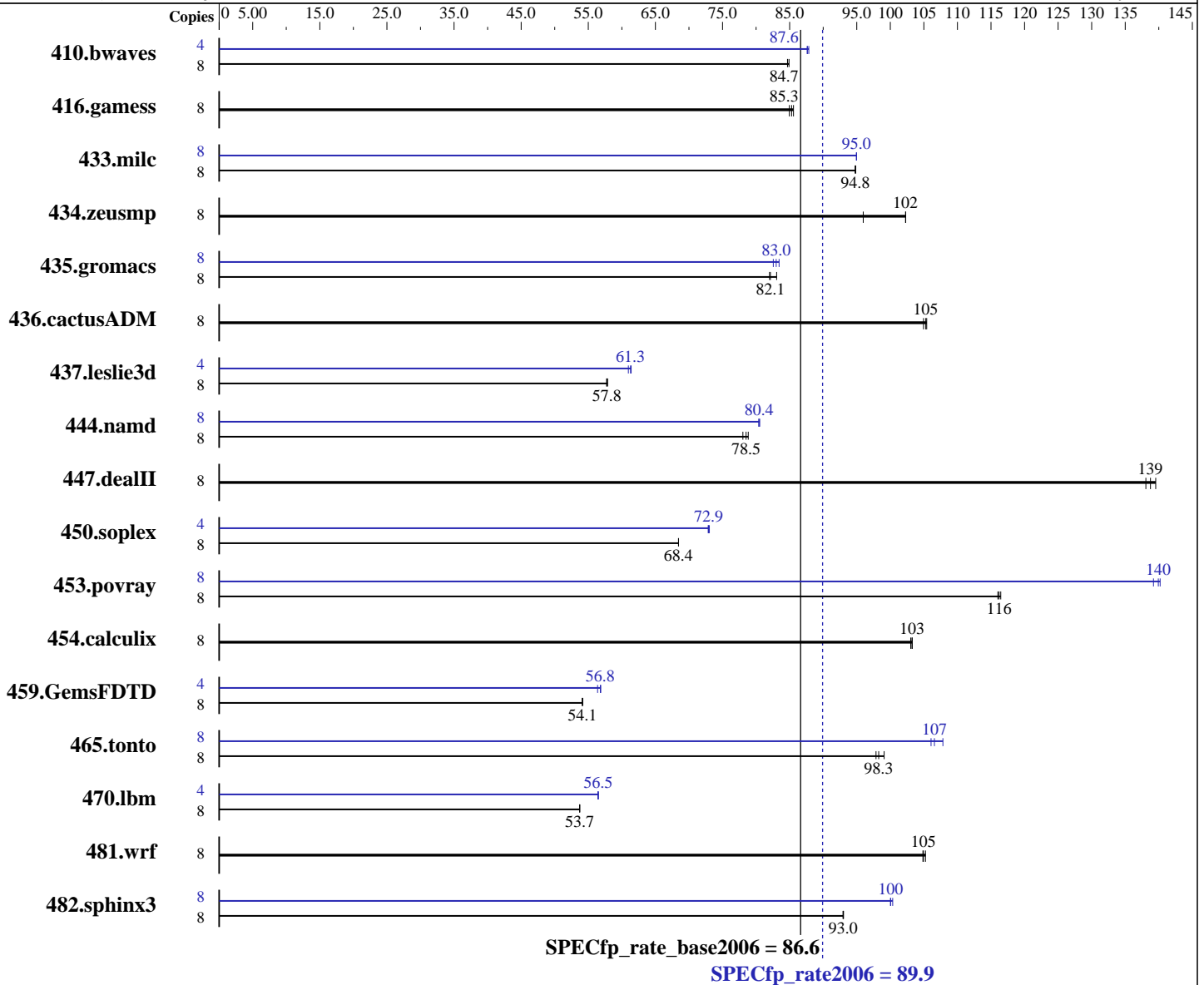
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2010

Hardware Availability: Jul-2010

Software Availability: May-2010



### Hardware

CPU Name: Intel Xeon E5630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.8 GHz  
 CPU MHz: 2533  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECfp\_rate2006 = **89.9**

PRIMERGY TX200 S6, Intel Xeon E5630, 2.53 GHz

SPECfp\_rate\_base2006 = **86.6**

CPU2006 license: 19

Test date: Aug-2010

Test sponsor: Fujitsu

Hardware Availability: Jul-2010

Tested by: Fujitsu

Software Availability: May-2010

L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
Disk Subsystem: 1 x SATA, 160 GB, 5.4 krpm  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	8	1280	84.9	<b>1284</b>	<b>84.7</b>	1284	84.7	4	620	87.6	619	87.9	<b>620</b>	<b>87.6</b>		
416.gamess	8	1830	85.6	1844	85.0	<b>1836</b>	<b>85.3</b>	8	1830	85.6	1844	85.0	<b>1836</b>	<b>85.3</b>		
433.milc	8	775	94.8	774	94.8	<b>775</b>	<b>94.8</b>	8	773	95.0	<b>773</b>	<b>95.0</b>	773	95.0		
434.zeusmp	8	<b>712</b>	<b>102</b>	759	96.0	712	102	8	<b>712</b>	<b>102</b>	759	96.0	712	102		
435.gromacs	8	688	83.1	697	82.0	<b>695</b>	<b>82.1</b>	8	692	82.6	<b>688</b>	<b>83.0</b>	685	83.4		
436.cactusADM	8	911	105	<b>908</b>	<b>105</b>	907	105	8	911	105	<b>908</b>	<b>105</b>	907	105		
437.leslie3d	8	<b>1302</b>	<b>57.8</b>	1299	57.9	1302	57.7	4	617	61.0	<b>613</b>	<b>61.3</b>	613	61.4		
444.namd	8	<b>817</b>	<b>78.5</b>	822	78.0	814	78.8	8	797	80.5	798	80.4	<b>798</b>	<b>80.4</b>		
447.dealII	8	656	140	<b>660</b>	<b>139</b>	663	138	8	656	140	<b>660</b>	<b>139</b>	663	138		
450.soplex	8	975	68.4	<b>975</b>	<b>68.4</b>	975	68.5	4	458	72.9	<b>458</b>	<b>72.9</b>	457	73.0		
453.povray	8	365	116	<b>366</b>	<b>116</b>	367	116	8	<b>304</b>	<b>140</b>	304	140	306	139		
454.calculix	8	<b>639</b>	<b>103</b>	641	103	639	103	8	<b>639</b>	<b>103</b>	641	103	639	103		
459.GemsFDTD	8	1567	54.2	1569	54.1	<b>1568</b>	<b>54.1</b>	4	<b>747</b>	<b>56.8</b>	746	56.9	753	56.4		
465.tonto	8	804	97.9	<b>801</b>	<b>98.3</b>	795	99.1	8	<b>739</b>	<b>107</b>	730	108	742	106		
470.lbm	8	2046	53.7	<b>2046</b>	<b>53.7</b>	2046	53.7	4	974	56.4	<b>973</b>	<b>56.5</b>	972	56.5		
481.wrf	8	852	105	<b>852</b>	<b>105</b>	849	105	8	852	105	<b>852</b>	<b>105</b>	849	105		
482.sphinx3	8	<b>1677</b>	<b>93.0</b>	1678	92.9	1676	93.0	8	1554	100	<b>1558</b>	<b>100</b>	1559	100		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 1066 MHz.  
BIOS configuration:  
Data Reuse Optimization = Disable



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECfp\_rate2006 = 89.9**

PRIMERGY TX200 S6, Intel Xeon E5630, 2.53 GHz

**SPECfp\_rate\_base2006 = 86.6**

**CPU2006 license:** 19  
**Test sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test date:** Aug-2010  
**Hardware Availability:** Jul-2010  
**Software Availability:** May-2010

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECfp\_rate2006 = 89.9**

PRIMERGY TX200 S6, Intel Xeon E5630, 2.53 GHz

**SPECfp\_rate\_base2006 = 86.6**

**CPU2006 license:** 19  
**Test sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test date:** Aug-2010  
**Hardware Availability:** Jul-2010  
**Software Availability:** May-2010

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
  
482.sphinx3: icc -m32  
  
C++ benchmarks (except as noted below):  
icpc -m64  
  
450.soplex: icpc -m32  
  
Fortran benchmarks:  
ifort -m64  
  
Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp\_rate2006 = 89.9

PRIMERGY TX200 S6, Intel Xeon E5630, 2.53 GHz

SPECfp\_rate\_base2006 = 86.6

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2010

Hardware Availability: Jul-2010

Software Availability: May-2010

## Peak Optimization Flags (Continued)

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECfp\_rate2006 = 89.9**

PRIMERGY TX200 S6, Intel Xeon E5630, 2.53 GHz

**SPECfp\_rate\_base2006 = 86.6**

**CPU2006 license:** 19

**Test sponsor:** Fujitsu

**Tested by:** Fujitsu

**Test date:** Aug-2010

**Hardware Availability:** Jul-2010

**Software Availability:** May-2010

## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 14:51:08 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 12 October 2010.