SPEC® CFP2006 Result

Fujitsu

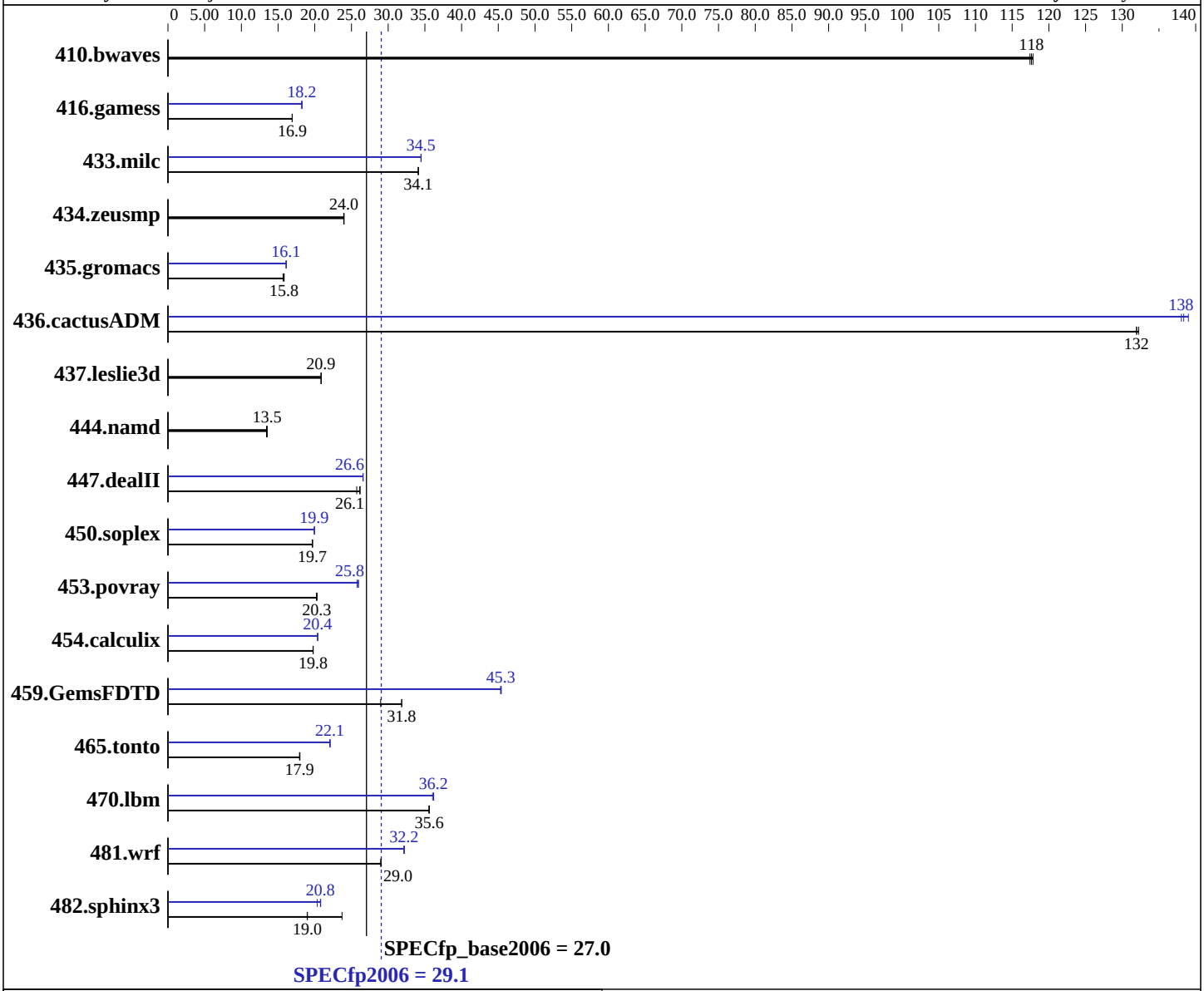
PRIMERGY TX200 S6, Intel Xeon E5507, 2.26 GHz

SPECfp®2006 = 29.1

SPECfp_base2006 = 27.0

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2010
Hardware Availability: Jul-2010
Software Availability: May-2010



Hardware

CPU Name:	Intel Xeon E5507
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem: 1 x SATA, 160 GB, 5.4 krpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	115	118	116	117	**116**	**118**	115	118	116	117	**116**	**118**
416.gamess	1158	16.9	1156	16.9	**1156**	**16.9**	**1074**	**18.2**	1072	18.3	1075	18.2
433.milc	**269**	**34.1**	269	34.1	269	34.1	266	34.5	**266**	**34.5**	266	34.5
434.zeusmp	380	23.9	**379**	**24.0**	379	24.0	380	23.9	**379**	**24.0**	379	24.0
435.gromacs	456	15.7	451	15.8	**451**	**15.8**	**444**	**16.1**	443	16.1	444	16.1
436.cactusADM	90.6	132	**90.6**	**132**	90.4	132	86.6	138	86.0	139	**86.4**	**138**
437.leslie3d	450	20.9	451	20.9	**451**	**20.9**	450	20.9	451	20.9	**451**	**20.9**
444.namd	597	13.4	**594**	**13.5**	594	13.5	597	13.4	**594**	**13.5**	594	13.5
447.dealII	**438**	**26.1**	445	25.7	437	26.2	431	26.6	**430**	**26.6**	430	26.6
450.soplex	424	19.7	424	19.7	**424**	**19.7**	418	20.0	**418**	**19.9**	418	19.9
453.povray	263	20.2	**262**	**20.3**	262	20.3	205	26.0	206	25.8	**206**	**25.8**
454.calculix	417	19.8	**417**	**19.8**	418	19.8	405	20.4	405	20.4	**405**	**20.4**
459.GemsFDTD	333	31.8	**333**	**31.8**	366	29.0	234	45.4	234	45.3	**234**	**45.3**
465.tonto	547	18.0	**549**	**17.9**	549	17.9	445	22.1	447	22.0	**446**	**22.1**
470.lbm	386	35.6	**386**	**35.6**	387	35.5	380	36.2	381	36.1	**380**	**36.2**
481.wrf	385	29.0	385	29.0	**385**	**29.0**	348	32.1	**347**	**32.2**	347	32.2
482.sphinx3	822	23.7	1028	19.0	**1026**	**19.0**	937	20.8	959	20.3	**938**	**20.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 800 MHz.
BIOS configuration:
Data Reuse Optimization = Disable

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

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General Notes (Continued)

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-ansi-alias`

470.lbm: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32 -unroll2`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

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Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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