# SPEC® CINT2006 Result

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## Fujitsu

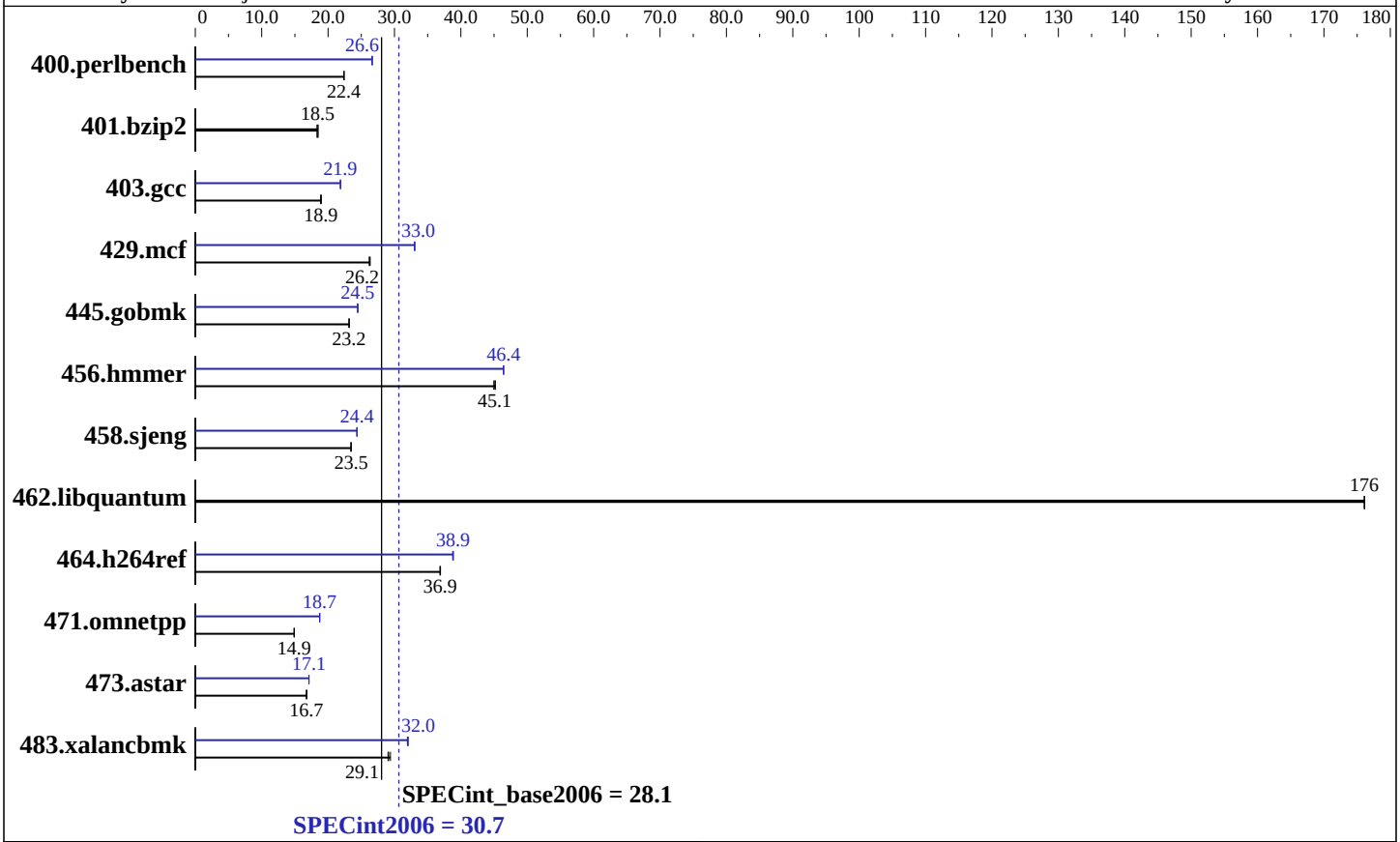
PRIMERGY RX100 S6, Intel Core i3-550, 3.20 GHz

SPECint®2006 = 30.7

SPECint_base2006 = 28.1

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Sep-2010
**Hardware Availability:** Aug-2010
**Software Availability:** Jan-2010




## Hardware

| | |
|---|---|
| CPU Name: | Intel Core i3-550 |
| CPU Characteristics: | |
| CPU MHz: | 3200 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 4 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 16 GB (4x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC) |
| Disk Subsystem: | 1 x SSD SATA, 64 GB |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064 |
| Auto Parallel: | Yes |
| File System: | ext3 |
| System State: | Run Level 3 (multi user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | **436** | **22.4** | 436 | 22.4 | 436 | 22.4 | **367** | **26.6** | 367 | 26.6 | 367 | 26.7 |
| 401.bzip2 | 522 | 18.5 | **523** | **18.5** | 527 | 18.3 | 522 | 18.5 | **523** | **18.5** | 527 | 18.3 |
| 403.gcc | 424 | 19.0 | **425** | **18.9** | 426 | 18.9 | 368 | 21.9 | 368 | 21.9 | **368** | **21.9** |
| 429.mcf | 346 | 26.3 | 348 | 26.2 | **348** | **26.2** | 276 | 33.0 | 276 | 33.1 | **276** | **33.0** |
| 445.gobmk | **453** | **23.2** | 453 | 23.1 | 453 | 23.2 | 429 | 24.5 | **429** | **24.5** | 429 | 24.5 |
| 456.hmmer | **207** | **45.1** | 208 | 45.0 | 206 | 45.2 | 201 | 46.4 | 201 | 46.4 | **201** | **46.4** |
| 458.sjeng | **515** | **23.5** | 515 | 23.5 | 516 | 23.4 | 497 | 24.3 | **497** | **24.4** | 496 | 24.4 |
| 462.libquantum | **118** | **176** | 118 | 176 | 118 | 176 | **118** | **176** | 118 | 176 | 118 | 176 |
| 464.h264ref | 600 | 36.9 | 599 | 36.9 | **600** | **36.9** | 571 | 38.8 | 570 | 38.9 | **570** | **38.9** |
| 471.omnetpp | 419 | 14.9 | **420** | **14.9** | 420 | 14.9 | 334 | 18.7 | 333 | 18.7 | **333** | **18.7** |
| 473.astar | **419** | **16.7** | 419 | 16.7 | 418 | 16.8 | 410 | 17.1 | 410 | 17.1 | **410** | **17.1** |
| 483.xalancbmk | 235 | 29.4 | **237** | **29.1** | 238 | 29.0 | 215 | 32.1 | 216 | 31.9 | **216** | **32.0** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:
Intel HT Technology = Disable

## General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

# Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m32

Continued on next page

## Peak Compiler Invocation (Continued)

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: basepeak = yes

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

Continued on next page

## Peak Optimization Flags (Continued)

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml


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For other inquiries, please contact webmaster@spec.org.

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