



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®2006 = 34.3

IBM System x3550 M3 (Intel Xeon E5520)

SPECfp_base2006 = 31.9

CPU2006 license: 11

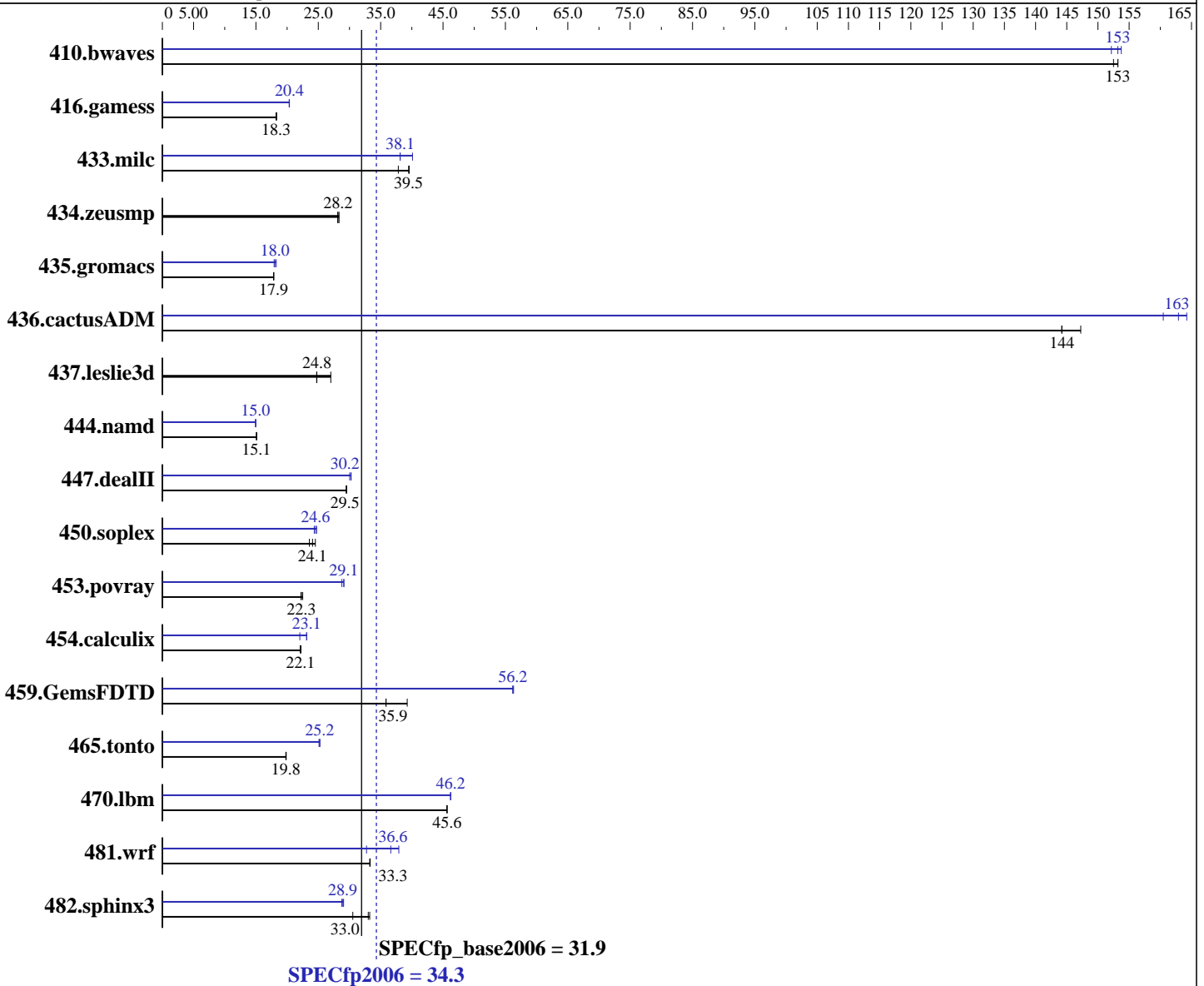
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jun-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5520
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SuSe Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB PC3-10600R CL9, 2 Rank)
 Disk Subsystem: 1 x 73 GB SAS, 15000 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	89.1	152	<u>88.7</u>	<u>153</u>	88.7	153	89.3	152	<u>88.7</u>	<u>153</u>	88.4	154
416.gamess	1069	18.3	1073	18.3	<u>1072</u>	<u>18.3</u>	961	20.4	<u>961</u>	<u>20.4</u>	963	20.3
433.milc	243	37.8	232	39.6	<u>233</u>	<u>39.5</u>	229	40.1	241	38.1	<u>241</u>	<u>38.1</u>
434.zeusmp	324	28.1	321	28.3	<u>323</u>	<u>28.2</u>	324	28.1	321	28.3	<u>323</u>	<u>28.2</u>
435.gromacs	400	17.9	401	17.8	<u>400</u>	<u>17.9</u>	<u>396</u>	<u>18.0</u>	391	18.3	398	17.9
436.cactusADM	82.9	144	81.2	147	<u>82.9</u>	<u>144</u>	<u>73.4</u>	<u>163</u>	74.5	160	72.8	164
437.leslie3d	<u>379</u>	<u>24.8</u>	380	24.7	348	27.0	<u>379</u>	<u>24.8</u>	380	24.7	348	27.0
444.namd	<u>531</u>	<u>15.1</u>	532	15.1	531	15.1	<u>536</u>	<u>15.0</u>	536	15.0	536	15.0
447.dealII	388	29.5	388	29.5	<u>388</u>	<u>29.5</u>	381	30.0	378	30.3	<u>378</u>	<u>30.2</u>
450.soplex	354	23.6	340	24.5	<u>346</u>	<u>24.1</u>	342	24.4	337	24.7	<u>339</u>	<u>24.6</u>
453.povray	239	22.2	<u>239</u>	<u>22.3</u>	236	22.5	183	29.1	185	28.8	<u>183</u>	<u>29.1</u>
454.calculix	371	22.2	373	22.1	<u>373</u>	<u>22.1</u>	356	23.2	374	22.0	<u>357</u>	<u>23.1</u>
459.GemsFDTD	<u>296</u>	<u>35.9</u>	296	35.8	270	39.3	189	56.2	<u>189</u>	<u>56.2</u>	188	56.3
465.tonto	497	19.8	<u>497</u>	<u>19.8</u>	495	19.9	392	25.1	390	25.2	<u>390</u>	<u>25.2</u>
470.lbm	301	45.7	301	45.6	<u>301</u>	<u>45.6</u>	297	46.2	<u>297</u>	<u>46.2</u>	297	46.2
481.wrf	336	33.3	<u>336</u>	<u>33.3</u>	335	33.3	<u>305</u>	<u>36.6</u>	295	37.9	341	32.7
482.sphinx3	<u>590</u>	<u>33.0</u>	638	30.5	586	33.2	<u>673</u>	<u>28.9</u>	677	28.8	671	29.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Turbo Mode Enable
 Turbo Boost set to Traditional
 CPU C State Enable

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
 'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.lelie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100804.xml>



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