



SPEC[®] CFP2006 Result

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Bull SAS

SPECfp[®]2006 = 48.4

NovaScale R460 F2 (Intel Xeon X5680, 3.33 GHz)

SPECfp_base2006 = 45.0

CPU2006 license: 20

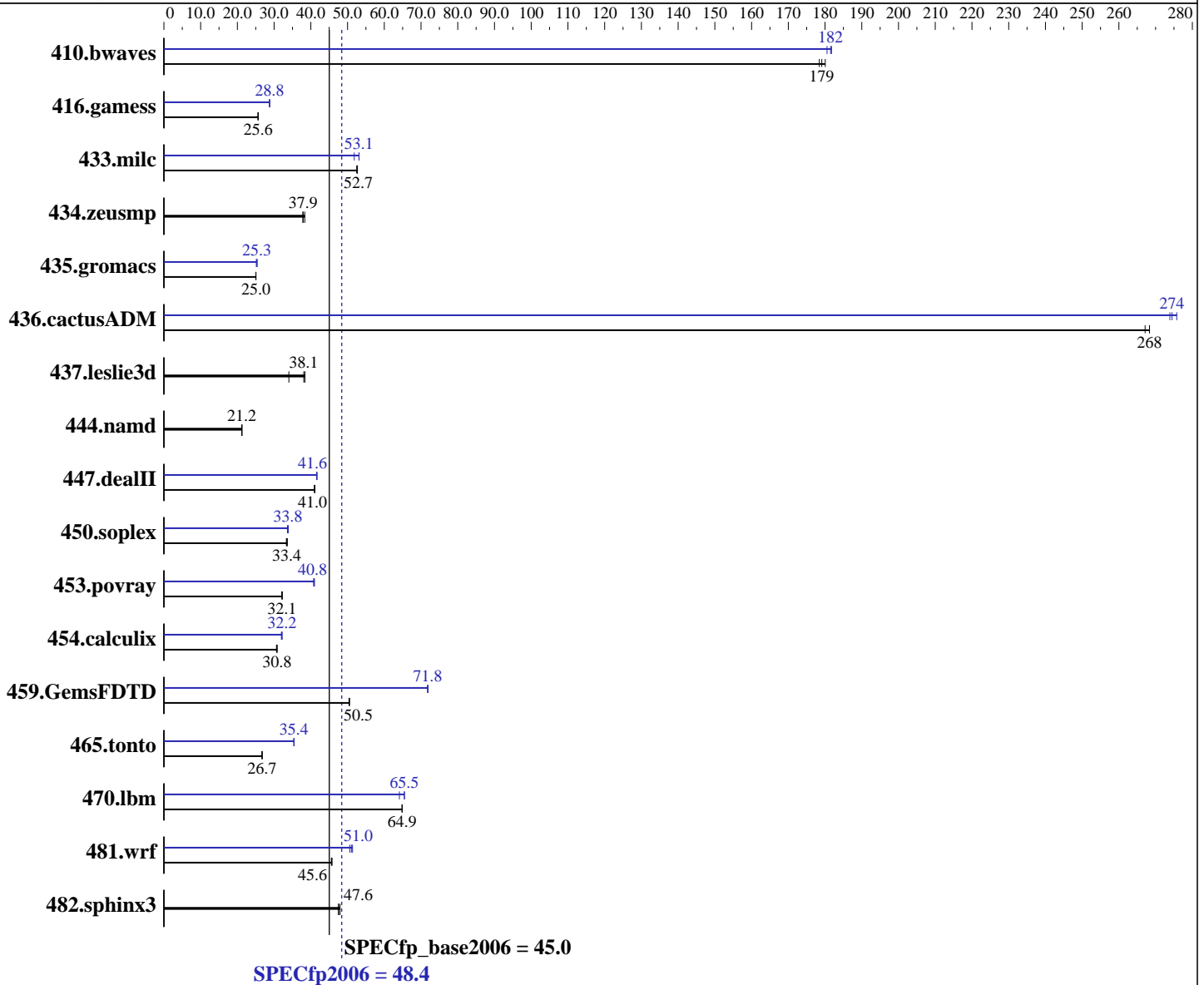
Test date: Mar-2010

Test sponsor: Bull SAS

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon X5680
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: I_cproc_p_11.1.064, I_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB DDR3-1333 DR RDIMM, CL9, ECC)
Disk Subsystem: 1 x 146 GB 15000 RPM SAS
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	76.2	178	<u>75.9</u>	<u>179</u>	75.5	180	<u>74.9</u>	<u>182</u>	75.3	181	74.8	182
416.gamess	764	25.6	<u>763</u>	<u>25.6</u>	763	25.7	682	28.7	<u>680</u>	<u>28.8</u>	680	28.8
433.milc	<u>174</u>	<u>52.7</u>	175	52.5	174	52.7	173	53.1	<u>173</u>	<u>53.1</u>	177	51.8
434.zeusmp	<u>240</u>	<u>37.9</u>	241	37.8	237	38.4	<u>240</u>	<u>37.9</u>	241	37.8	237	38.4
435.gromacs	<u>285</u>	<u>25.0</u>	285	25.1	285	25.0	284	25.2	281	25.4	<u>282</u>	<u>25.3</u>
436.cactusADM	44.7	267	44.5	268	<u>44.5</u>	<u>268</u>	43.6	274	43.3	276	<u>43.5</u>	<u>274</u>
437.leslie3d	276	34.0	245	38.4	<u>247</u>	<u>38.1</u>	276	34.0	245	38.4	<u>247</u>	<u>38.1</u>
444.namd	<u>378</u>	<u>21.2</u>	378	21.2	378	21.2	<u>378</u>	<u>21.2</u>	378	21.2	378	21.2
447.dealII	278	41.1	<u>279</u>	<u>41.0</u>	279	41.0	<u>275</u>	<u>41.6</u>	275	41.7	275	41.6
450.soplex	248	33.6	250	33.3	<u>249</u>	<u>33.4</u>	246	33.8	248	33.6	<u>247</u>	<u>33.8</u>
453.povray	165	32.2	166	32.1	<u>165</u>	<u>32.1</u>	130	40.8	130	41.0	<u>130</u>	<u>40.8</u>
454.calculix	269	30.6	<u>268</u>	<u>30.8</u>	268	30.8	257	32.2	<u>257</u>	<u>32.2</u>	258	32.0
459.GemsFDTD	<u>210</u>	<u>50.5</u>	211	50.4	210	50.5	148	71.9	<u>148</u>	<u>71.8</u>	148	71.8
465.tonto	368	26.8	<u>368</u>	<u>26.7</u>	369	26.7	278	35.5	<u>278</u>	<u>35.4</u>	278	35.4
470.lbm	212	64.9	<u>212</u>	<u>64.9</u>	212	64.8	214	64.1	<u>210</u>	<u>65.5</u>	210	65.5
481.wrf	<u>245</u>	<u>45.6</u>	244	45.8	245	45.6	<u>219</u>	<u>51.0</u>	221	50.6	217	51.4
482.sphinx3	406	48.0	410	47.5	<u>410</u>	<u>47.6</u>	406	48.0	410	47.5	<u>410</u>	<u>47.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Data Reuse = Disabled (Default = Enabled)

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

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General Notes (Continued)

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
The Dell PowerEdge R710 and
the Bull NovaScale R460 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R710 model.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias`

470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealIII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

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Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.02.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.02.xml>

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