



SPEC® CFP2006 Result

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Dell Inc.

PowerEdge R710
(Intel Xeon E5502, 1.86 GHz)

SPECfp®2006 = 24.4

SPECfp_base2006 = 22.8

CPU2006 license: 55

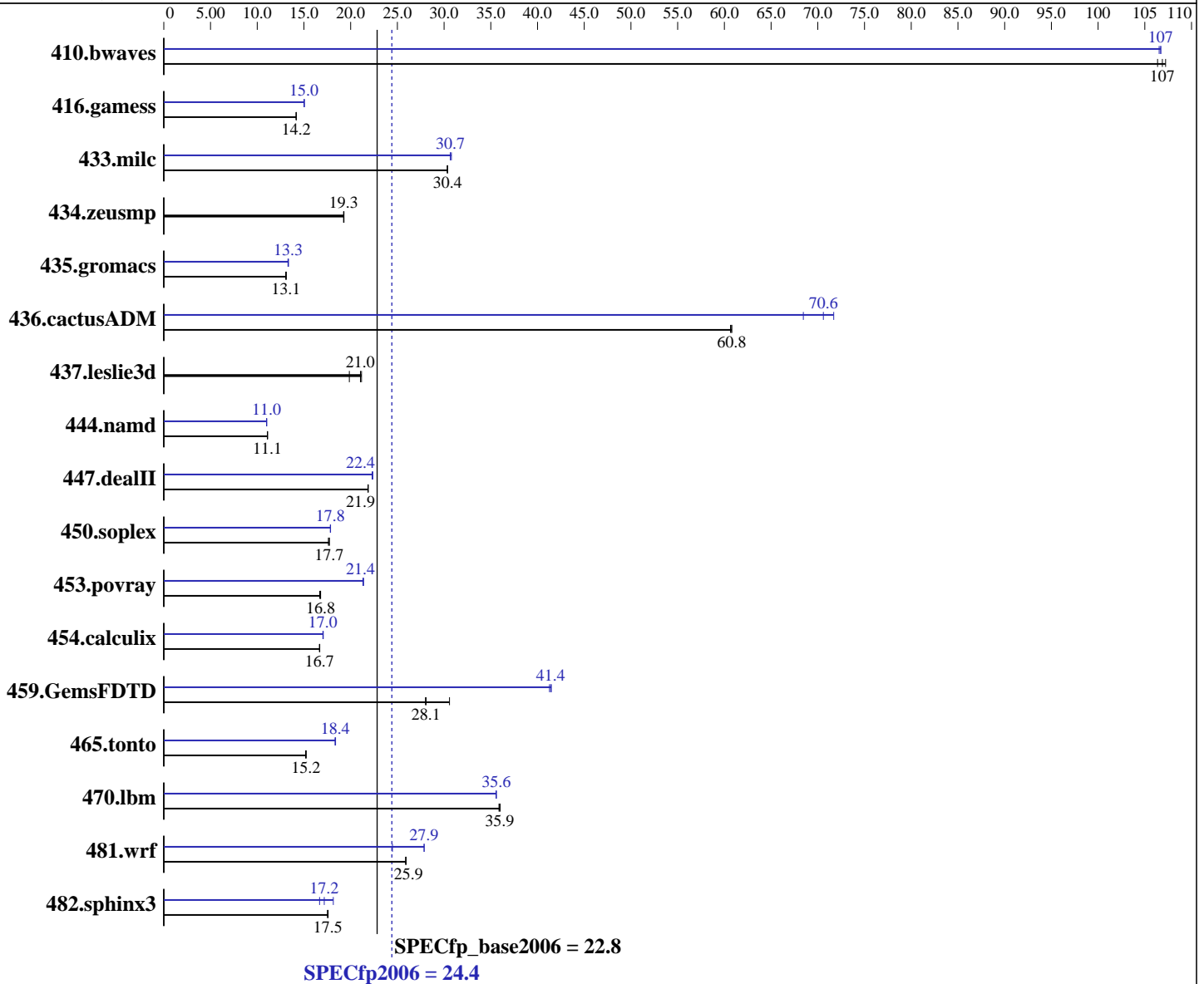
Test sponsor: Dell Inc.

Tested by: Bull SAS

Test date: Mar-2010

Hardware Availability: Mar-2009

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics:
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ReiserFS

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB PC3-10600R, 2 Rank, CL9-9-9, ECC, running at 800 MHz)
Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	127	107	128	106	<u>127</u>	<u>107</u>	<u>127</u>	<u>107</u>	127	107	128	107
416.gamess	1379	14.2	1385	14.1	<u>1380</u>	<u>14.2</u>	<u>1303</u>	<u>15.0</u>	1303	15.0	1302	15.0
433.milc	302	30.3	<u>302</u>	<u>30.4</u>	302	30.4	298	30.8	<u>299</u>	<u>30.7</u>	299	30.7
434.zeusmp	473	19.2	<u>472</u>	<u>19.3</u>	472	19.3	473	19.2	<u>472</u>	<u>19.3</u>	472	19.3
435.gromacs	547	13.1	545	13.1	<u>546</u>	<u>13.1</u>	<u>536</u>	<u>13.3</u>	535	13.3	537	13.3
436.cactusADM	197	60.8	<u>197</u>	<u>60.8</u>	197	60.7	167	71.7	175	68.5	<u>169</u>	<u>70.6</u>
437.leslie3d	<u>447</u>	<u>21.0</u>	473	19.9	444	21.2	<u>447</u>	<u>21.0</u>	473	19.9	444	21.2
444.namd	722	11.1	724	11.1	<u>723</u>	<u>11.1</u>	729	11.0	<u>729</u>	<u>11.0</u>	728	11.0
447.dealII	523	21.9	523	21.9	<u>523</u>	<u>21.9</u>	512	22.4	<u>512</u>	<u>22.4</u>	513	22.3
450.soplex	471	17.7	<u>471</u>	<u>17.7</u>	473	17.6	468	17.8	<u>468</u>	<u>17.8</u>	467	17.8
453.povray	319	16.7	317	16.8	<u>317</u>	<u>16.8</u>	249	21.4	<u>249</u>	<u>21.4</u>	250	21.3
454.calculix	495	16.7	494	16.7	<u>494</u>	<u>16.7</u>	<u>484</u>	<u>17.0</u>	484	17.0	483	17.1
459.GemsFDTD	379	28.0	347	30.6	<u>378</u>	<u>28.1</u>	<u>256</u>	<u>41.4</u>	257	41.3	256	41.5
465.tonto	646	15.2	<u>647</u>	<u>15.2</u>	648	15.2	537	18.3	535	18.4	<u>536</u>	<u>18.4</u>
470.lbm	383	35.9	<u>382</u>	<u>35.9</u>	382	36.0	386	35.6	386	35.6	<u>386</u>	<u>35.6</u>
481.wrf	432	25.9	431	25.9	<u>431</u>	<u>25.9</u>	401	27.9	<u>401</u>	<u>27.9</u>	456	24.5
482.sphinx3	<u>1111</u>	<u>17.5</u>	1112	17.5	1108	17.6	<u>1135</u>	<u>17.2</u>	1074	18.1	1169	16.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
The Dell PowerEdge R710 and
the Bull NovaScale R460 F2 models are electronically equivalent.
The results have been measured on a Bull NovaScale R460 F2 model.



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.xml>



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