SPEC® CFP2006 Result

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NEC Corporation

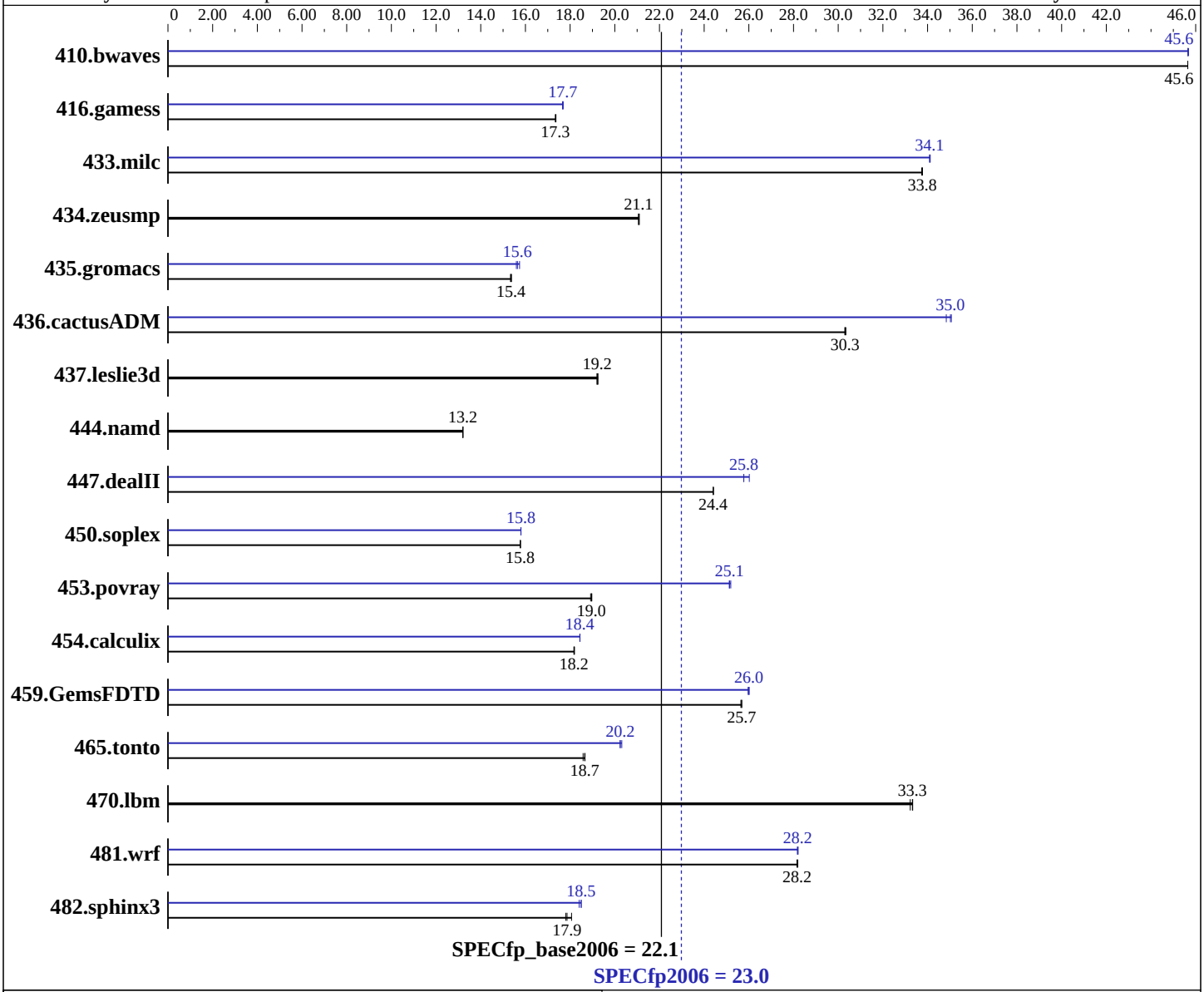
Express5800/GT110b
(Intel Celeron G1101)

SPECfp®2006 = 23.0

SPECfp_base2006 = 22.1

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Mar-2010
Hardware Availability: Apr-2010
Software Availability: Nov-2009



Hardware

CPU Name:	Intel Celeron G1101
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059, l_cprof_p_11.1.059
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

Continued on next page

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L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (4 x 2 GB PC3-10600E running at 1066 MHz)
Disk Subsystem: 1x160 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	298	45.6	298	45.6	**298**	**45.6**	298	45.6	298	45.7	**298**	**45.6**
416.gamess	1128	17.4	**1129**	**17.3**	1129	17.3	**1108**	**17.7**	1109	17.7	1107	17.7
433.milc	272	33.7	**272**	**33.8**	272	33.8	269	34.1	**269**	**34.1**	269	34.1
434.zeusmp	432	21.1	431	21.1	**432**	**21.1**	432	21.1	431	21.1	**432**	**21.1**
435.gromacs	**465**	**15.4**	464	15.4	466	15.3	454	15.7	**456**	**15.6**	458	15.6
436.cactusADM	394	30.3	394	30.3	**394**	**30.3**	**341**	**35.0**	341	35.1	343	34.8
437.leslie3d	488	19.2	490	19.2	**489**	**19.2**	488	19.2	490	19.2	**489**	**19.2**
444.namd	608	13.2	**608**	**13.2**	607	13.2	608	13.2	**608**	**13.2**	607	13.2
447.dealII	**469**	**24.4**	469	24.4	469	24.4	440	26.0	444	25.8	**444**	**25.8**
450.soplex	529	15.8	**529**	**15.8**	529	15.8	**528**	**15.8**	528	15.8	528	15.8
453.povray	281	18.9	**281**	**19.0**	280	19.0	211	25.2	212	25.1	**212**	**25.1**
454.calculix	453	18.2	454	18.2	**454**	**18.2**	447	18.4	447	18.4	**447**	**18.4**
459.GemsFDTD	**413**	**25.7**	414	25.6	413	25.7	**408**	**26.0**	408	26.0	409	26.0
465.tonto	527	18.7	**528**	**18.7**	530	18.6	484	20.3	486	20.2	**486**	**20.2**
470.lbm	412	33.3	414	33.2	**412**	**33.3**	412	33.3	414	33.2	**412**	**33.3**
481.wrf	396	28.2	**397**	**28.2**	397	28.2	396	28.2	**396**	**28.2**	396	28.2
482.sphinx3	**1091**	**17.9**	1095	17.8	1079	18.1	1054	18.5	**1054**	**18.5**	1059	18.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Default BIOS settings were used.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2

Continued on next page

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revE.20100302.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revE.20100302.xml`

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For other inquiries, please contact webmaster@spec.org.

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