



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 26.6

PowerEdge R810 (Intel Xeon E7520, 1.87 GHz)

SPECfp_base2006 = 25.0

CPU2006 license: 55

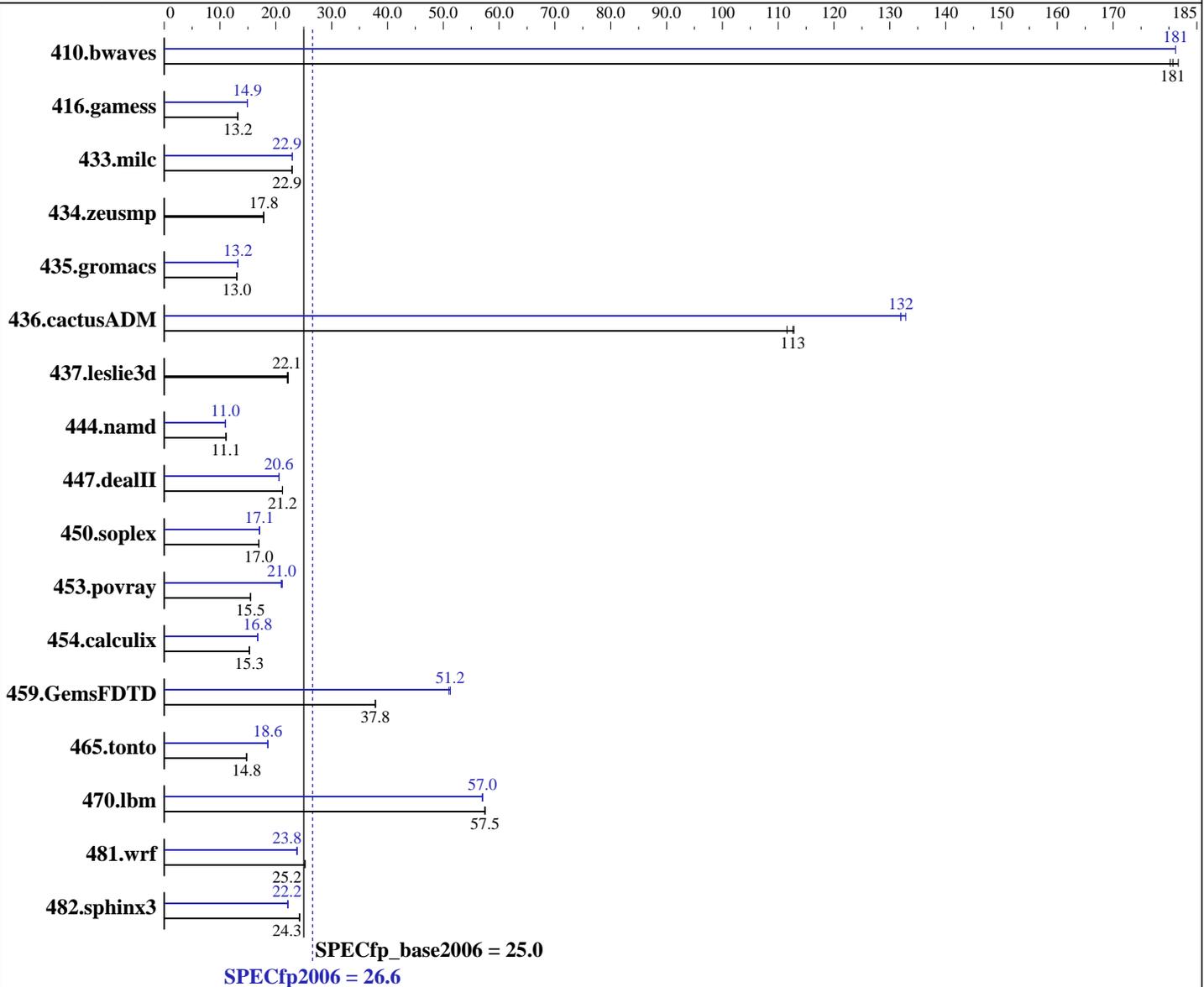
Test date: Mar-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon E7520
 CPU Characteristics:
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux 5 (x86_64) Update 4 errata kernel (RHEL 5.4.z) kernel-2.6.18-164.9.1.el5.x86_64
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3

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L3 Cache: 18 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (32 x 4 GB DDR3-1066 QR RDIMM)
 Disk Subsystem: 6 x 73 GB 15000 RPM SAS
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	75.4	180	74.8	182	<u>75.2</u>	<u>181</u>	75.0	181	75.0	181	<u>75.0</u>	<u>181</u>
416.gamess	1488	13.2	<u>1483</u>	<u>13.2</u>	1482	13.2	1312	14.9	1316	14.9	<u>1314</u>	<u>14.9</u>
433.milc	400	22.9	<u>401</u>	<u>22.9</u>	401	22.9	400	22.9	399	23.0	<u>400</u>	<u>22.9</u>
434.zeusmp	510	17.8	511	17.8	<u>510</u>	<u>17.8</u>	510	17.8	511	17.8	<u>510</u>	<u>17.8</u>
435.gromacs	<u>548</u>	<u>13.0</u>	548	13.0	549	13.0	541	13.2	540	13.2	<u>540</u>	<u>13.2</u>
436.cactusADM	107	112	<u>106</u>	<u>113</u>	106	113	<u>90.5</u>	<u>132</u>	89.9	133	90.6	132
437.leslie3d	423	22.2	426	22.0	<u>424</u>	<u>22.1</u>	423	22.2	426	22.0	<u>424</u>	<u>22.1</u>
444.namd	<u>724</u>	<u>11.1</u>	724	11.1	724	11.1	<u>731</u>	<u>11.0</u>	731	11.0	731	11.0
447.dealII	<u>540</u>	<u>21.2</u>	540	21.2	540	21.2	555	20.6	557	20.6	<u>557</u>	<u>20.6</u>
450.soplex	491	17.0	<u>492</u>	<u>17.0</u>	492	16.9	<u>488</u>	<u>17.1</u>	490	17.0	487	17.1
453.povray	344	15.4	<u>344</u>	<u>15.5</u>	343	15.5	251	21.2	254	21.0	<u>253</u>	<u>21.0</u>
454.calculix	541	15.3	540	15.3	<u>541</u>	<u>15.3</u>	492	16.8	493	16.7	<u>492</u>	<u>16.8</u>
459.GemsFDTD	281	37.8	<u>280</u>	<u>37.8</u>	280	37.9	208	51.0	207	51.2	<u>207</u>	<u>51.2</u>
465.tonto	664	14.8	<u>667</u>	<u>14.8</u>	669	14.7	530	18.6	<u>530</u>	<u>18.6</u>	530	18.6
470.lbm	<u>239</u>	<u>57.5</u>	239	57.4	239	57.5	<u>241</u>	<u>57.0</u>	241	57.1	241	57.0
481.wrf	<u>442</u>	<u>25.2</u>	442	25.3	443	25.2	<u>469</u>	<u>23.8</u>	468	23.8	470	23.8
482.sphinx3	<u>803</u>	<u>24.3</u>	803	24.3	805	24.2	<u>879</u>	<u>22.2</u>	880	22.2	879	22.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

vm.zone_reclaim_mode = 1 in /etc/sysctl.conf file
 BIOS Settings:
 Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.xml>



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