



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®2006 = **45.1**

IBM BladeCenter HS22V (Intel Xeon X5670)

SPECfp_base2006 = **42.4**

CPU2006 license: 11

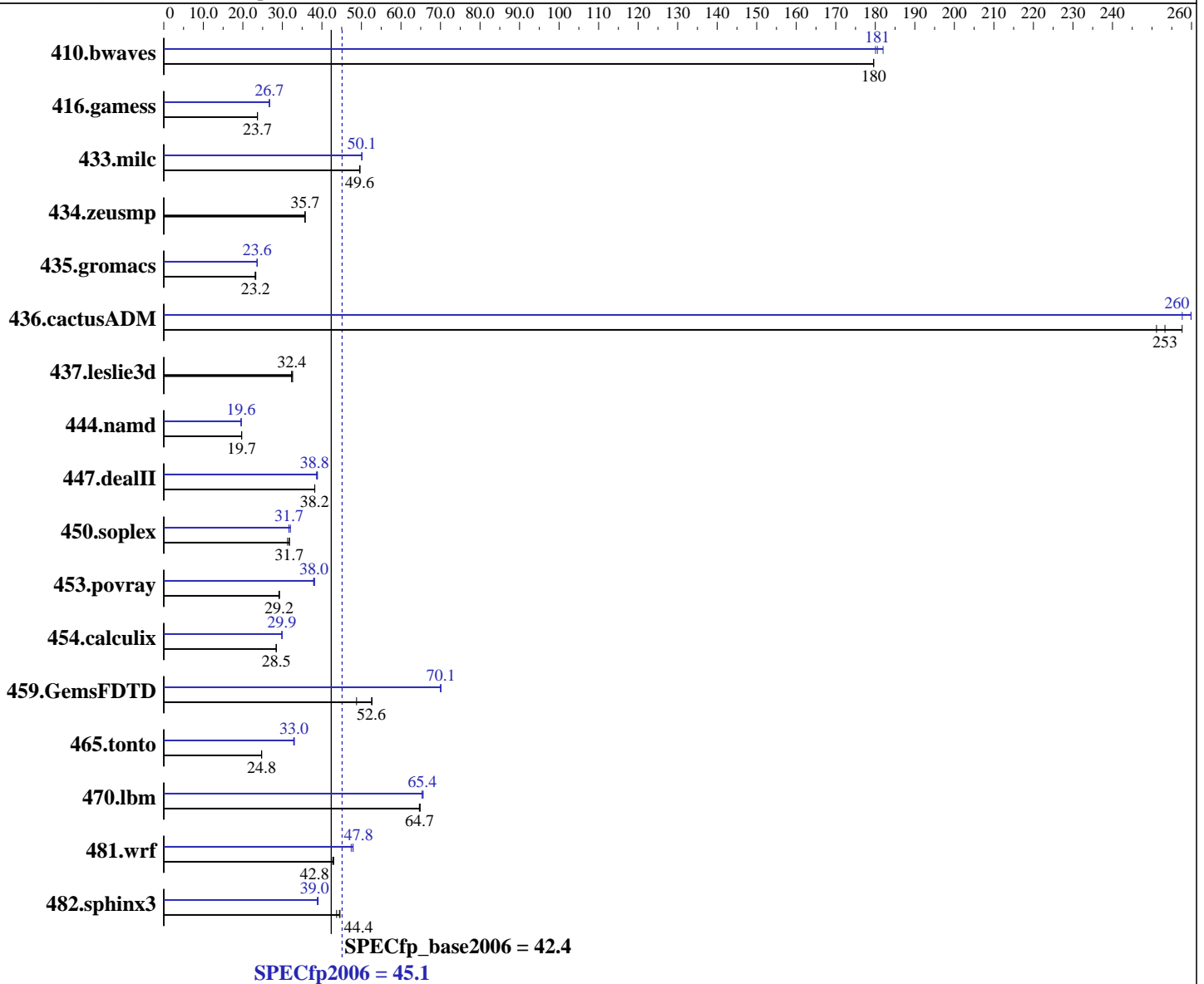
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Feb-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5670
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB PC3-10600R)
Disk Subsystem: 2 x 50 GB SATA, SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	75.7	180	<u>75.7</u>	<u>180</u>	75.7	180	<u>75.3</u>	<u>181</u>	75.5	180	74.7	182
416.gamess	827	23.7	827	23.7	<u>827</u>	<u>23.7</u>	733	26.7	<u>733</u>	<u>26.7</u>	732	26.8
433.milc	<u>185</u>	<u>49.6</u>	185	49.6	185	49.6	183	50.1	<u>183</u>	<u>50.1</u>	183	50.2
434.zeusmp	<u>255</u>	<u>35.7</u>	255	35.7	254	35.8	<u>255</u>	<u>35.7</u>	255	35.7	254	35.8
435.gromacs	<u>308</u>	<u>23.2</u>	309	23.1	307	23.3	302	23.6	<u>302</u>	<u>23.6</u>	302	23.6
436.cactusADM	<u>47.2</u>	<u>253</u>	46.4	258	47.6	251	46.4	258	46.0	260	<u>46.0</u>	<u>260</u>
437.leslie3d	288	32.6	<u>290</u>	<u>32.4</u>	291	32.3	288	32.6	<u>290</u>	<u>32.4</u>	291	32.3
444.namd	<u>407</u>	<u>19.7</u>	407	19.7	408	19.7	<u>409</u>	<u>19.6</u>	409	19.6	410	19.5
447.dealII	299	38.2	<u>300</u>	<u>38.2</u>	300	38.2	<u>294</u>	<u>38.8</u>	294	38.9	296	38.6
450.soplex	262	31.8	<u>263</u>	<u>31.7</u>	266	31.3	260	32.0	<u>263</u>	<u>31.7</u>	263	31.7
453.povray	182	29.3	183	29.1	<u>182</u>	<u>29.2</u>	<u>140</u>	<u>38.0</u>	140	37.9	139	38.2
454.calculix	290	28.5	<u>290</u>	<u>28.5</u>	290	28.4	276	29.9	276	29.9	<u>276</u>	<u>29.9</u>
459.GemsFDTD	<u>202</u>	<u>52.6</u>	201	52.7	218	48.8	<u>151</u>	<u>70.1</u>	152	70.0	151	70.1
465.tonto	398	24.8	<u>398</u>	<u>24.8</u>	398	24.7	298	33.0	<u>298</u>	<u>33.0</u>	299	32.9
470.lbm	213	64.7	212	64.9	<u>212</u>	<u>64.7</u>	<u>210</u>	<u>65.4</u>	209	65.6	210	65.4
481.wrf	<u>261</u>	<u>42.8</u>	260	43.0	261	42.8	<u>233</u>	<u>47.8</u>	233	47.9	236	47.4
482.sphinx3	437	44.6	<u>439</u>	<u>44.4</u>	446	43.7	<u>500</u>	<u>39.0</u>	499	39.0	502	38.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Turbo Mode enabled
Turbo Boost set to Traditional
Power C-States enabled
Demand Scrub disabled

General Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.03.xml>



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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 05:11:55 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 30 March 2010.