



SPEC® CFP2006 Result

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Fujitsu

SPECfp®2006 = **28.3**

PRIMERGY RX300 S6, Intel Xeon E5506, 2.13 GHz

SPECfp_base2006 = **26.4**

CPU2006 license: 19

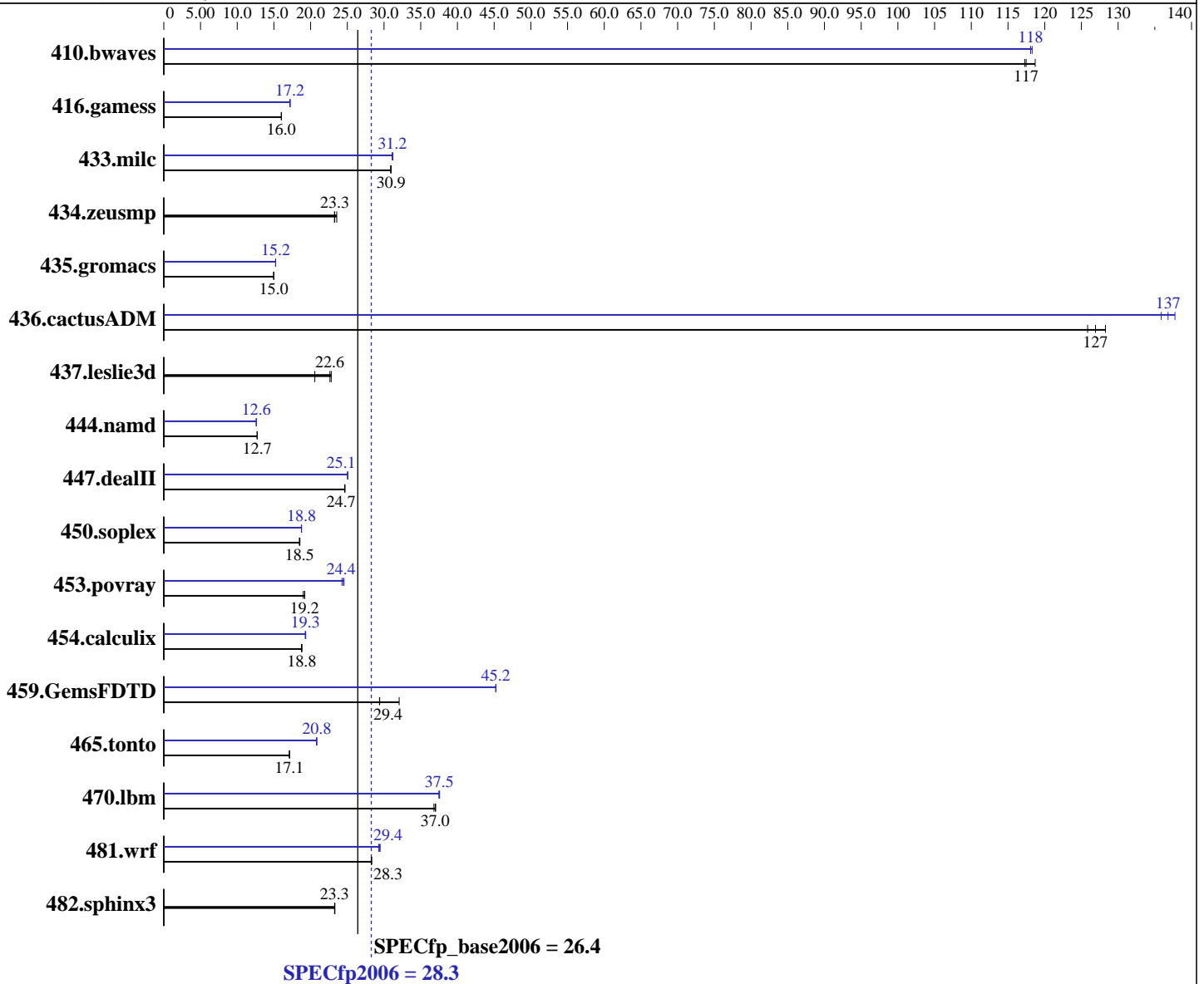
Test date: Jan-2010

Test sponsor: Fujitsu

Hardware Availability: Apr-2010

Tested by: Fujitsu

Software Availability: Jan-2010



Hardware	
CPU Name:	Intel Xeon E5506
CPU Characteristics:	
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software	
Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User Run Level 3

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem: 1 x SATA, 250 GB, 5.4 krpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	116	117	116	117	114	119	115	118	115	118	115	118
416.gamess	1223	16.0	1221	16.0	1224	16.0	1138	17.2	1139	17.2	1139	17.2
433.milc	297	30.9	297	31.0	297	30.9	295	31.1	294	31.2	295	31.2
434.zeusmp	386	23.6	391	23.2	391	23.3	386	23.6	391	23.2	391	23.3
435.gromacs	477	15.0	477	15.0	477	15.0	469	15.2	468	15.2	469	15.2
436.cactusADM	95.0	126	93.2	128	94.2	127	88.0	136	87.4	137	86.8	138
437.leslie3d	412	22.8	415	22.6	457	20.6	412	22.8	415	22.6	457	20.6
444.namd	631	12.7	630	12.7	630	12.7	636	12.6	637	12.6	636	12.6
447.dealII	464	24.7	464	24.7	464	24.7	457	25.1	457	25.1	457	25.0
450.soplex	451	18.5	451	18.5	450	18.5	445	18.8	444	18.8	445	18.7
453.povray	277	19.2	277	19.2	280	19.0	218	24.4	217	24.6	219	24.3
454.calculix	438	18.8	440	18.7	439	18.8	428	19.3	427	19.3	428	19.3
459.GemsFDTD	361	29.4	331	32.1	361	29.4	235	45.2	235	45.2	234	45.3
465.tonto	575	17.1	574	17.1	575	17.1	472	20.8	472	20.8	473	20.8
470.lbm	373	36.8	371	37.0	372	37.0	367	37.5	366	37.6	366	37.5
481.wrf	394	28.3	395	28.3	395	28.3	381	29.3	380	29.4	379	29.5
482.sphinx3	838	23.3	836	23.3	838	23.3	838	23.3	836	23.3	838	23.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 800 MHz.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6 and the PRIMERGY RX300 S6 are electronically equivalent.

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General Notes (Continued)

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias`

470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32`

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Peak Optimization Flags (Continued)

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.00.xml>

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