



SPEC® CFP2006 Result

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Fujitsu

SPECfp®2006 = 44.8

PRIMERGY TX300 S6, Intel Xeon X5660, 2.80 GHz

SPECfp_base2006 = 41.6

CPU2006 license: 19

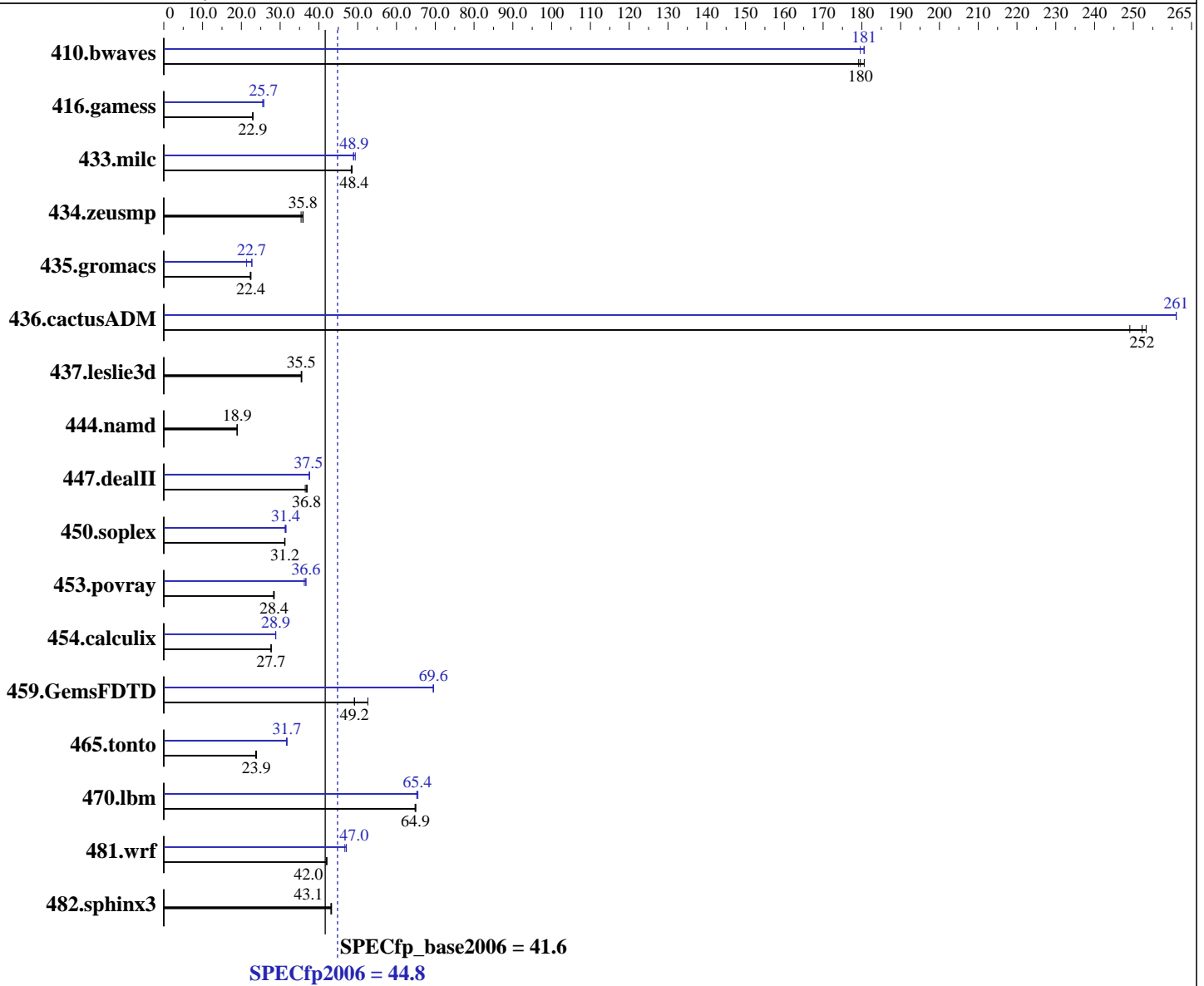
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5660
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: I_cproc_p_11.1.064, I_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SATA, 160 GB, 5.4 krpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>75.7</u>	<u>180</u>	75.8	179	75.2	181	75.7	180	75.2	181	<u>75.3</u>	<u>181</u>
416.gamess	855	22.9	<u>854</u>	<u>22.9</u>	850	23.0	760	25.8	<u>761</u>	<u>25.7</u>	768	25.5
433.milc	189	48.6	<u>190</u>	<u>48.4</u>	190	48.3	186	49.4	188	48.9	<u>188</u>	<u>48.9</u>
434.zeusmp	<u>254</u>	<u>35.8</u>	257	35.4	253	35.9	<u>254</u>	<u>35.8</u>	257	35.4	253	35.9
435.gromacs	320	22.3	318	22.5	<u>319</u>	<u>22.4</u>	334	21.4	314	22.7	<u>315</u>	<u>22.7</u>
436.cactusADM	48.0	249	47.2	253	<u>47.4</u>	<u>252</u>	45.8	261	45.8	261	<u>45.8</u>	<u>261</u>
437.leslie3d	264	35.6	<u>265</u>	<u>35.5</u>	265	35.4	264	35.6	<u>265</u>	<u>35.5</u>	265	35.4
444.namd	<u>424</u>	<u>18.9</u>	424	18.9	424	18.9	<u>424</u>	<u>18.9</u>	424	18.9	424	18.9
447.dealII	309	37.0	<u>311</u>	<u>36.8</u>	314	36.5	305	37.5	305	37.6	<u>305</u>	<u>37.5</u>
450.soplex	<u>267</u>	<u>31.2</u>	267	31.3	268	31.1	267	31.3	265	31.5	<u>266</u>	<u>31.4</u>
453.povray	188	28.3	187	28.5	<u>187</u>	<u>28.4</u>	145	36.6	<u>145</u>	<u>36.6</u>	147	36.2
454.calculix	298	27.7	<u>298</u>	<u>27.7</u>	299	27.6	<u>286</u>	<u>28.9</u>	286	28.8	286	28.9
459.GemsFDTD	216	49.2	<u>216</u>	<u>49.2</u>	202	52.6	153	69.5	<u>153</u>	<u>69.6</u>	153	69.6
465.tonto	415	23.7	412	23.9	<u>412</u>	<u>23.9</u>	310	31.8	<u>310</u>	<u>31.7</u>	311	31.6
470.lbm	211	65.0	212	64.8	<u>212</u>	<u>64.9</u>	211	65.2	<u>210</u>	<u>65.4</u>	210	65.5
481.wrf	266	42.1	<u>266</u>	<u>42.0</u>	266	41.9	<u>238</u>	<u>47.0</u>	239	46.7	237	47.1
482.sphinx3	452	43.1	450	43.3	<u>452</u>	<u>43.1</u>	452	43.1	450	43.3	<u>452</u>	<u>43.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable
Intel HT Technology = Disable

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

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General Notes (Continued)

This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6 and the PRIMERGY RX300 S6 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias`

470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealIII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

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Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml>

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