



# SPEC® CFP2006 Result

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Bull SAS

BL265  
(Intel Xeon E5530, 2.40 GHz)

**SPECfp®2006 = 32.9**

**SPECfp\_base2006 = 31.0**

CPU2006 license: 20

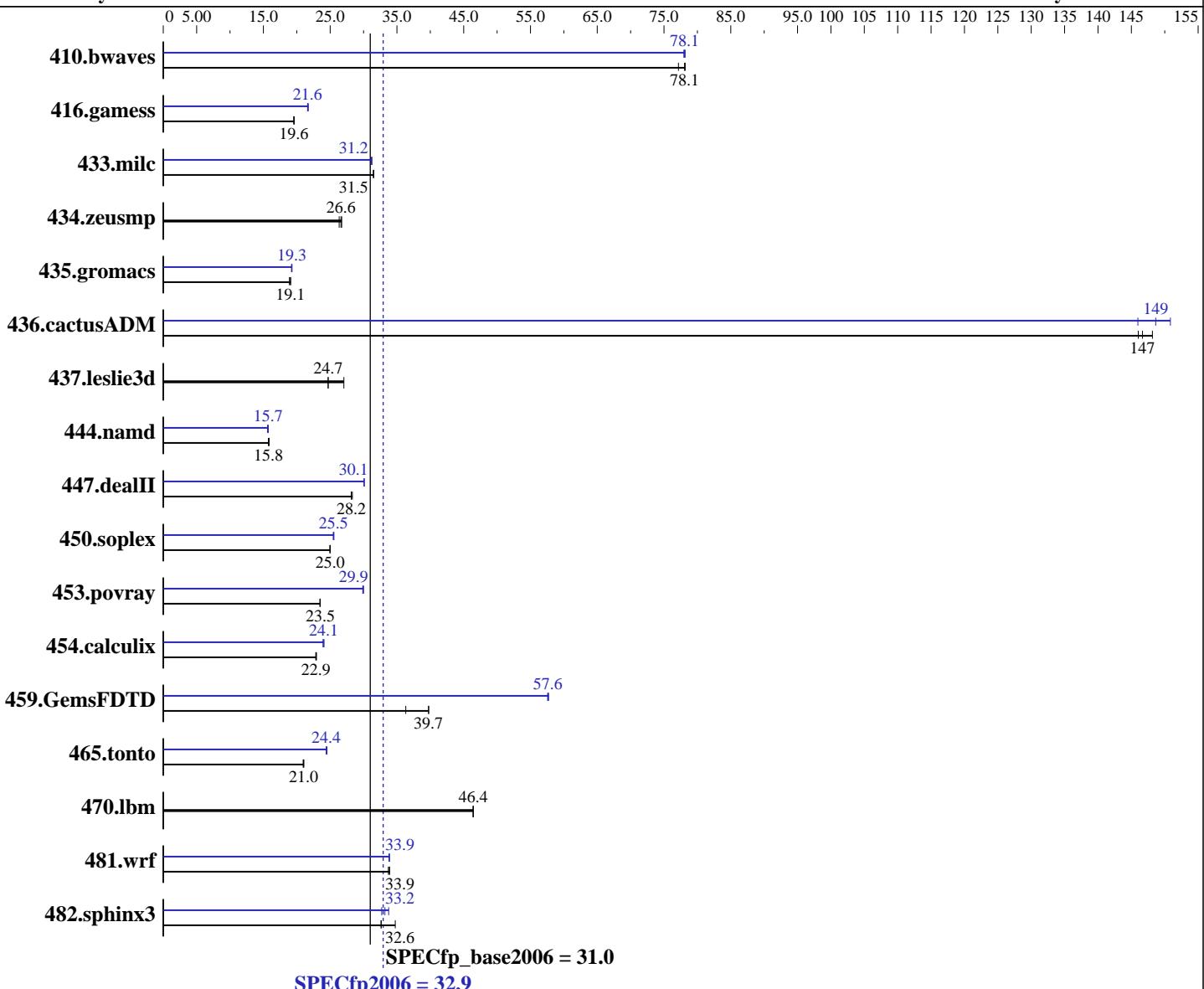
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Jan-2010

Hardware Availability: May-2009

Software Availability: Feb-2009



## Hardware

CPU Name: Intel Xeon E5530  
CPU Characteristics: Intel Turbo Boost Technology up to 2.66 GHz  
CPU MHz: 2400  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp  
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080, l\_cprof\_p\_11.0.080  
Auto Parallel: Yes  
File System: ReiserFS  
System State: Run level 3 (multi-user)

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L3 Cache:	8 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	24 GB (6 x 4 GB PC3-10600R, 2 Rank, CL9-9-9, ECC, running at 1066 MHz)	Other Software:	Binutils 2.18.50.0.7.20080502
Disk Subsystem:	1 x 73 GB SAS, 10000 RPM		
Other Hardware:	None		

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	176	77.2	174	78.2	<b><u>174</u></b>	<b><u>78.1</u></b>	174	78.0	<b><u>174</u></b>	<b><u>78.1</u></b>	174	78.2
416.gamess	<b><u>1001</u></b>	<b><u>19.6</u></b>	999	19.6	1001	19.6	906	21.6	<b><u>905</u></b>	<b><u>21.6</u></b>	901	21.7
433.milc	291	31.5	<b><u>292</u></b>	<b><u>31.5</u></b>	292	31.5	294	31.2	<b><u>294</u></b>	<b><u>31.2</u></b>	294	31.2
434.zeusmp	<b><u>342</u></b>	<b><u>26.6</u></b>	345	26.3	341	26.7	<b><u>342</u></b>	<b><u>26.6</u></b>	345	26.3	341	26.7
435.gromacs	378	18.9	<b><u>375</u></b>	<b><u>19.1</u></b>	374	19.1	<b><u>371</u></b>	<b><u>19.3</u></b>	370	19.3	372	19.2
436.cactusADM	81.8	146	<b><u>81.5</u></b>	<b><u>147</u></b>	80.7	148	<b><u>79.2</u></b>	151	81.9	146	<b><u>80.4</u></b>	<b><u>149</u></b>
437.leslie3d	347	27.1	<b><u>381</u></b>	<b><u>24.7</u></b>	381	24.7	347	27.1	<b><u>381</u></b>	<b><u>24.7</u></b>	381	24.7
444.namd	<b><u>509</u></b>	<b><u>15.8</u></b>	506	15.9	509	15.8	<b><u>513</u></b>	<b><u>15.6</u></b>	510	15.7	<b><u>510</u></b>	<b><u>15.7</u></b>
447.dealII	406	28.2	404	28.3	<b><u>406</u></b>	<b><u>28.2</u></b>	380	30.1	<b><u>380</u></b>	<b><u>30.1</u></b>	380	30.1
450.soplex	334	25.0	<b><u>334</u></b>	<b><u>25.0</u></b>	333	25.0	<b><u>327</u></b>	<b><u>25.5</u></b>	326	25.6	328	25.5
453.povray	<b><u>226</u></b>	<b><u>23.5</u></b>	227	23.5	226	23.5	<b><u>178</u></b>	29.9	<b><u>177</u></b>	30.0	<b><u>178</u></b>	<b><u>29.9</u></b>
454.calculix	361	22.9	359	22.9	<b><u>361</u></b>	<b><u>22.9</u></b>	343	24.1	<b><u>343</u></b>	<b><u>24.1</u></b>	345	23.9
459.GemsFDTD	<b><u>267</u></b>	<b><u>39.7</u></b>	267	39.8	292	36.3	184	57.6	<b><u>184</u></b>	<b><u>57.6</u></b>	184	57.7
465.tonto	470	20.9	<b><u>468</u></b>	<b><u>21.0</u></b>	467	21.1	401	24.5	<b><u>403</u></b>	<b><u>24.4</u></b>	403	24.4
470.lbm	296	46.4	<b><u>296</u></b>	<b><u>46.4</u></b>	296	46.4	<b><u>296</u></b>	<b><u>46.4</u></b>	<b><u>296</u></b>	<b><u>46.4</u></b>	296	46.4
481.wrf	331	33.7	330	33.9	<b><u>330</u></b>	<b><u>33.9</u></b>	330	33.8	<b><u>330</u></b>	<b><u>33.9</u></b>	330	33.9
482.sphinx3	561	34.7	597	32.6	<b><u>597</u></b>	<b><u>32.6</u></b>	596	32.7	<b><u>587</u></b>	<b><u>33.2</u></b>	577	33.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
KMP\_STACKSIZE set to 200M



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**Hardware Availability:** May-2009

**Software Availability:** Feb-2009

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll12 -ansi-alias -scalar-rep -opt-prefetch
```

```
450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -opt-malloc-options=3
```

```
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -unroll14 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
            -parallel
```

```
416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll12 -Ob0 -ansi-alias -scalar-rep
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll12 -Ob0 -opt-prefetch -parallel
```

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll14 -auto
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -opt-prefetch -auto-ilp32
```

```
436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                 -unroll12 -opt-prefetch -parallel -auto-ilp32
```

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## Peak Optimization Flags (Continued)

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20100202.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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